



## UC3844/45

## LINEAR INTEGRATED CIRCUIT

### HIGH PERFORMANCE CURRENT MODE PWM CONTROLLERS

#### ■ DESCRIPTION

The UTC **UC3844/3845** are high performance fixed frequency current mode controllers that specifically designed for Off-Line and DC to DC converter applications with minimal external parts count.

The differences between **UC3844** and **UC3845** are the maximum duty cycle ranges and under-voltage lockout thresholds. The **UC3844** ideally suited to off-line applications with UVLO thresholds of  $16V_{(ON)}$  and  $10V_{(OFF)}$ , and **UC3845** has UVLO thresholds of  $8.5V_{(ON)}$  and  $7.6V_{(OFF)}$  for lower voltage applications.

#### ■ FEATURES

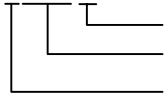
- \* Operation output switching frequency up to 500 kHz
- \* Output deadtime adjustable from 50% to 70%
- \* Automatic feed forward compensation
- \* Latching PWM for cycle-by-cycle current limiting
- \* High current totem pole output
- \* Internally trimmed reference with under voltage lockout
- \* UVLO with hysteresis
- \* Low startup and operating current

[www.DataSheet4U.com](http://www.DataSheet4U.com)

#### ■ ORDERING INFORMATION

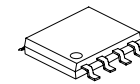
Order Number		Package	Packing
Normal	Lead Free Plating		
UC3844-D08-T	UC3844L-D08-T	DIP-8	Tube
UC3844-S08-R	UC3844L-S08-R	SOP-8	Tape Reel
UC3844-S08-T	UC3844L-S08-T	SOP-8	Tube
UC3845-D08-T	UC3845L-D08-T	DIP-8	Tube
UC3845-S08-R	UC3845L-S08-R	SOP-8	Tape Reel
UC3845-S08-T	UC3845L-S08-T	SOP-8	Tube

UC3844L-D08-R

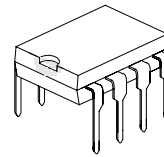


- (1)Packing Type
- (2)Package Type
- (3)Lead Plating

- (1) R: Tape Reel, T: Tube
- (2) D08: DIP-8, S08: SOP-8
- (3) L: Lead Free Plating, Blank: Pb/Sn



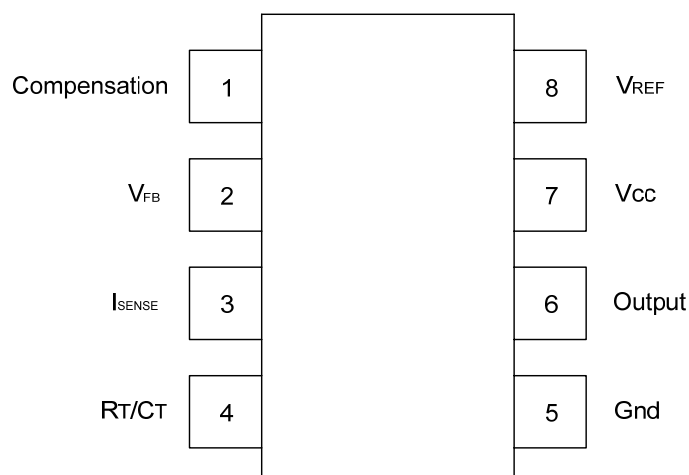
SOP-8



DIP-8

\*Pb-free plating product number:  
UC3844L/UC3845L

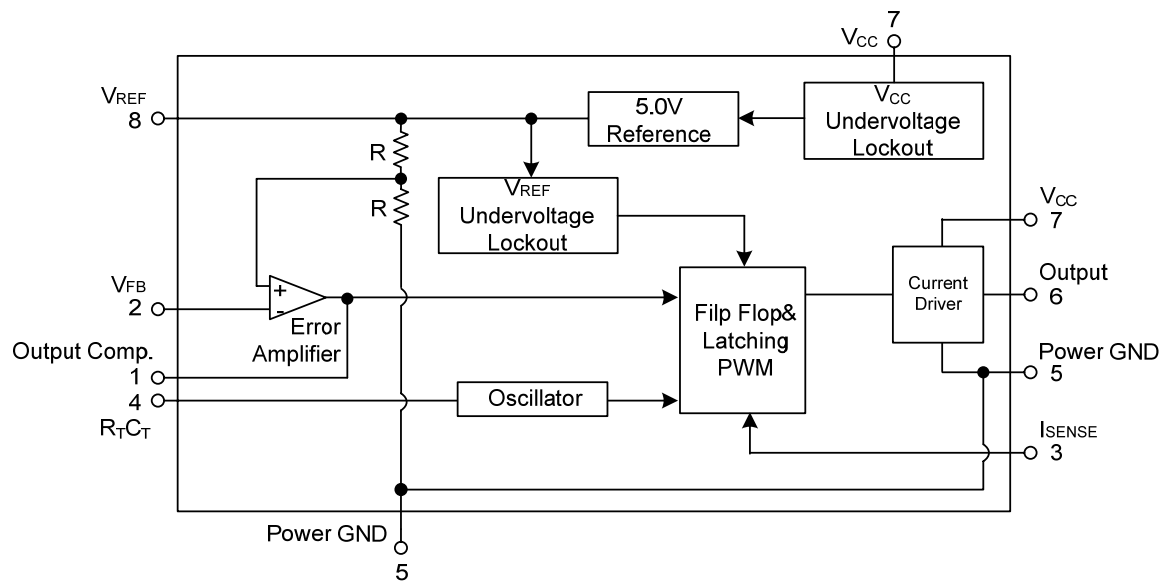
## ■ PIN CONFIGURATION



## ■ PIN DESCRIPTION

PIN NO	PIN NAME	FUNCTION
1	Compensation	Error amplifier output, this pin is made available for loop compensation.
2	V <sub>FB</sub>	Voltage Feedback, the inverting input of the Error Amplifier. It is normally connected to the switching power supply output through a resistor divider.
3	I <sub>SENSE</sub>	A voltage proportional to inductor current is connected to this input. The PWM uses this information to terminate the output switch conduction.
4	R <sub>T</sub> /C <sub>T</sub>	The Oscillator frequency and maximum output duty cycle are programmed by connecting resistor R <sub>T</sub> to V <sub>ref</sub> and capacitor C <sub>T</sub> to ground. Operation to 1 MHz is possible.
5	GND	Power ground.
6	Output	This output directly drives the gate of a power MOSFET. Peak currents up to 1A are sourced and sunk by this pin. The output switches at one-half the oscillator frequency.
7	V <sub>CC</sub>	Positive supply.
8	V <sub>REF</sub>	Reference output, provides charging current for capacitor C <sub>T</sub> though resistor R <sub>T</sub> .

## ■ BLOCK DIAGRAM



### ■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER		SYMBOL	RATINGS	UNIT
Total Power Supply and Zener Current		(I <sub>CC</sub> +I <sub>Z</sub> )	30	mA
Output Current, Source or Sink (Note 2)		I <sub>OUT</sub>	1.0	A
Output Energy (Capacitive Load per cycle)		W	5.0	μJ
Current Sense and Voltage feedback Inputs		V <sub>IN</sub>	-0.3 ~ +5.5	V
Error Amp Output Sink Current		I <sub>SINK</sub>	10	mA
Power Dissipation	SOP-8	P <sub>D</sub>	862	mW
	DIP-8		1250	mW
Junction Temperature		T <sub>J</sub>	+150	°C
Operation Temperature Range		T <sub>OPR</sub>	0 ~ 70	°C
Storage Temperature Range		T <sub>STG</sub>	-65 ~ +150	°C

Note:1.Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2.Maximum package power dissipation limits must be observed.

### ■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Thermal Resistance(Junction-to-Air)	SOP-8	θ <sub>JA</sub>	145	°C/W
	DIP-8		100	°C/W

### ■ ELECTRICAL CHARACTERISTICS

(Ta=25°C, V<sub>CC</sub>=15V, R<sub>T</sub>=10k, C<sub>T</sub>=3.3nF, 0°C ≤ T<sub>A</sub> ≤ 70°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>REFERENCE SECTION</b>						
Reference Output Voltage	V <sub>REF</sub>	I <sub>OUT</sub> =1.0mA, T <sub>J</sub> =25°C	4.9	5.0	5.1	V
Line Regulation	ΔV <sub>OUT</sub>	V <sub>CC</sub> =12V ~ 25V		2.0	20	mV
Load Regulation	ΔV <sub>OUT</sub>	I <sub>OUT</sub> =1.0mA ~ 20mA		3.0	25	mV
Temperature Stability	t <sub>s</sub>			0.2		mV/°C
Total Output Variation over Line, Load, Temperature	V <sub>REF</sub>		4.82		5.18	V
Output Noise Voltage	e <sub>N</sub>	f=10Hz ~ kHz, T <sub>J</sub> =25°C		50		μV
Long Term Stability	S	T <sub>A</sub> =125°C for 1000 Hours		5		mV
Output Short Circuit Current	I <sub>SC</sub>		-30	-85	-180	mA
<b>OSCILLATOR SECTION</b>						
Oscillator Voltage Swing	V <sub>OSC</sub>			1.6		V
Discharge Current	I <sub>DSG</sub>	V <sub>OSC</sub> =2.0V, T <sub>J</sub> =25°C		10.8		mA
Frequency	f <sub>OSC</sub>	T <sub>J</sub> =25°C,	47	52	57	kHz
		0°C ≤ T <sub>A</sub> ≤ 70°C	46		60	
Frequency Change with Voltage	Δf <sub>OSC</sub> /ΔV	V <sub>CC</sub> =12V ~ 25V		0.2	1.0	%
Frequency Change with Temperature	Δf <sub>OSC</sub> /ΔT	0°C ≤ T <sub>A</sub> ≤ 70°C		5.0		%
<b>ERROR AMPLIFIER SECTION</b>						
Voltage Feedback Input	V <sub>FB</sub>	V <sub>OUT</sub> =2.5V	2.42	2.50	2.58	V
Output Voltage Swing	High	V <sub>OH</sub>	5.0	6.2		V
	Low	V <sub>OL</sub>		0.8	1.1	
Output Current	Sink	I <sub>SINK</sub>	2.0	12		mA
	Source	I <sub>SOURCE</sub>	-0.5	-1.0		
Input Bias Current	I <sub>I(BIAS)</sub>	V <sub>FB</sub> =2.7V		-0.1	-2.0	μA
Open Loop Voltage Gain	G <sub>VO</sub>	V <sub>OUT</sub> =2.0V ~ 4.0V	65	90		dB
Power Supply Rejection Ratio	PSRR	V <sub>CC</sub> =12V ~ 25V	60	70		dB
Unity Gain Bandwidth	GB <sub>W</sub>	T <sub>J</sub> =25°C	0.7	1.0		MHz

## ■ ELECTRICAL CHARACTERISTICS(Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
CURRENT SENSE SECTION							
Current Sense Input Voltage Gain (Note 2 & 3)		G <sub>V</sub>		2.85	3.0	3.15	V/V
Maximum Current Sense Input Threshold (Note 2)		V <sub>I(THR)</sub>		0.9	1.0	1.1	V
Input Bias Current		I <sub>I(BIAS)</sub>			-2.0	-10	μA
Power Supply Rejection Ratio		PSRR	V <sub>CC</sub> =12V ~ 25V (Note4)		70		dB
Propagation Delay		t <sub>PLH(IN/OUT)</sub>			150	300	ns
OUTPUT SECTION							
Output Voltage	Low	V <sub>OL</sub>	I <sub>SINK</sub> =20mA		0.1	0.4	V
			I <sub>SINK</sub> =200mA		1.6	2.2	
	High	V <sub>OH</sub>	I <sub>SINK</sub> =20mA	13	13.5		V
			I <sub>SINK</sub> =200mA	12	13.4		
Output Voltage with U <sub>VLO</sub> Activated		V <sub>OL</sub> (U <sub>VLO</sub> )	V <sub>CC</sub> =6.0V, I <sub>SINK</sub> =1.0mA		0.1	1.1	V
Output Voltage Rise Time		t <sub>R</sub>	C <sub>L</sub> =1.0nF, T <sub>J</sub> =25°C		50	150	ns
Output Voltage Fall Time		t <sub>F</sub>	C <sub>L</sub> =1.0nF, T <sub>J</sub> =25°C		50	150	ns
UNDERVOLTAGE LOCKOUT SECTION							
Startup Threshold	UC3844	V <sub>THR</sub>		14.5	16.0	17.5	V
	UC3845			7.8	8.4	9.0	
Minimum Operating Voltage After Turn-On	UC3844	V <sub>CC(MIN)</sub>		8.5	10.0	11.5	V
	UC3845			7.0	7.6	8.2	
PWM SECTION							
Duty Cycle	Max	DC <sub>MAX</sub>		47	48	50	%
	Min	DC <sub>MIN</sub>				0	%
TOTAL DEVICE							
Power Supply Zener Voltage		V <sub>Z</sub>	I <sub>CC</sub> =25mA	30	36	-	V
Power Supply Current (Note 4)	UC3845	I <sub>CC</sub>	V <sub>CC</sub> =6.5V		0.5	1.0	mA
	UC3844		V <sub>CC</sub> =14V		12	17	

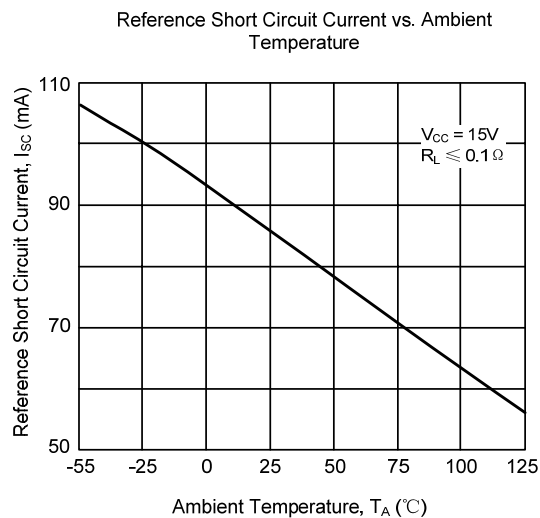
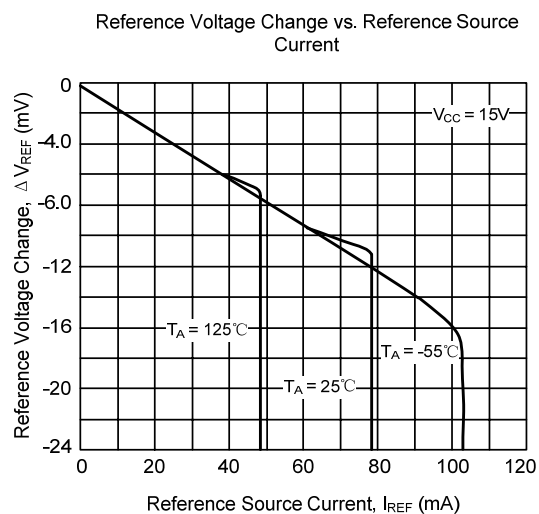
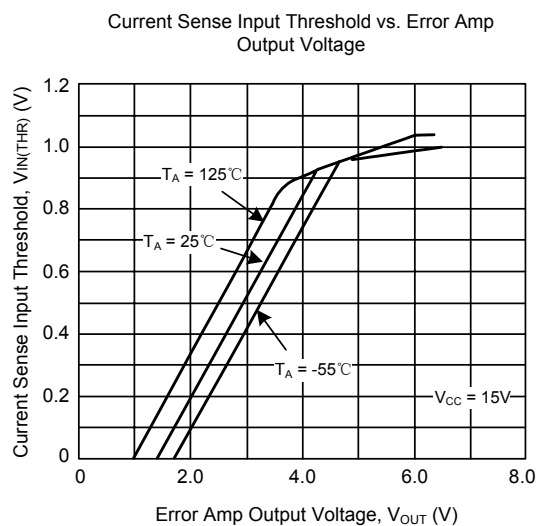
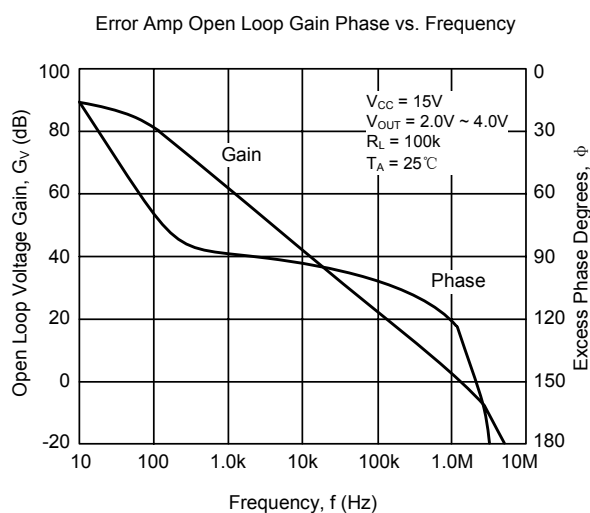
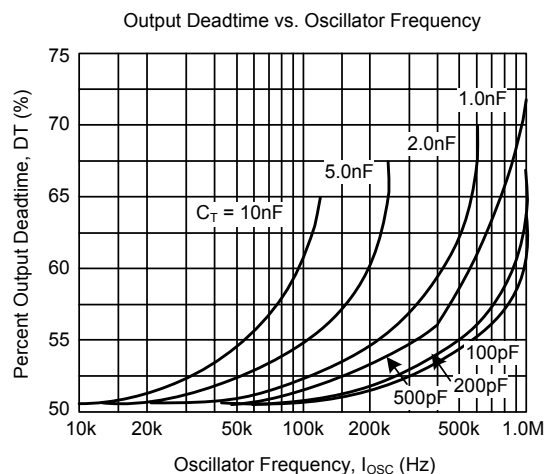
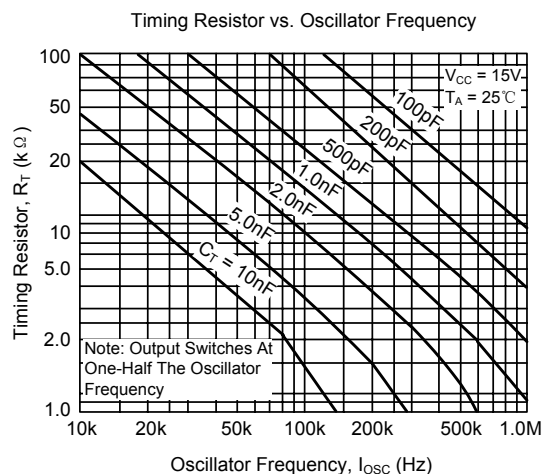
Note: 1. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient as possible.

2. This parameter is measured at the latch trip point with  $V_{FB}=0V$ .

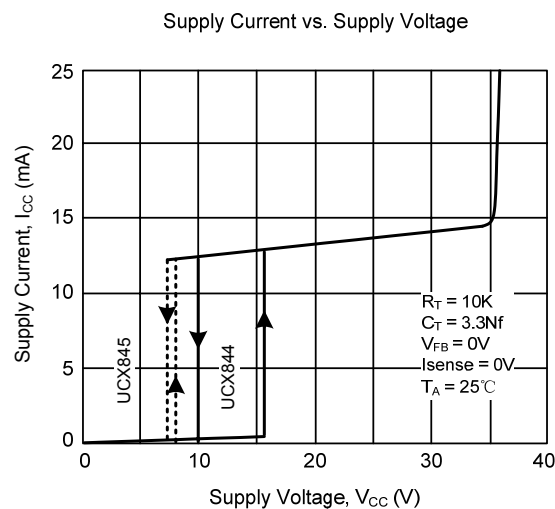
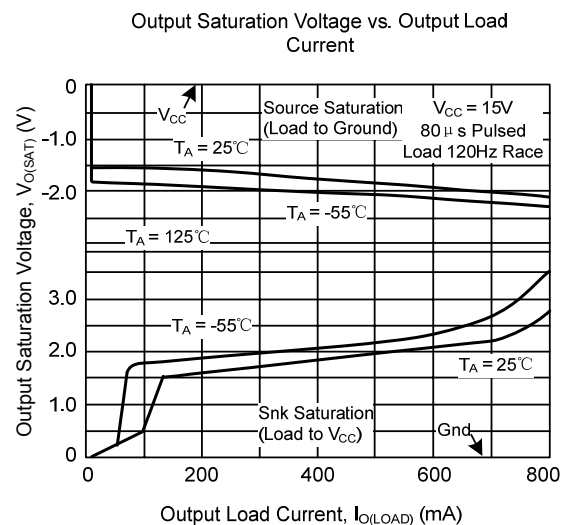
3. Comparator gain is defined as:  $A_V = \frac{\Delta V \text{ Output Compensation}}{\Delta V \text{ Current Sense Input}}$

4. Adjust  $V_{CC}$  above the startup threshold before setting to 15V.

## ■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS(Cont.)



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