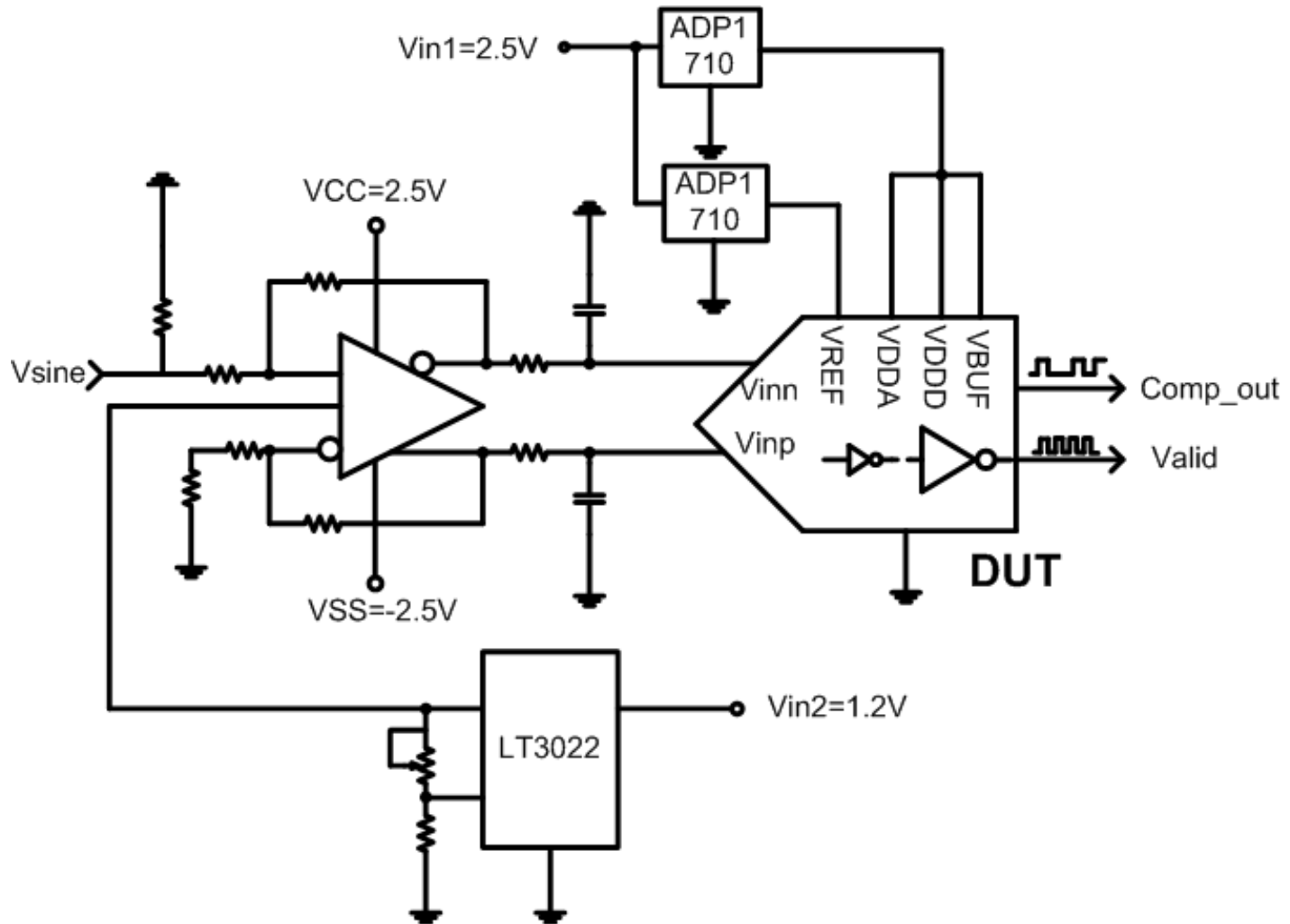
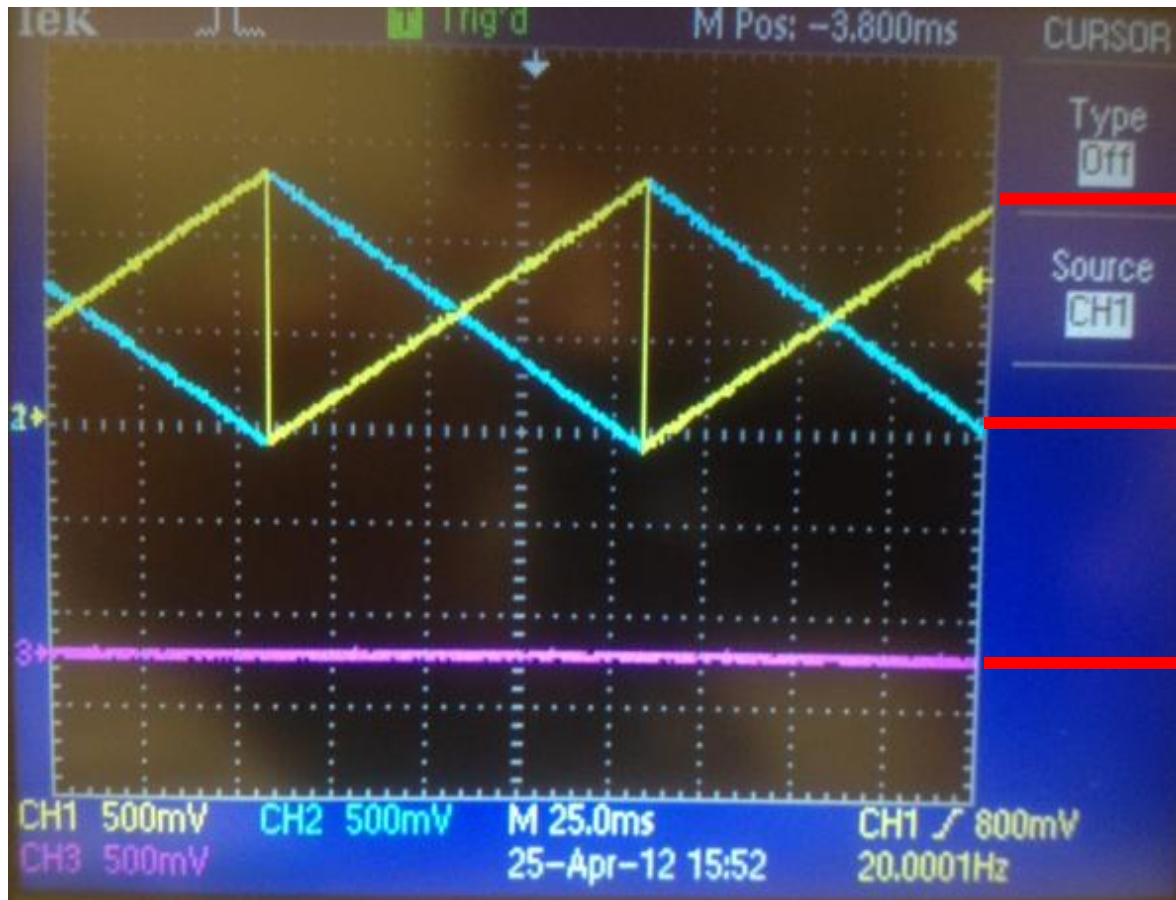


Testing config



Signals

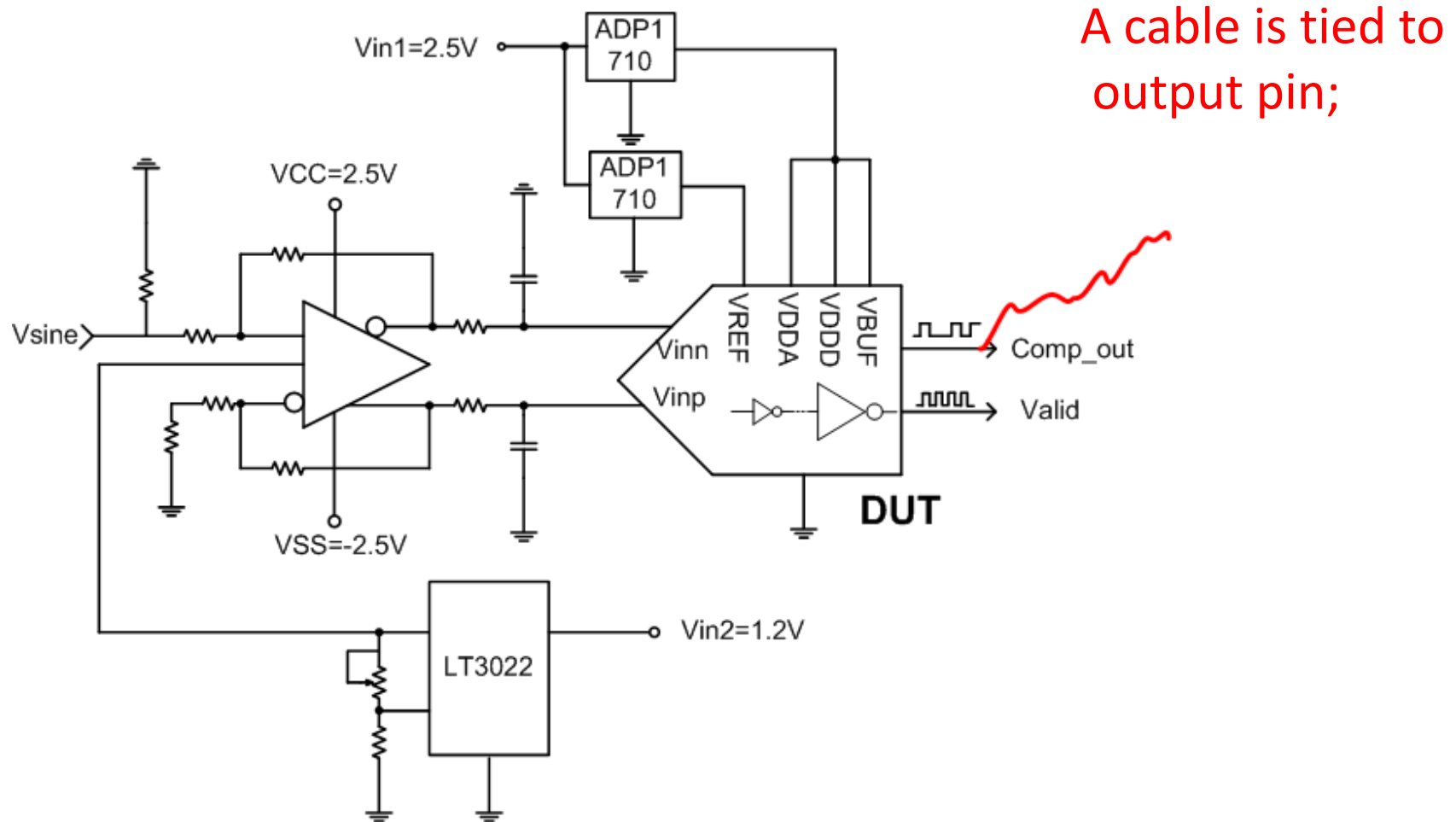


V_{inp} of DUT

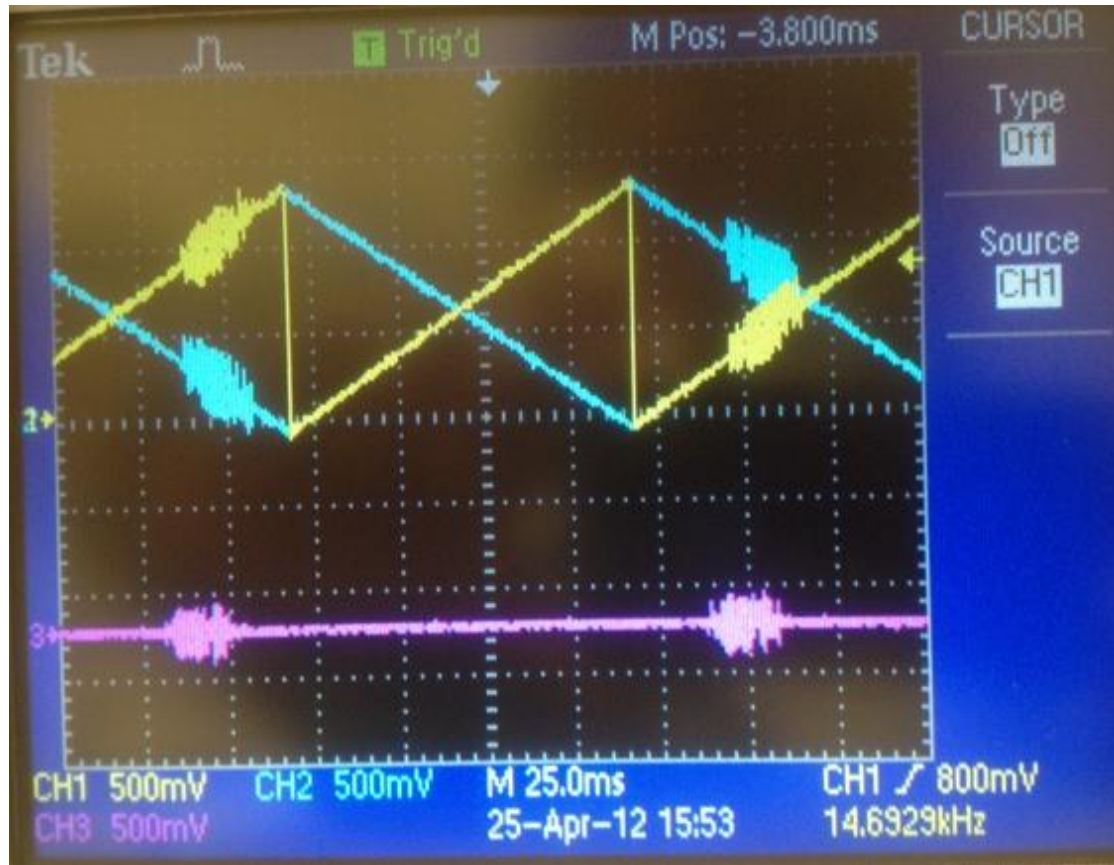
V_{inn} of DUT

GND signal

When output pin is loaded with very small load

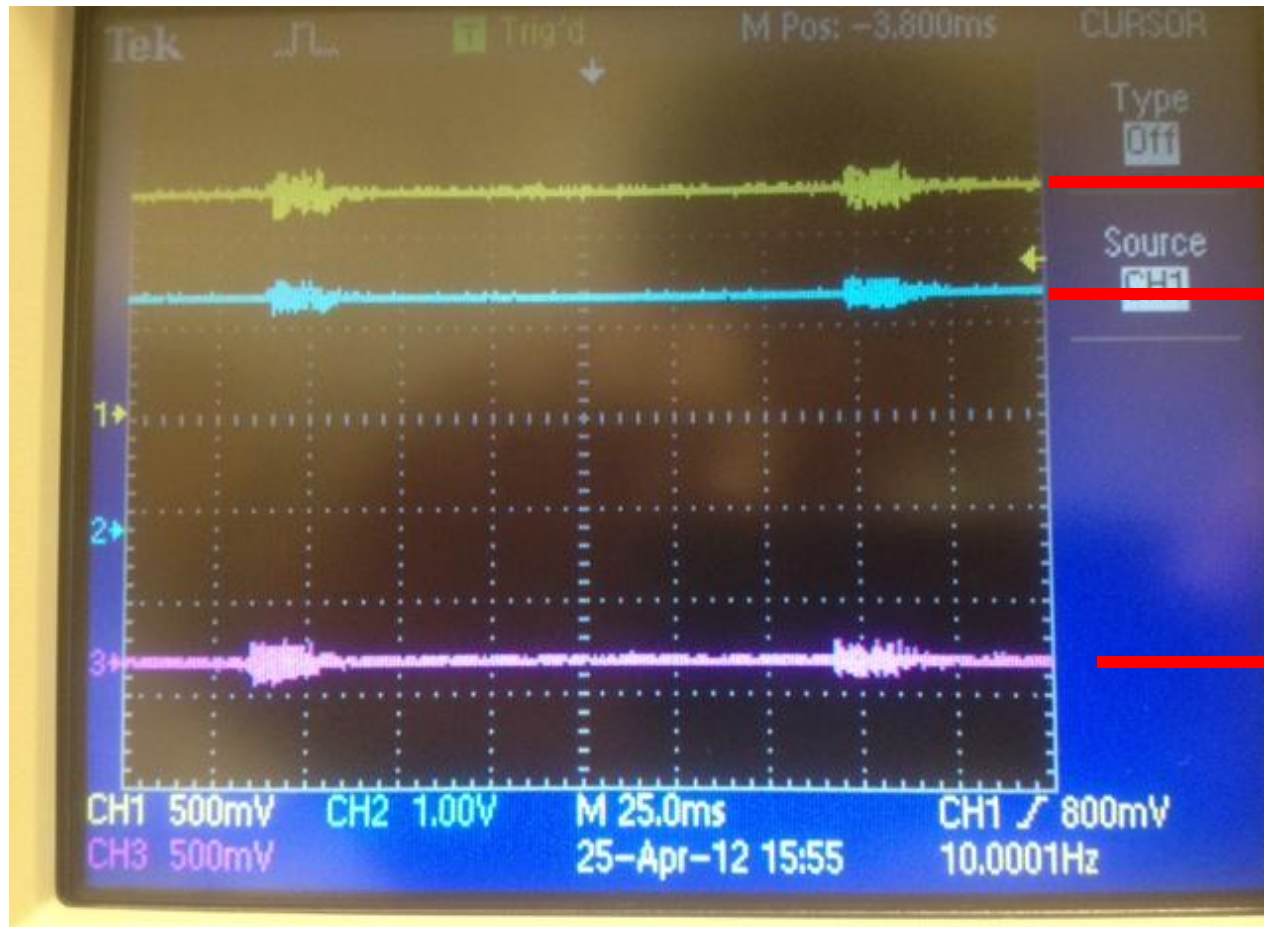


Signals are distorted



Basically the GND is distorted. Therefore, all other signals are distorted.

Other DC supplies are also distorted



VREF of DUT

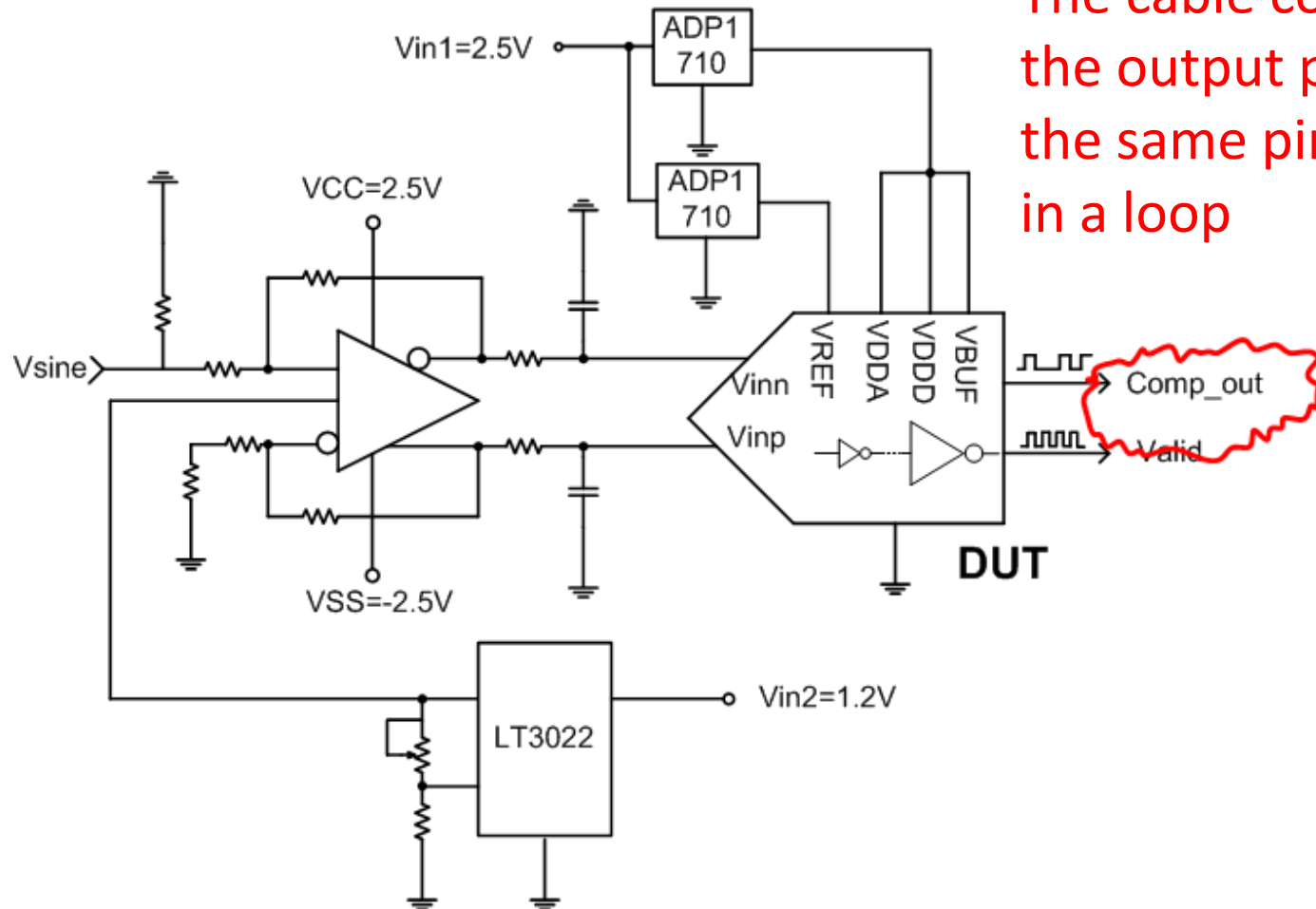
Vin1 to ADP LDO

GND signal

Problem:

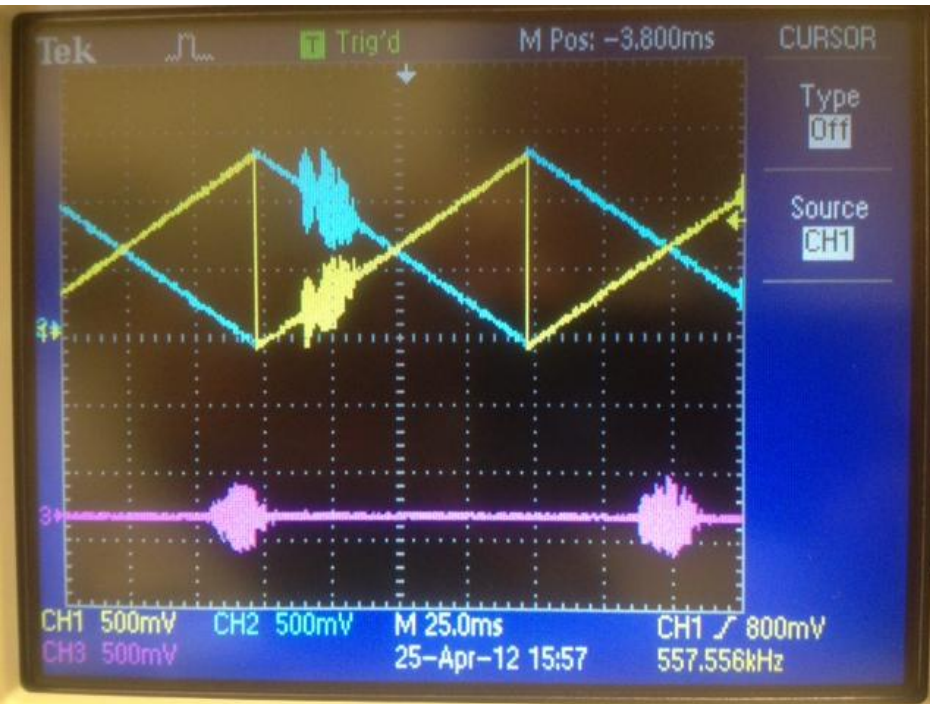
- GND signal is distorted; Therefore
 - Vin1 to ADP LDOs affected → VREF, VDDA/VDDD
 - Vin2 to LTC LDO affected → VCM to OPamp distorted
 - VCC,VSS are also distorted → VINP,VINN of DUT distorted

One interesting phenomenon

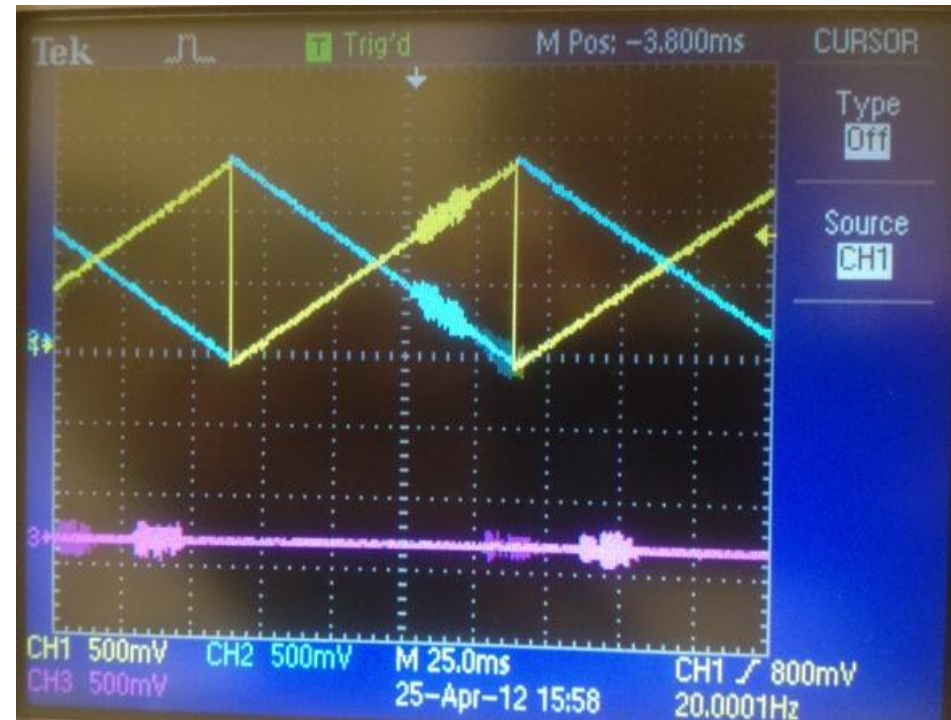


The cable connected to the output pin also tied to the same pin – connected in a loop

slight decrease in the magnitude of distortion



Only one end of cable tied to output pin



Cable connected to the output pin as a loop