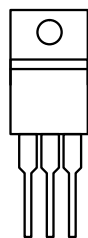


# Automotive P-Channel 30 V (D-S) 175 °C MOSFET

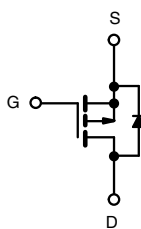
## PRODUCT SUMMARY

$V_{DS}$ (V)	- 30
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -10$ V	0.0070
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -4.5$ V	0.0110
$I_D$ (A)	- 50
Configuration	Single

**TO-220AB**


Top View

DRAIN connected to TAB



P-Channel MOSFET

## FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_g$  and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Compliant to RoHS Directive 2002/95/EC


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## ORDERING INFORMATION

Package	TO-220AB
Lead (Pb)-free and Halogen-free	SQP50P03-07-GE3

## ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	- 30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$I_D$	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	- 50	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	- 200	
Single Pulse Avalanche Current	$I_{AS}$	- 50	
Single Pulse Avalanche Energy	$E_{AS}$	125	mJ
Maximum Power Dissipation <sup>b</sup>	$P_D$	$T_C = 25$ °C	W
		$T_C = 125$ °C	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C

## THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	62	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	1	

### Notes

- Package limited.
- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0, I <sub>D</sub> = - 250 μA		- 30	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA		- 1.5	- 2.0	- 2.5	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 30 V	-	-	- 1	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 30 V, T <sub>J</sub> = 125 °C	-	-	- 50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 30 V, T <sub>J</sub> = 175 °C	-	-	- 250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 10 V	V <sub>DS</sub> ≤ - 5 V	- 80	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A	-	0.0050	0.0070	Ω
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A, T <sub>J</sub> = 125 °C	-	-	0.0102	
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A, T <sub>J</sub> = 175 °C	-	-	0.0118	
		V <sub>GS</sub> = - 4.5 V	I <sub>D</sub> = - 20 A	-	0.0089	0.0110	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 30 A		-	62	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 25 V, f = 1 MHz	-	4304	5380	pF
Output Capacitance	C <sub>oss</sub>			-	764	955	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	680	850	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = - 10 V	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 75 A	-	103.5	155	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	14.3	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	26.9	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.42	2.85	4.28	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = - 15 V, R <sub>L</sub> = 0.2 Ω I <sub>D</sub> ≅ - 75 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		-	11	17	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			-	10	15	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	63	95	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	26	39	
Source-Drain Diode Ratings and Characteristics <sup>b</sup>							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 200	A
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 45 A, V <sub>GS</sub> = 0		-	- 0.9	- 1.5	V

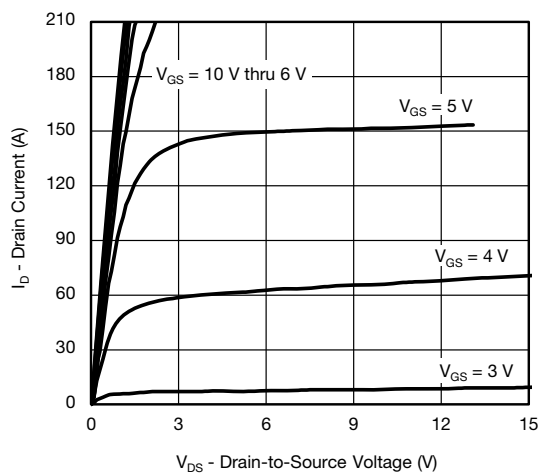
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

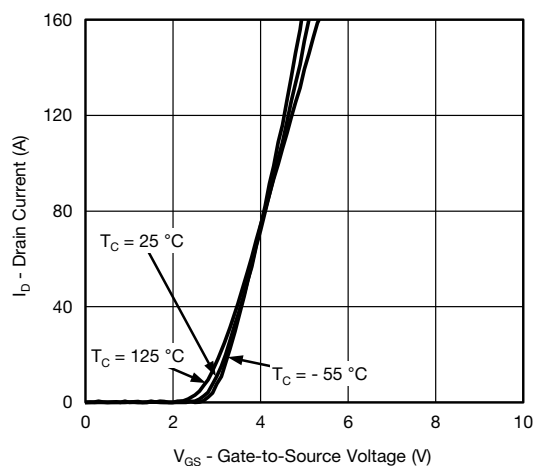
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



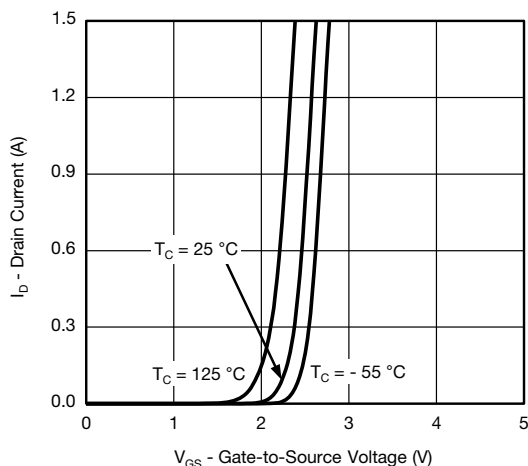
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



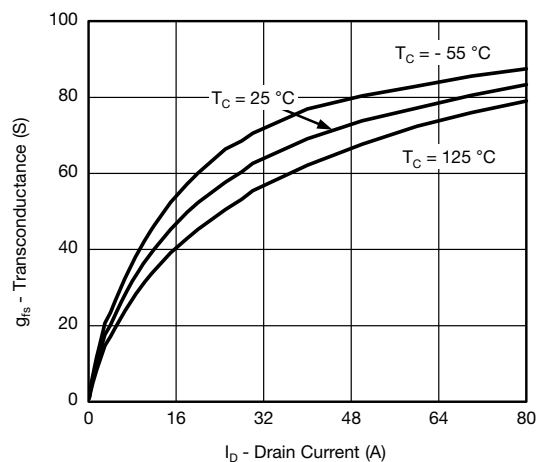
**Output Characteristics**



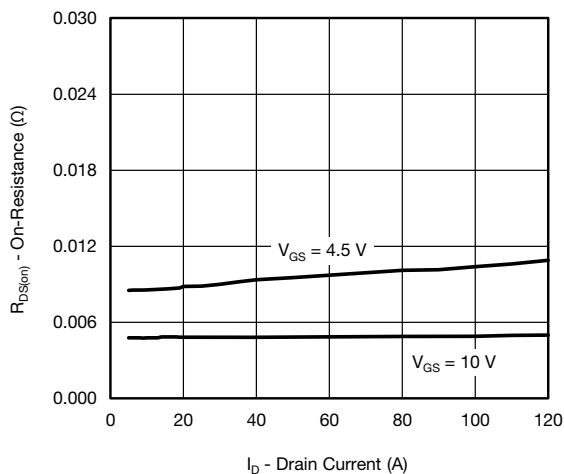
**Transfer Characteristics**



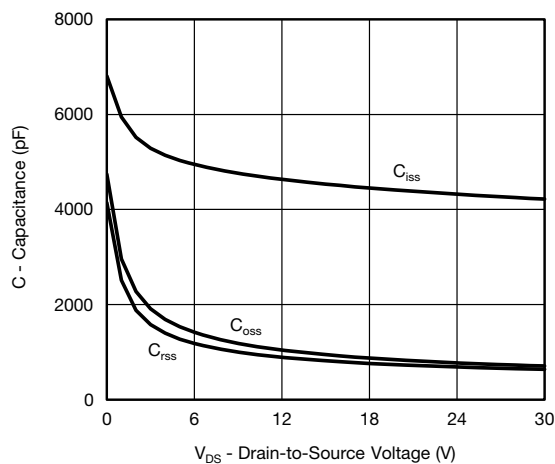
**Transfer Characteristics**



**Transconductance**



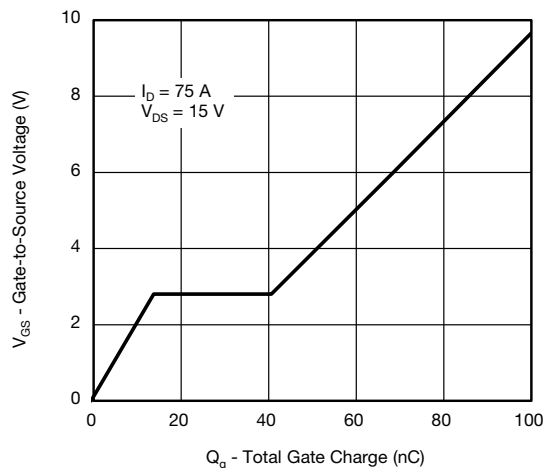
**On-Resistance vs. Drain Current**



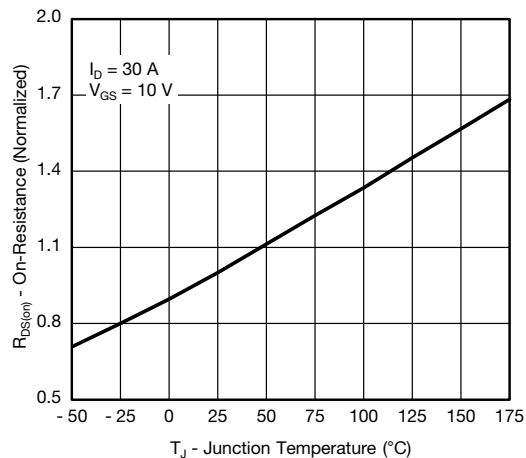
**Capacitance**



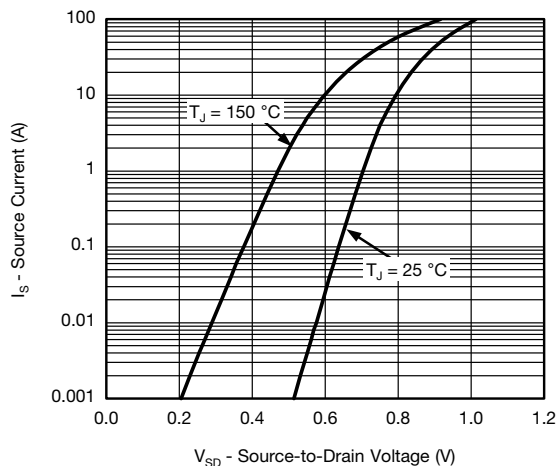
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



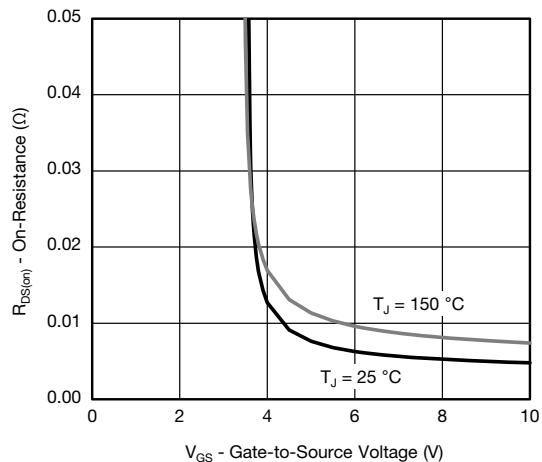
**Gate Charge**



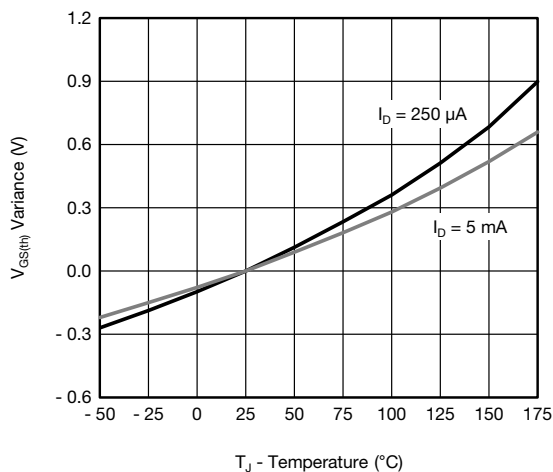
**On-Resistance vs. Junction Temperature**



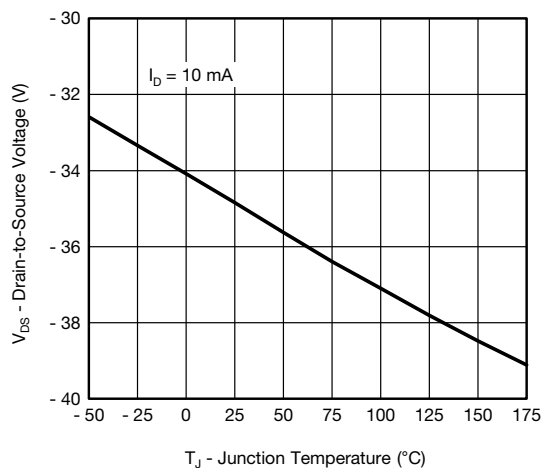
**Source Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**



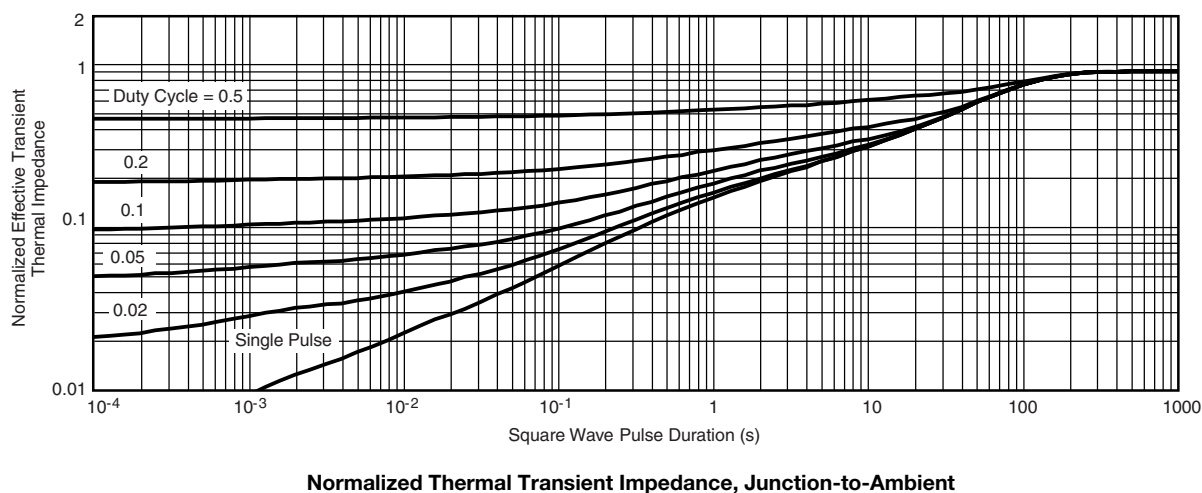
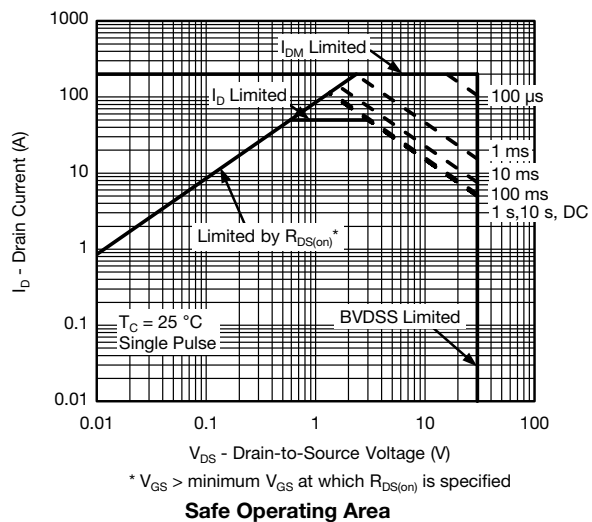
**Threshold Voltage**



**Drain Source Breakdown vs. Junction Temperature**

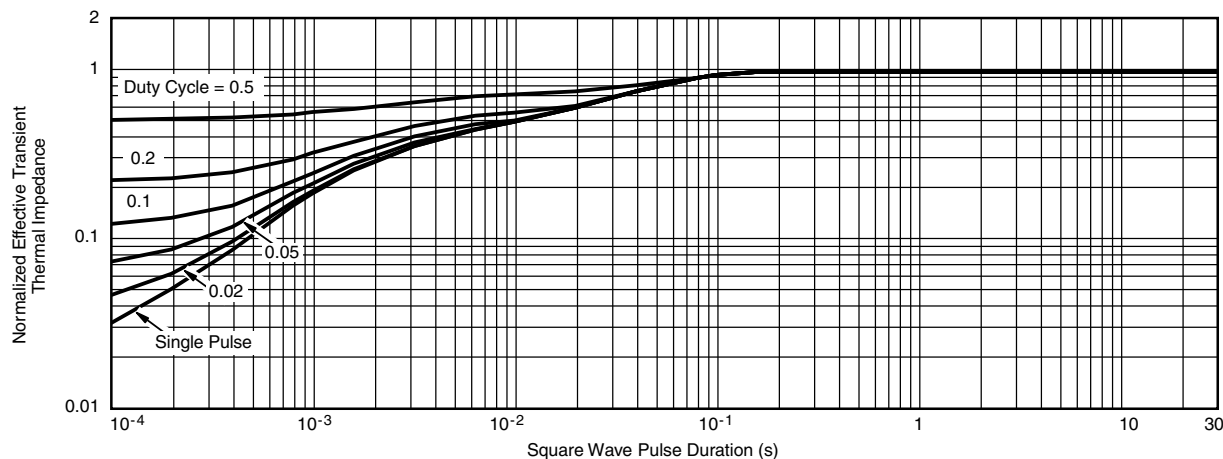


**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)





**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^{\circ}\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Case ( $25\text{ }^{\circ}\text{C}$ )are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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## TO-220AB

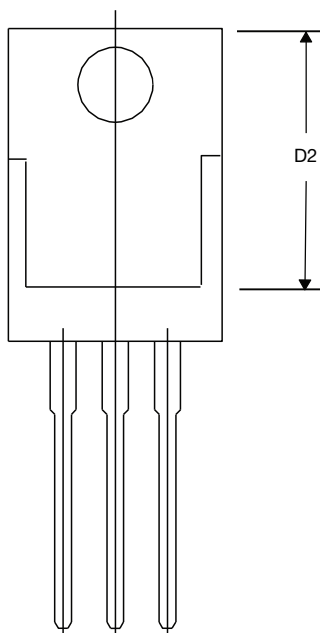


DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
c	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
E	10.04	10.51	0.395	0.414
e	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
$\varnothing P$	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

ECN: T14-0413-Rev. P, 16-Jun-14  
DWG: 5471

### Note

\*  $M = 1.32$  mm to  $1.62$  mm (dimension including protrusion)  
Heatsink hole for HVM





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