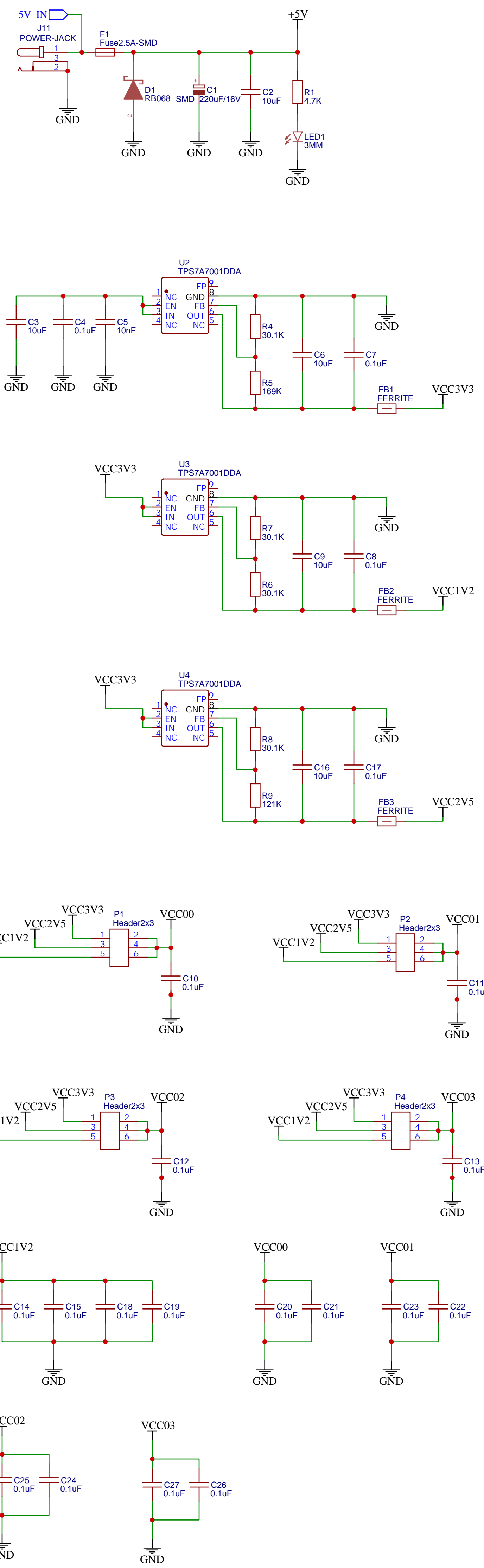


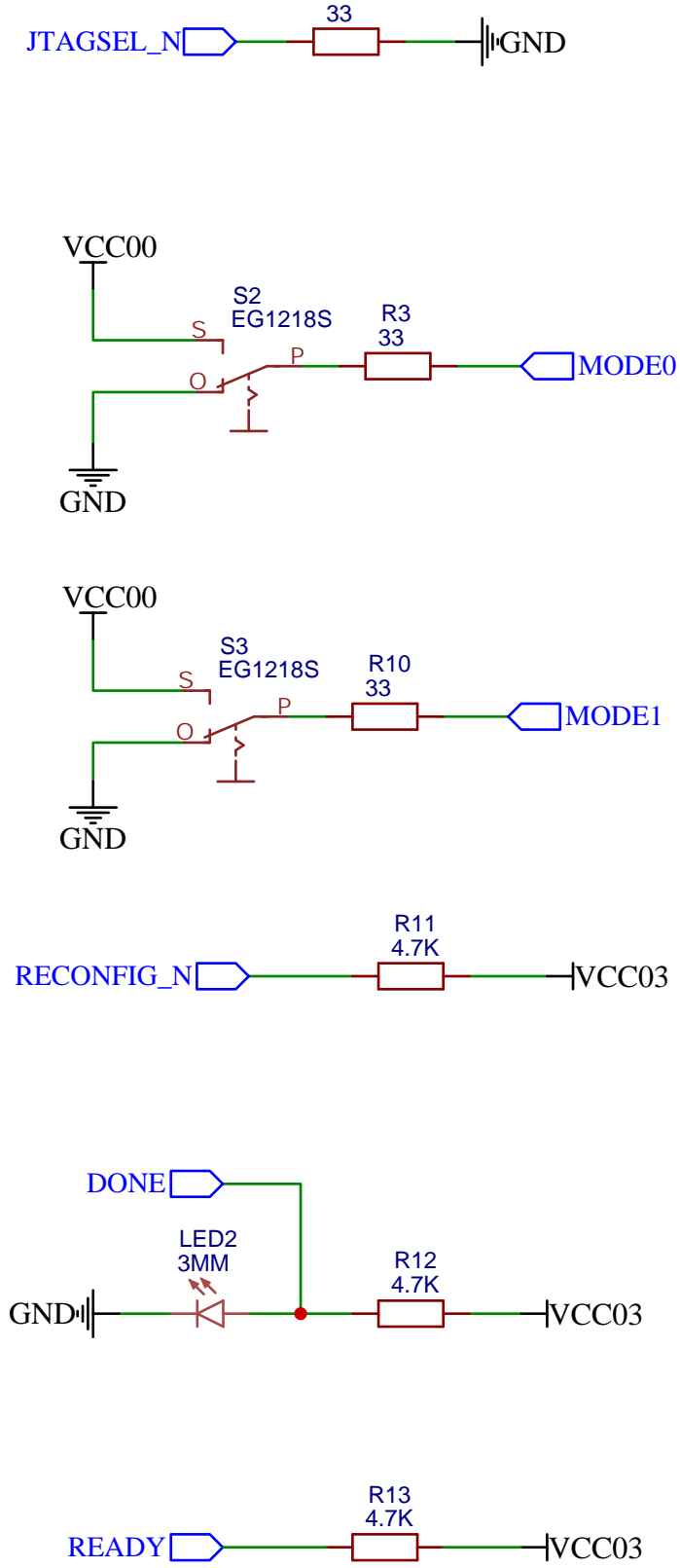
POWER ADAPTERS



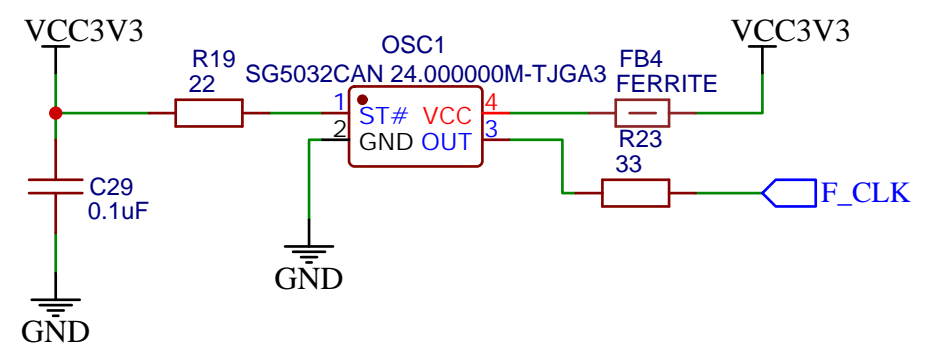
User LEDs

DIP-Switch'es

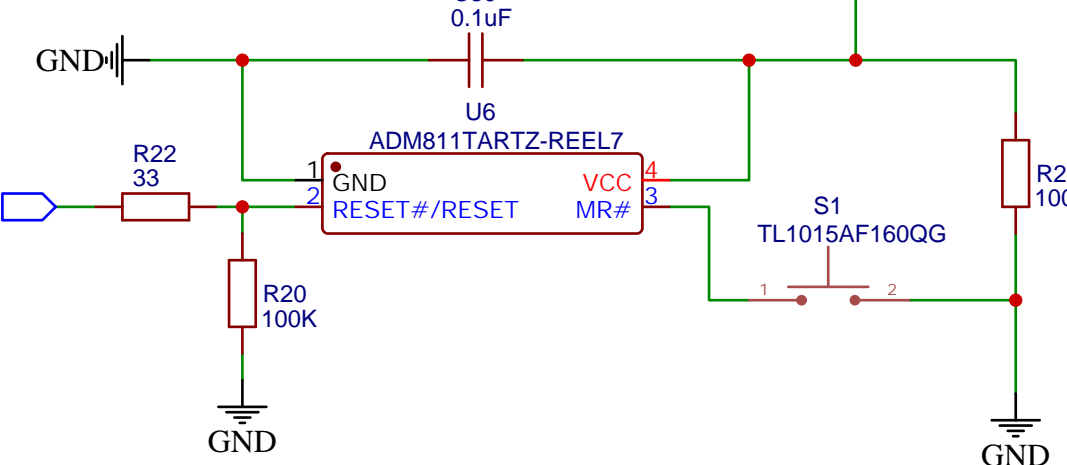
CONTROL SIGNALS FPGA



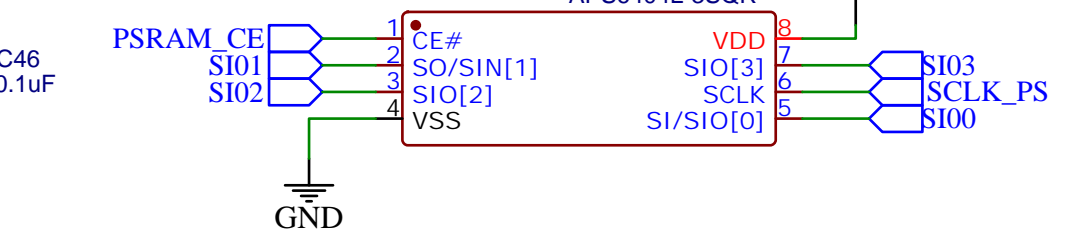
FPGA CLOCK CIRCUIT



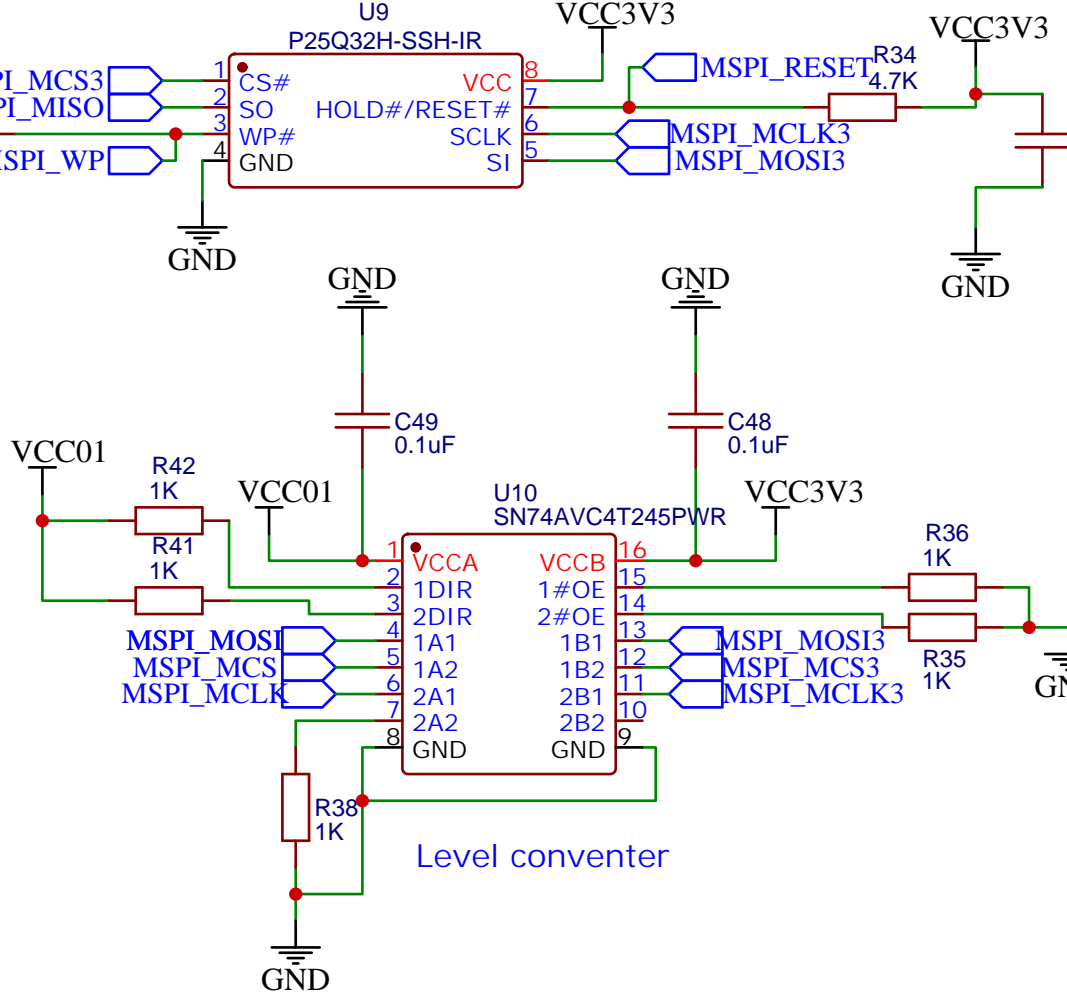
FPGA RESET CIRCUIT



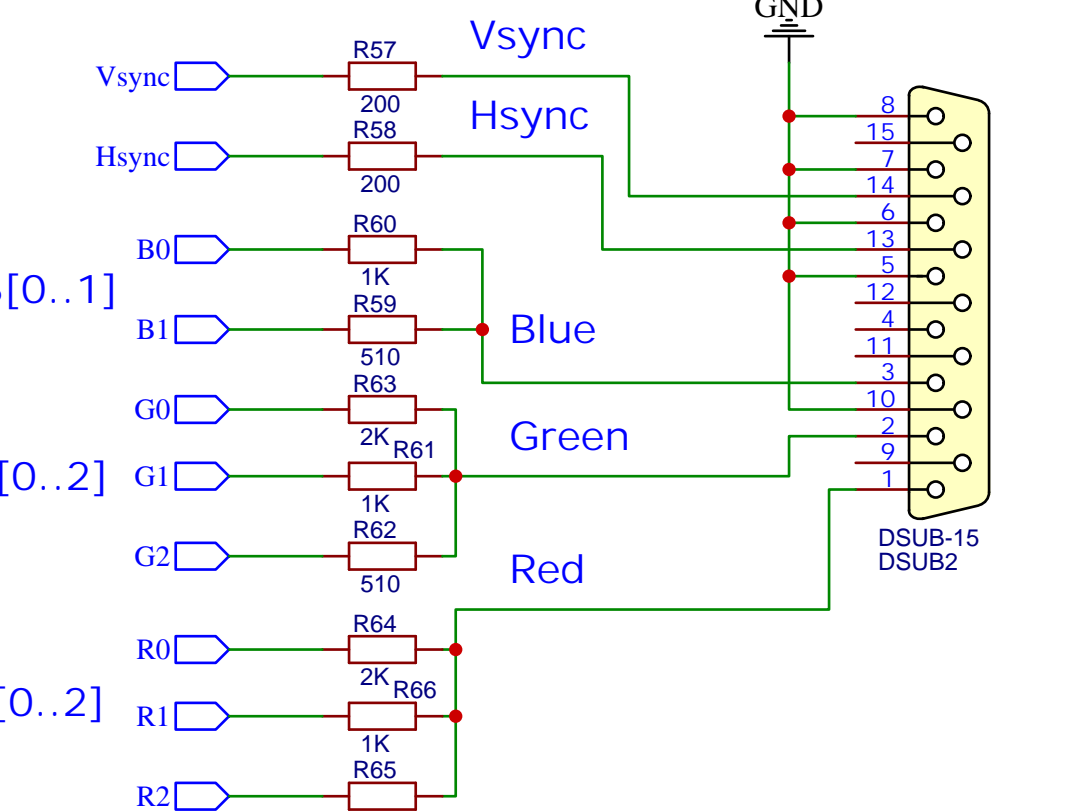
PSRAM 8MB



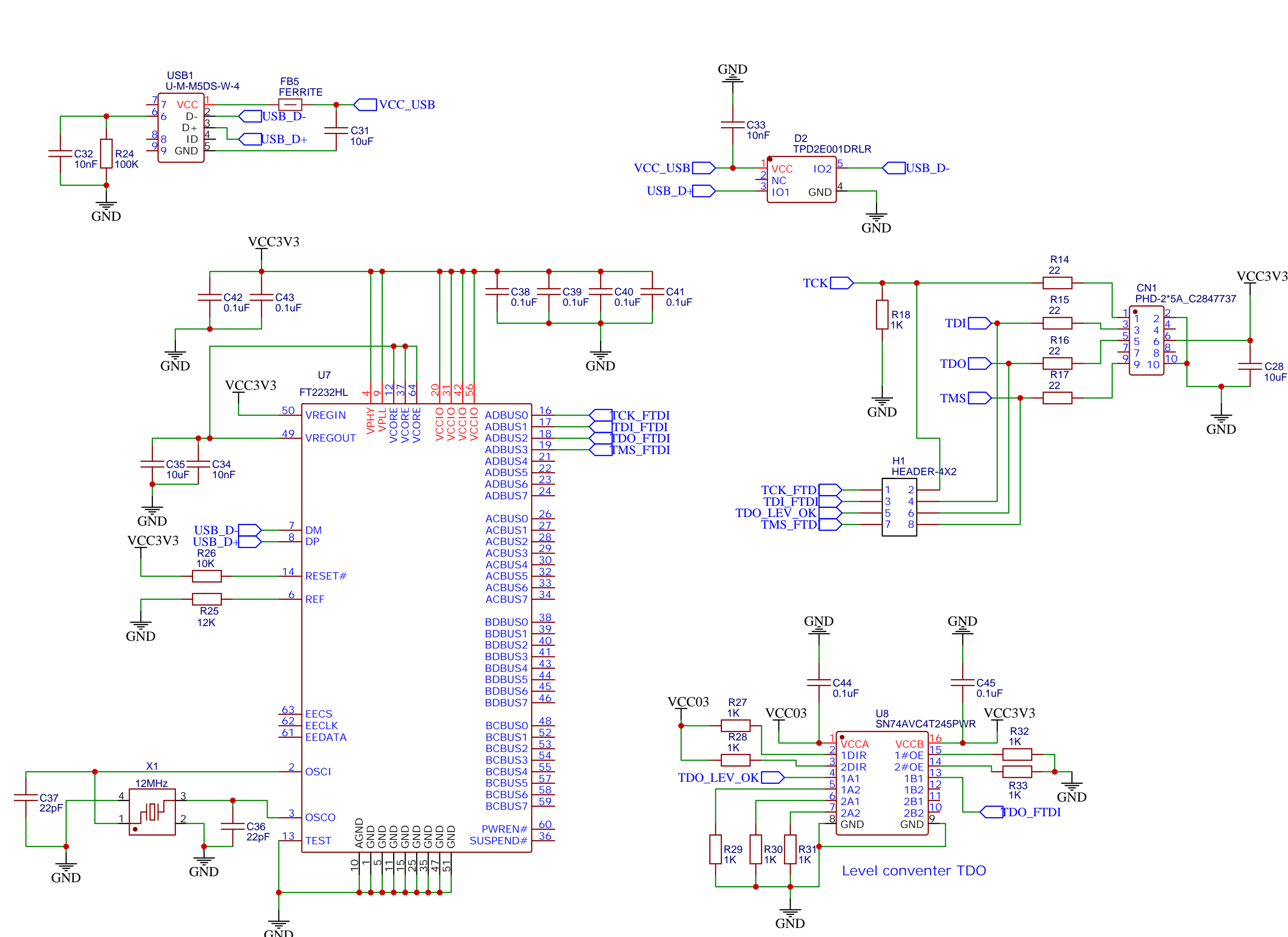
SPI Flash 32Mb



VGA Connector



USB 2 JTAG and JTAG Header



FPGA Boot Mode

VGA Output

LEDs

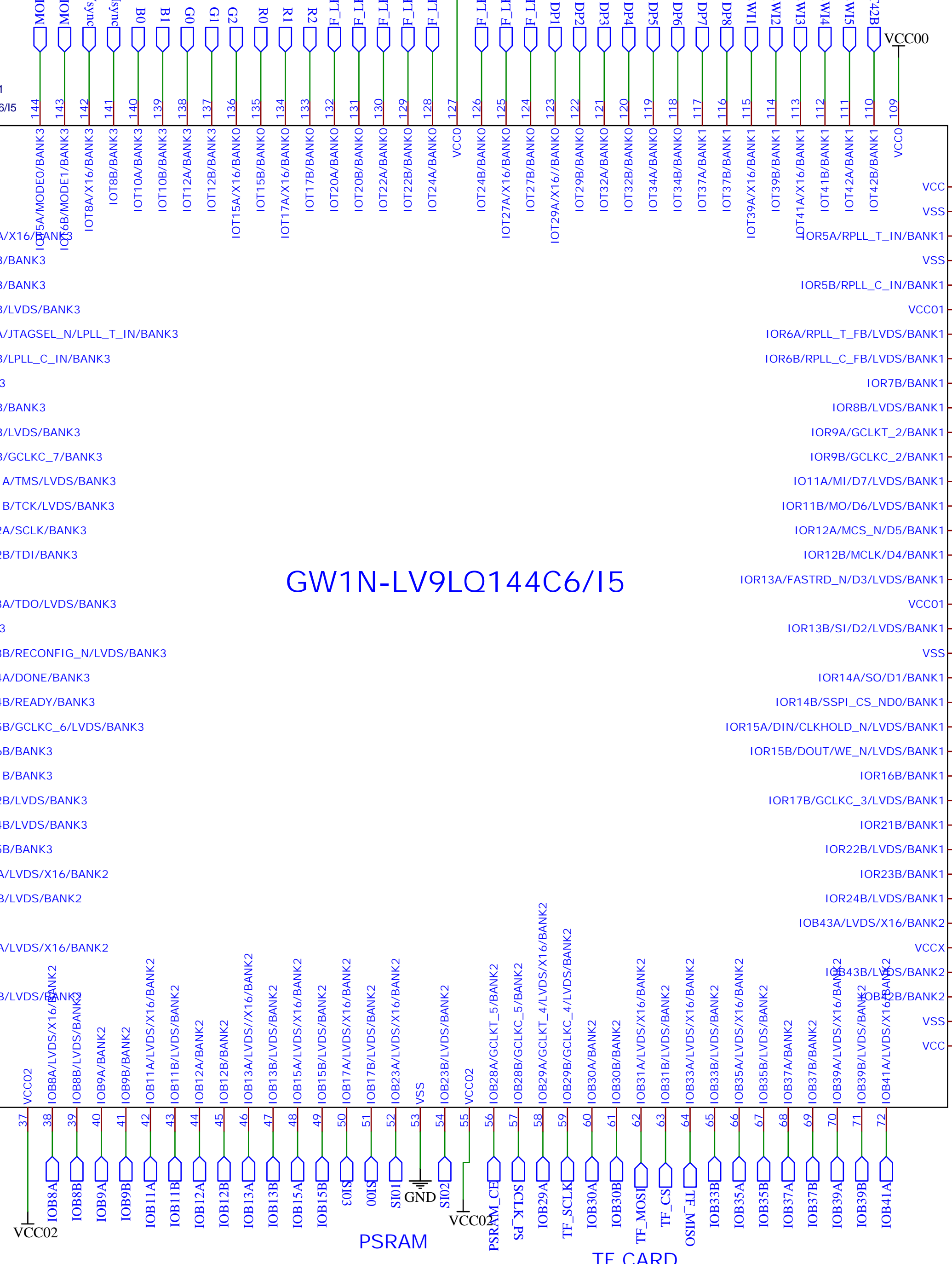
DIP Switch'es

Buttons

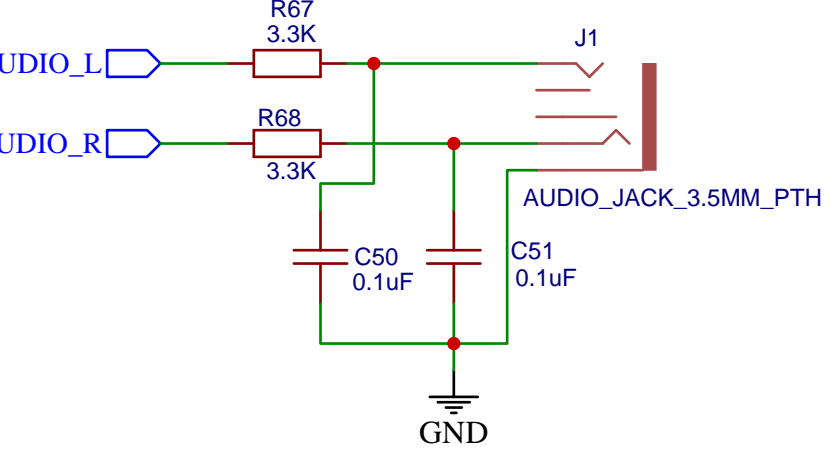
JTAG

Setup FPGA

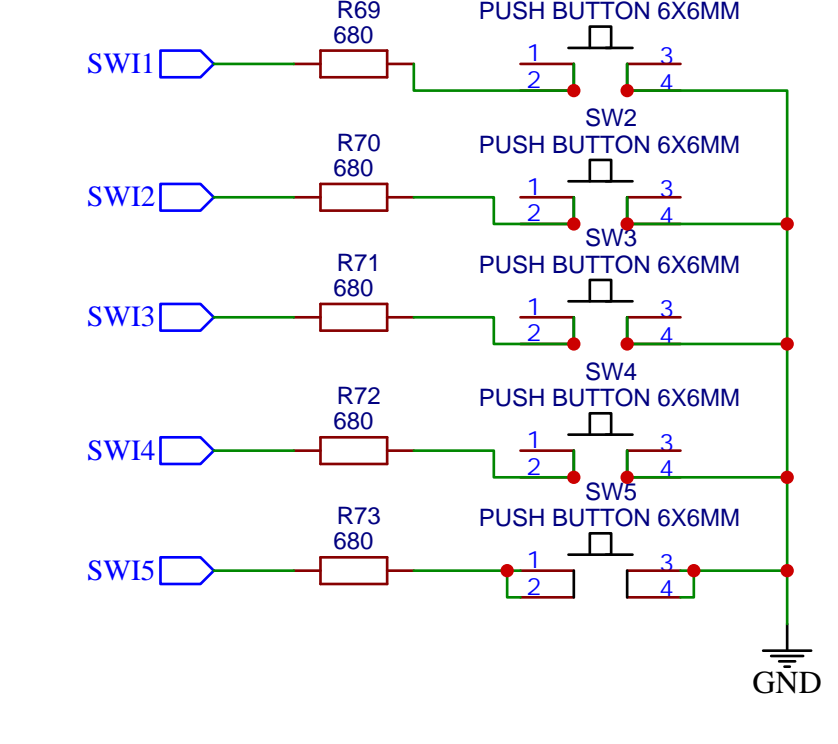
GW1N-LV9LQ144C6/I5



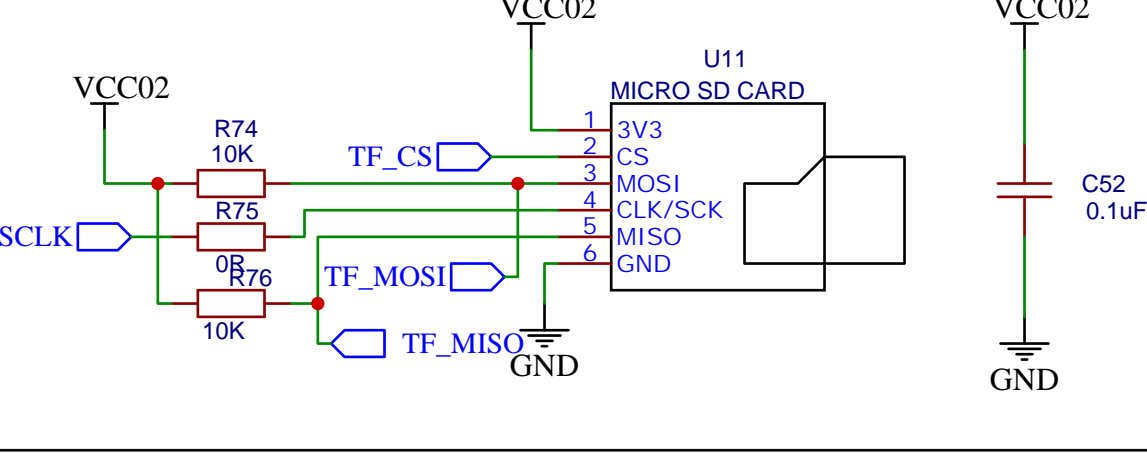
AUDIO OUTPUT



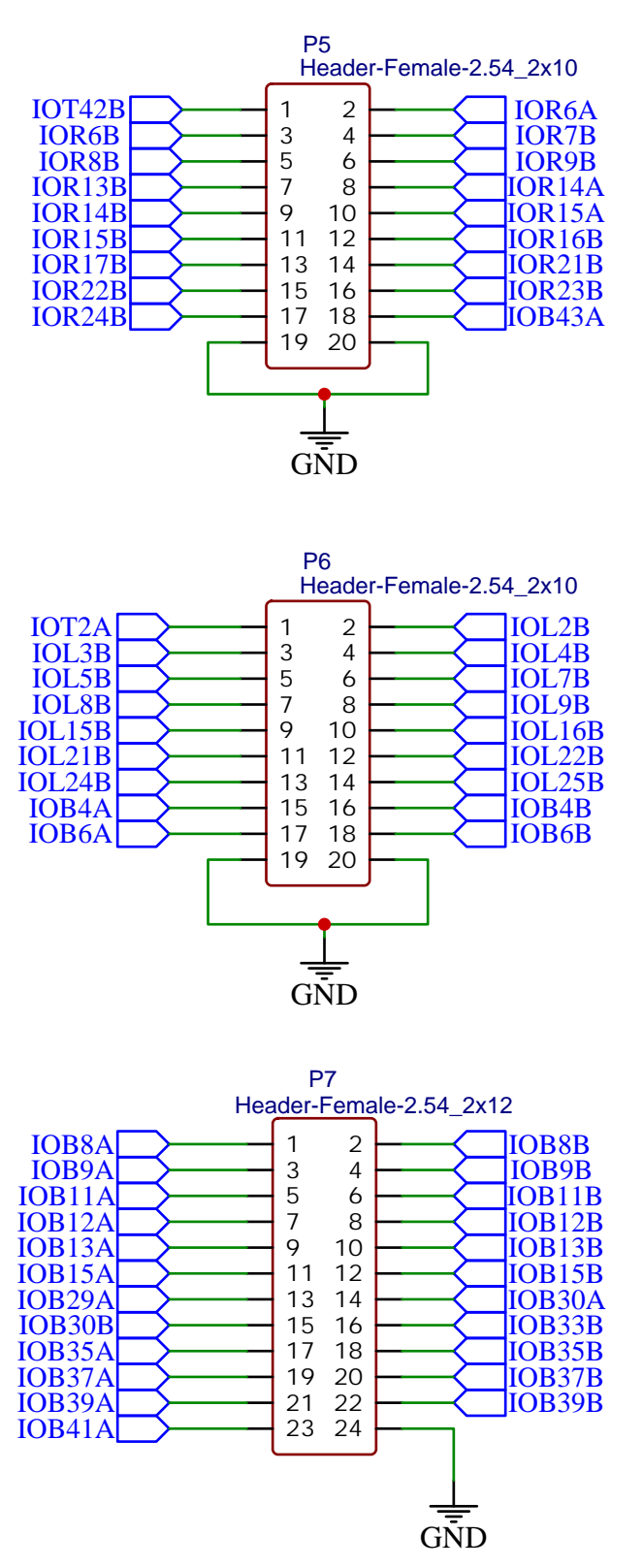
PUSH BUTTON'S



TF CARD



Goldpin Headers



FPGA Clock 24 Mhz

Flash

Audio Out