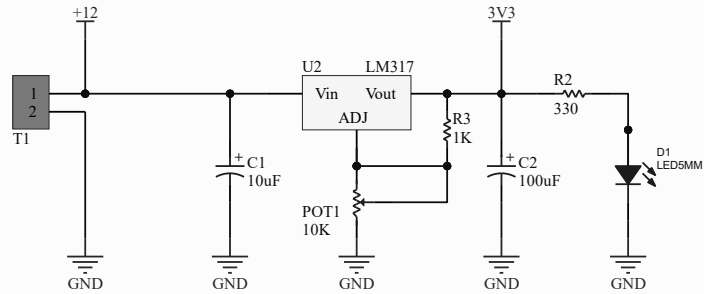
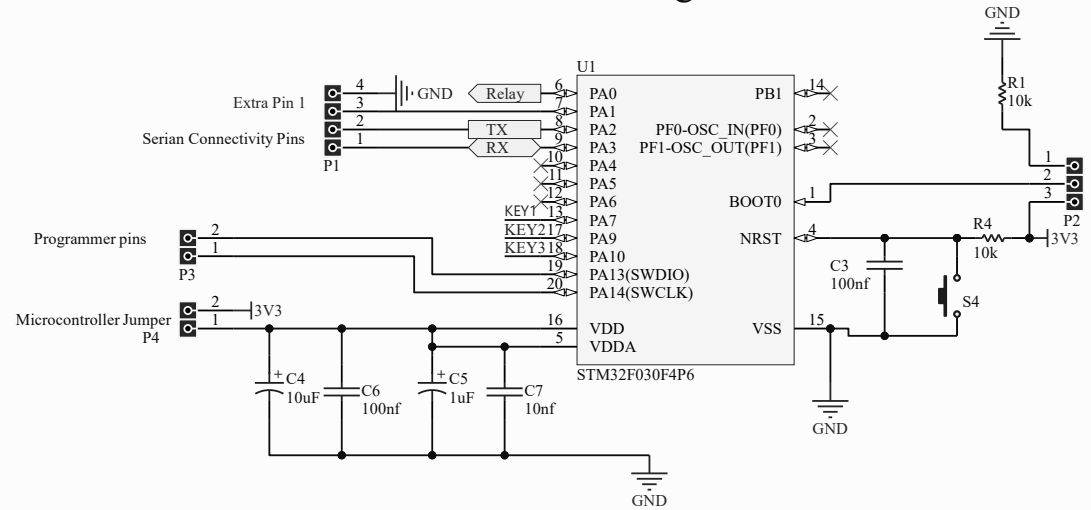


Power



Logic



Output

