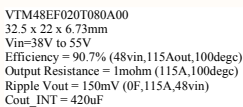
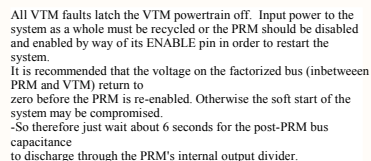


48Vin to 1V5 at 95 Amps



Op-Amp Buffer



If only a very small amount of low ESR ceramic input capacitance (and no bulk caps), and also the line inductance was relatively large, then input filter instability could result. (as per equation (16) of the PRM datasheet. One way to mitigate this would be to add series resistance with the ceramic capacitors. However, the converter dynamic input impedance should be higher than the input line resistance or the capacitor ESR



LOAD =
1V5, 95A, 147W

Title			
Size A3	Number		Revision
Date:	19/07/2016	Sheet of	
File:	C:\Altium\Vicor\Remote sense IVS 9 SchDoc		