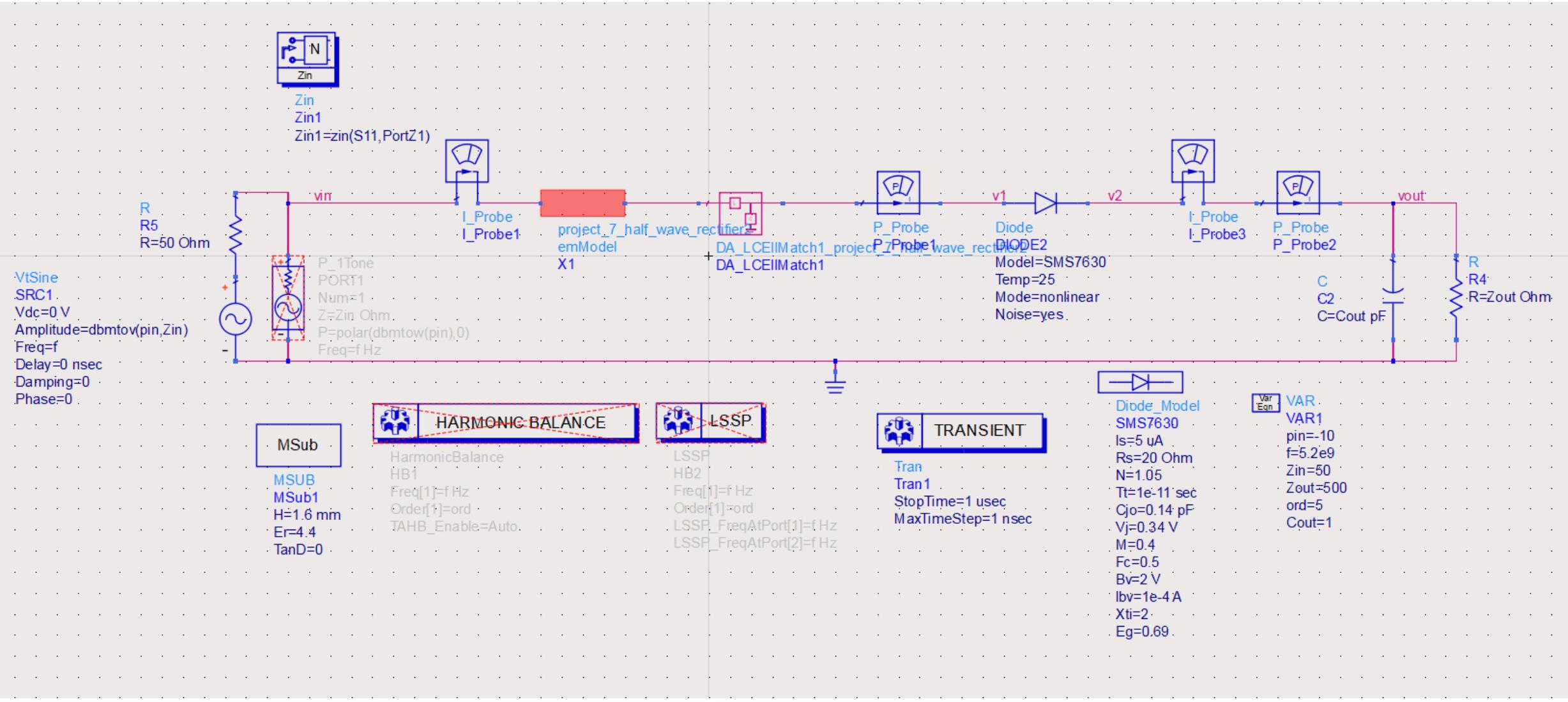
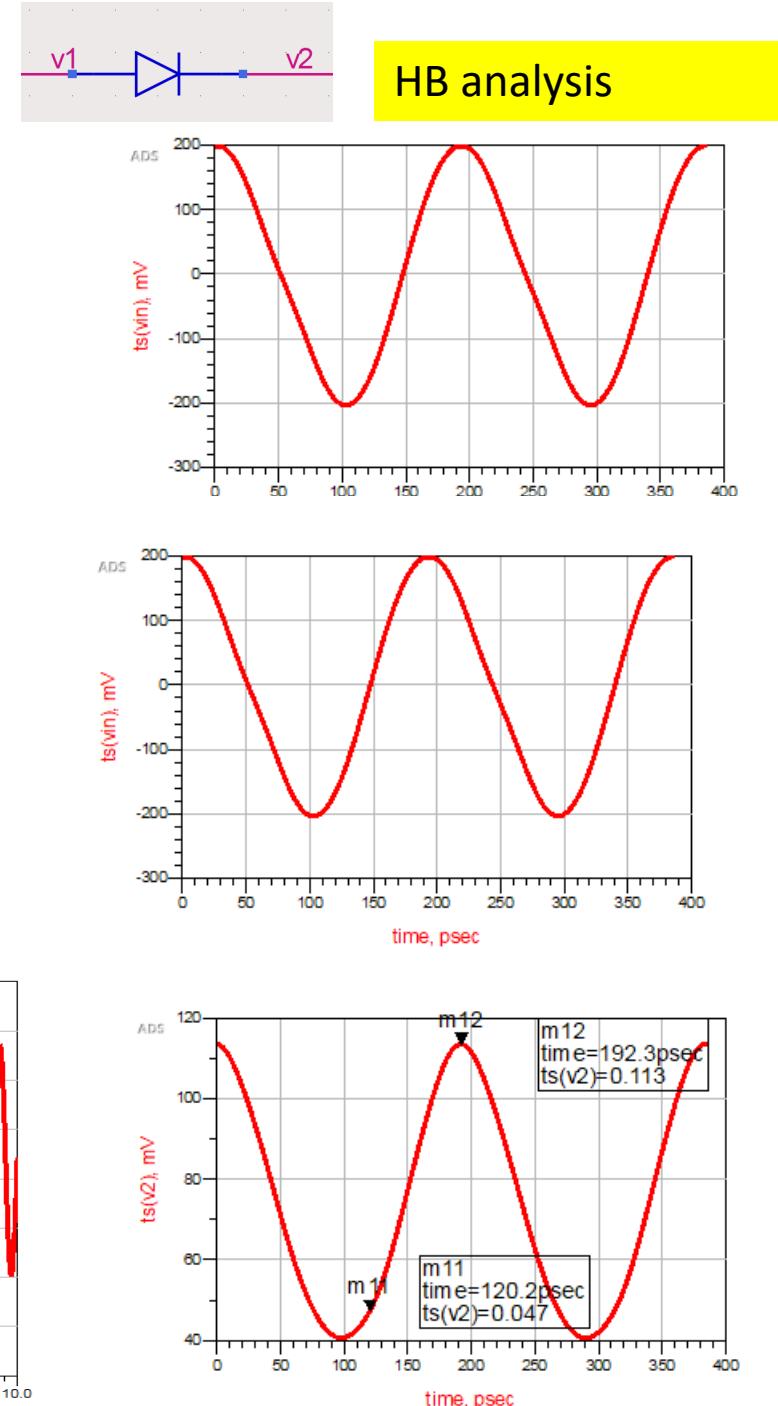
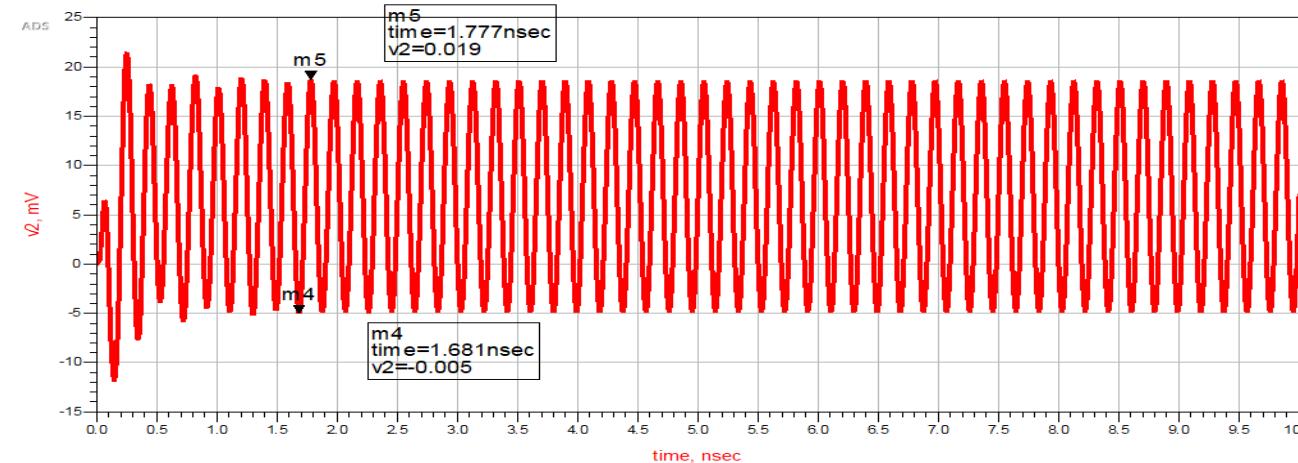
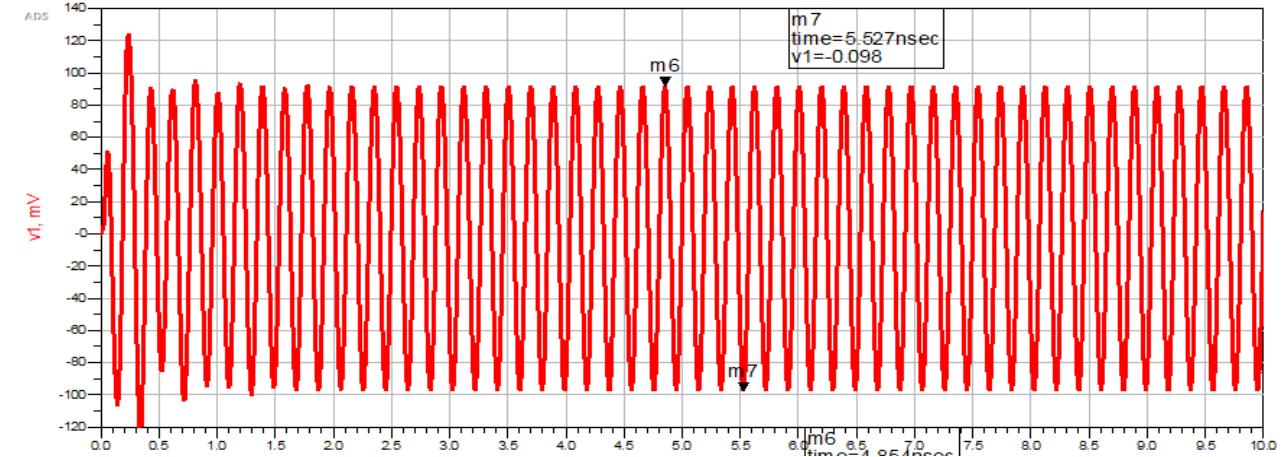
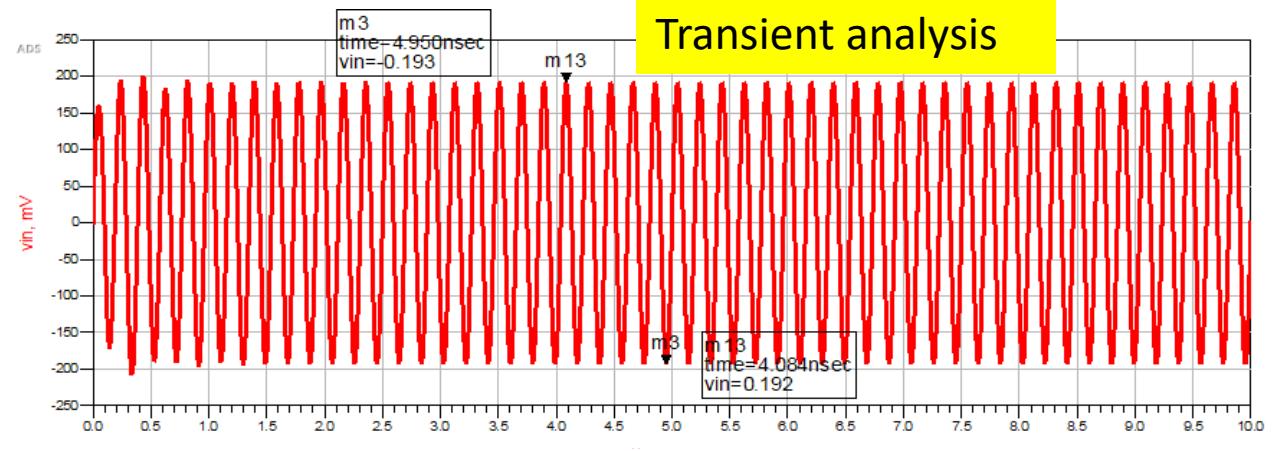


Circuit under study



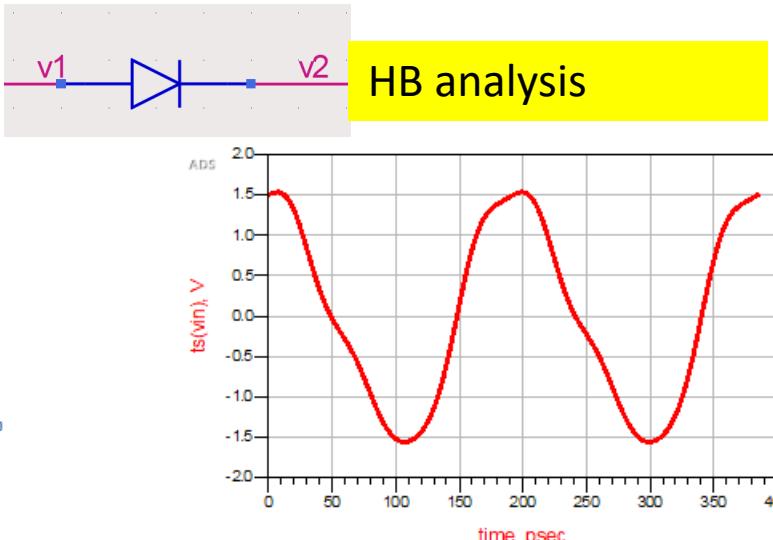
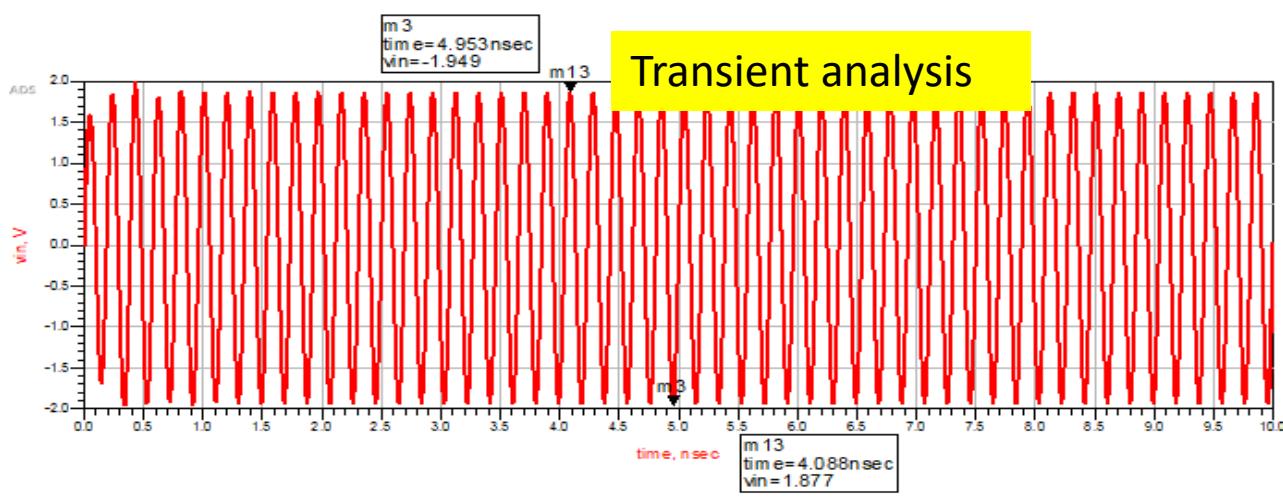


Pin=-10dBm

input

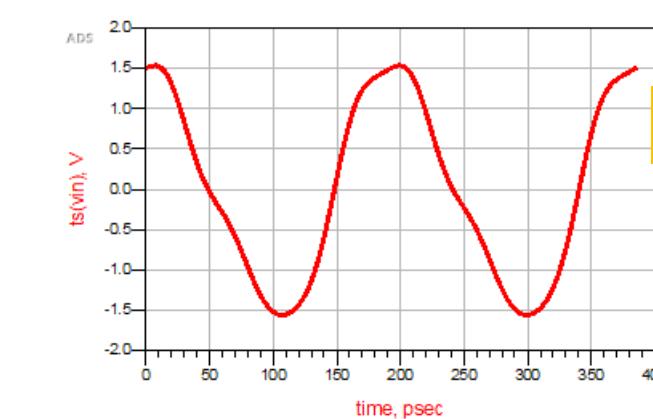
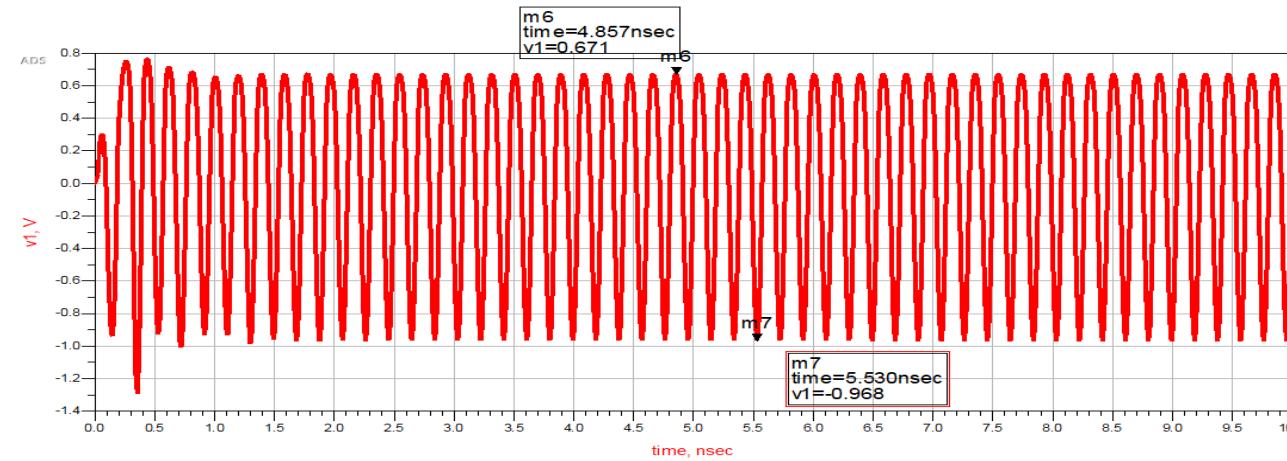
V1 voltage

V2 voltage

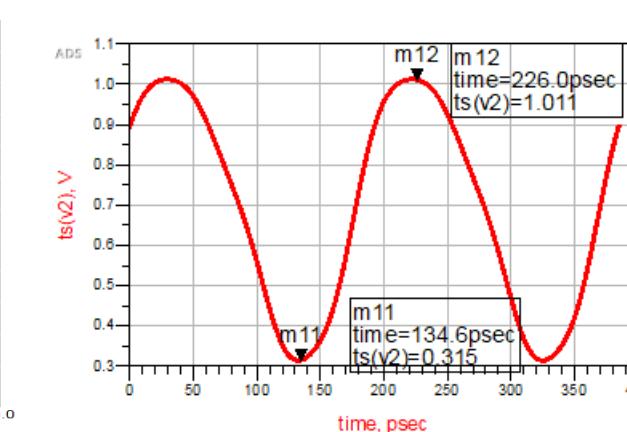
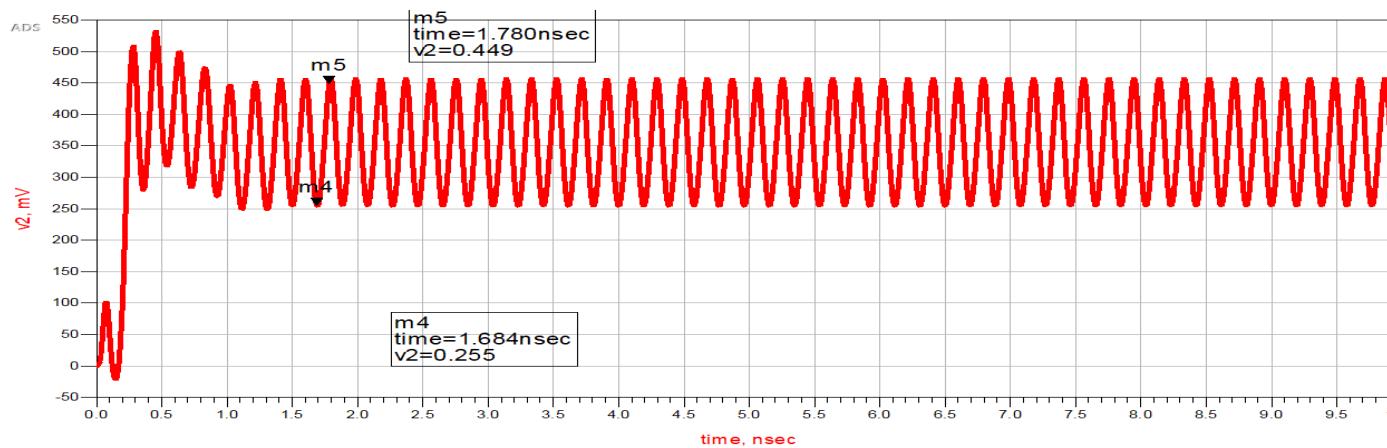


Pin=+10dBm

input



V1 voltage



V2 voltage