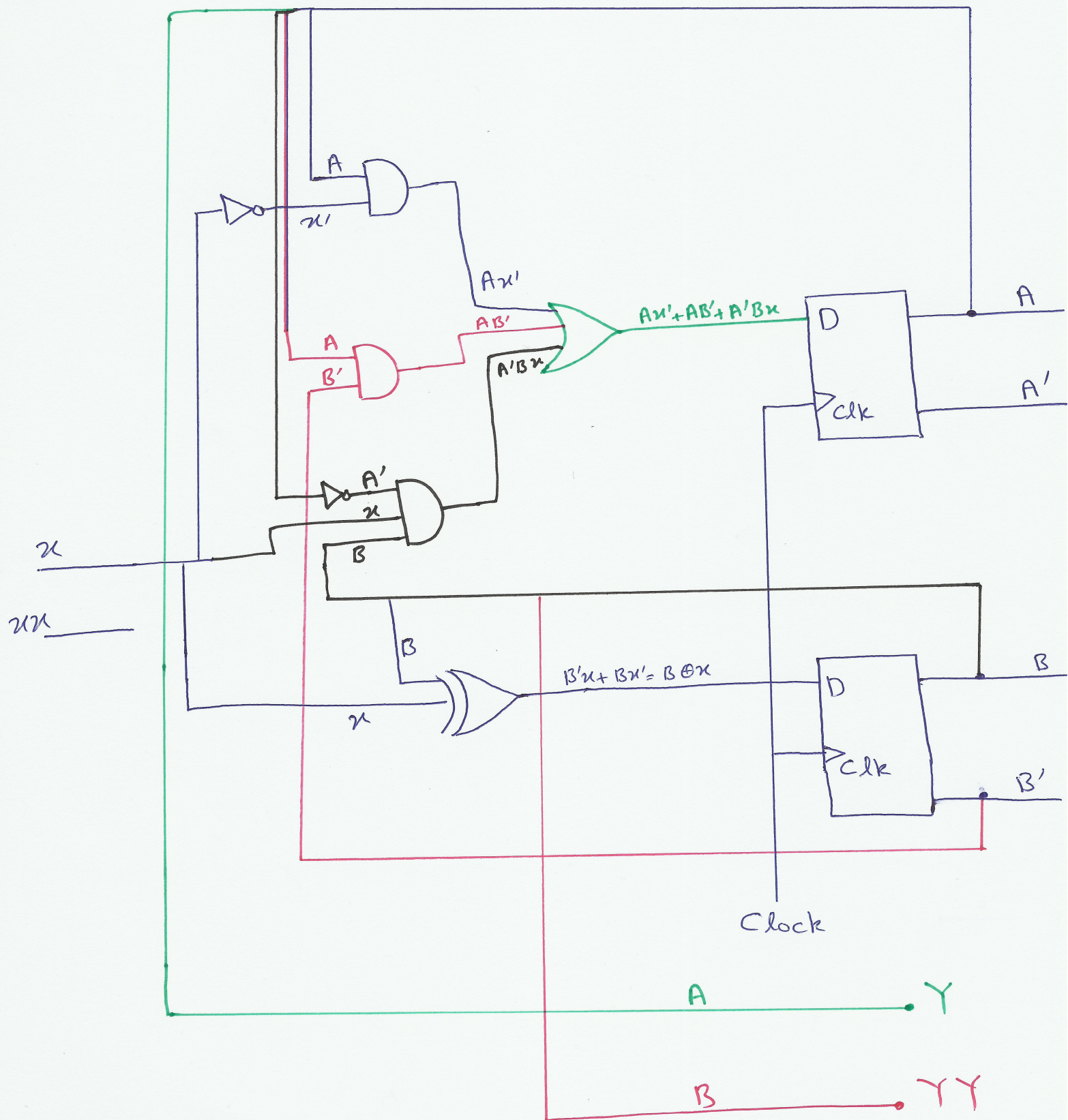


# Logic Diagram



- D And Gate
- OR Gate
- Exclusive OR Gate
- Not Gate