



### TL5/TL6

```
.lib ARF475-6FL.lib
.net I(Rout) V1 ; Rin & Rout determined from V4 and Rout
.ac dec 1k 100meg 200meg
```

```
.param Vp_pcb=5.7 ;inches per nanosecond
.param Vp_coax=7.08 ;inches per nanosecond
.param f0=128meg
;tran 0 10u 0 1n
;dc Vg 1 10 0.01 ;to find DC bias conditions
```

### TL3/TL4

```
.param TL2_L=0.65 ;total length of TL3/TL4 in inches
.param TL2_f=0.1 ;determines position of L1 along TL3/TL4
```

### TL1/TL2

```
.param TL1_L=0.75 ;total length of TL1/TL2 inches
.param TL1_f=0.9 ;determines position of C3 along TL1/TL2
```

;step param TL1\_f 0.6 0.95 0.05

.param T2\_L=1 ;length of T2 coax, in inches

.param balun\_AL=1u

.param T1\_L=1.5; length of T1 and T3 coax, in inches