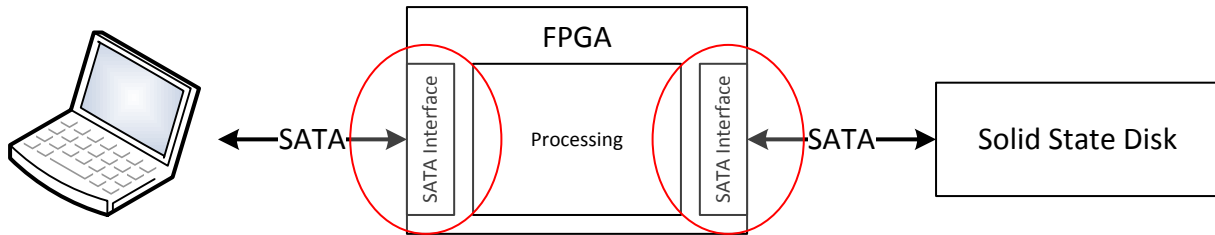
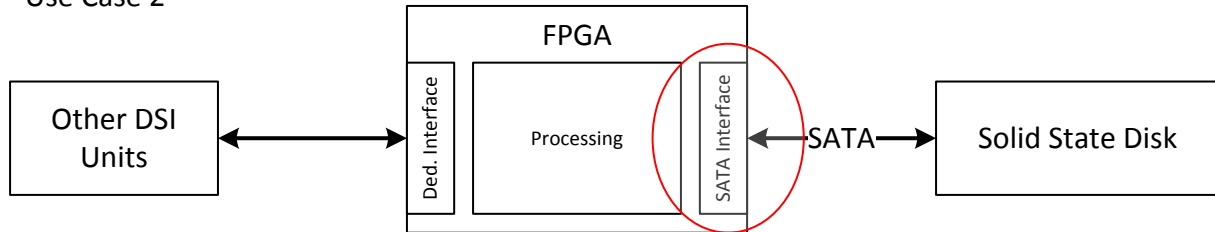


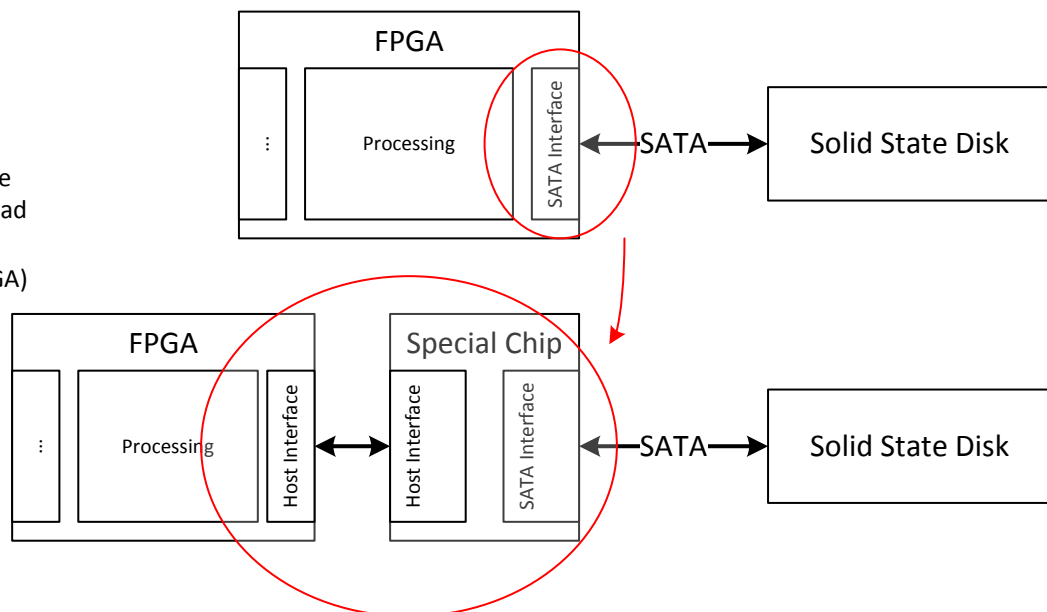
Use Case 1



Use Case 2



Alternative Implementation  
(dedicated chip will be used (if available) instead of SATA interface implemented in an FPGA)



On user side (processing side / host side) often some kind of FIFO Interface are used