

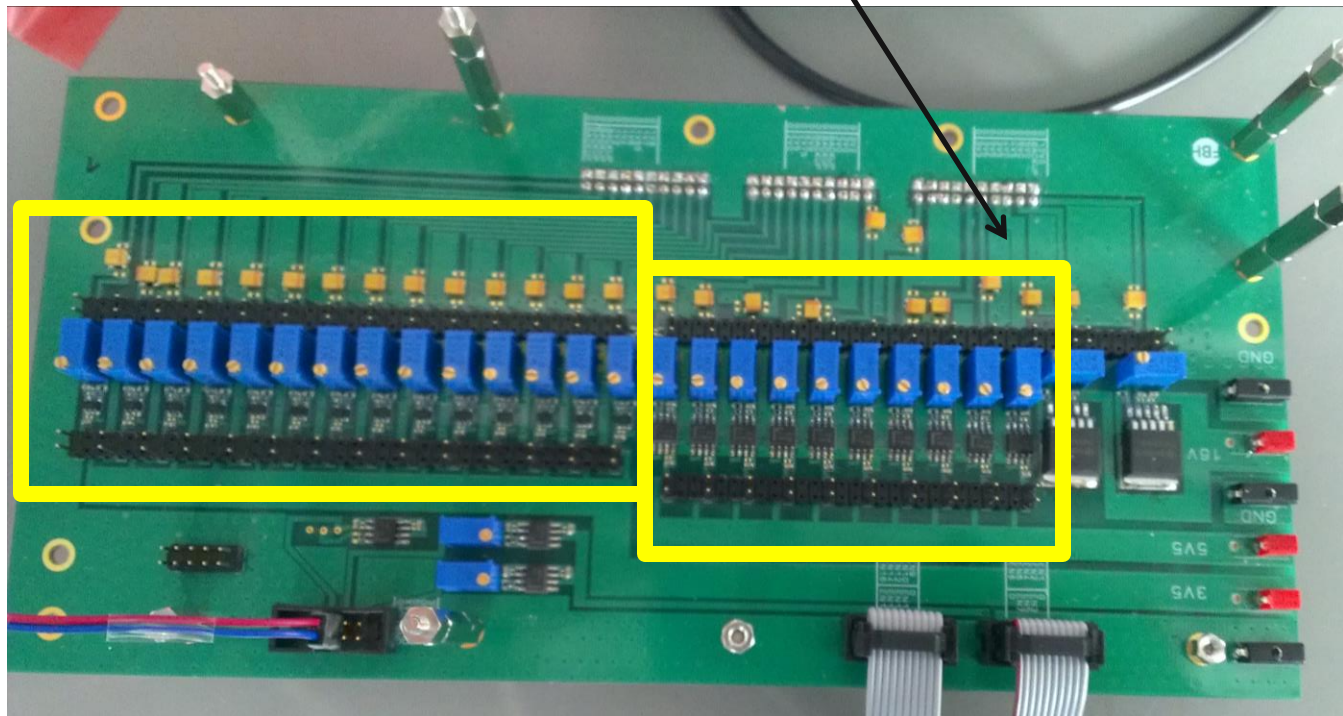
Low dropout linear voltage regulator (LDO) board

Input voltages = 5V5 → TPS7A7001

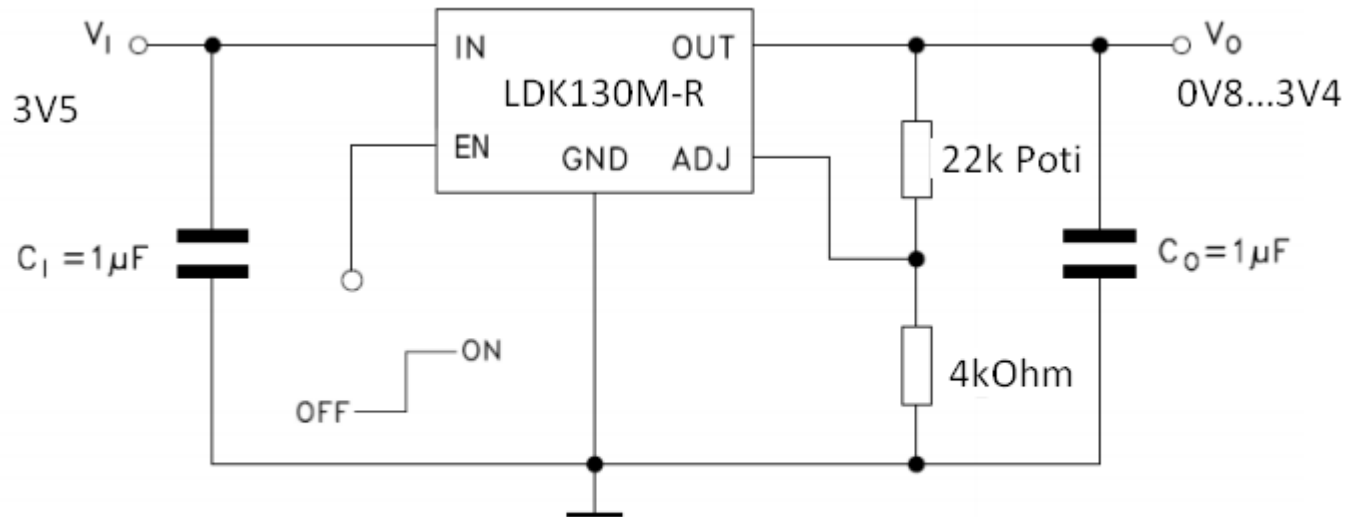
<http://www.ti.com/lit/ds/symlink/tps7a7001.pdf>

Input voltages = 3V5 → LDK130

www.st.com/resource/en/datasheet/ldk130.pdf

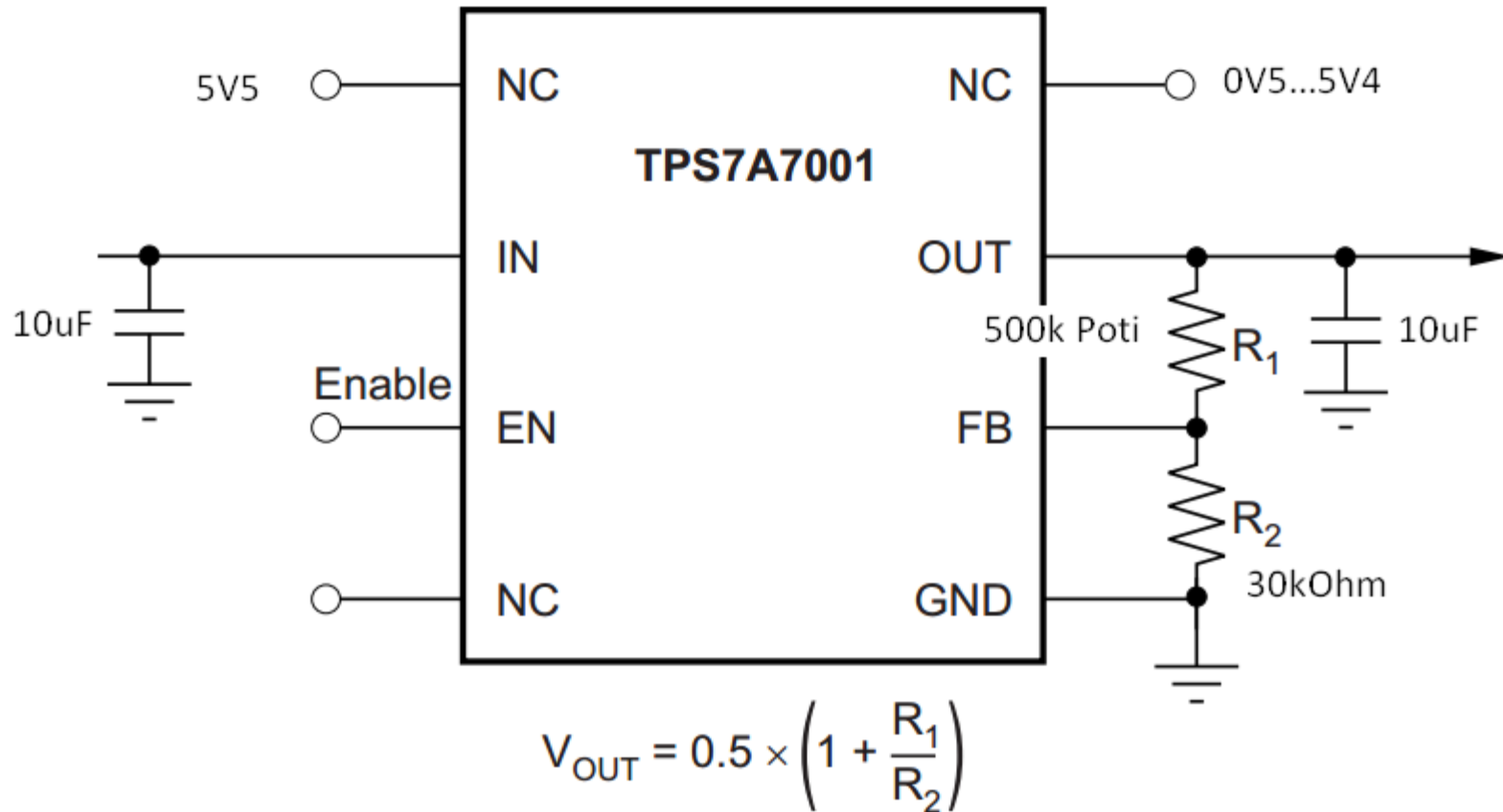


LDK130M-R



$$V_O = V_{ADJ} (1 + R_1/R_2)$$

TPS7A7001DDA



input and output capacitor

FIRST:

Murata, 10 μ F, **6V3**, X5R, 0402

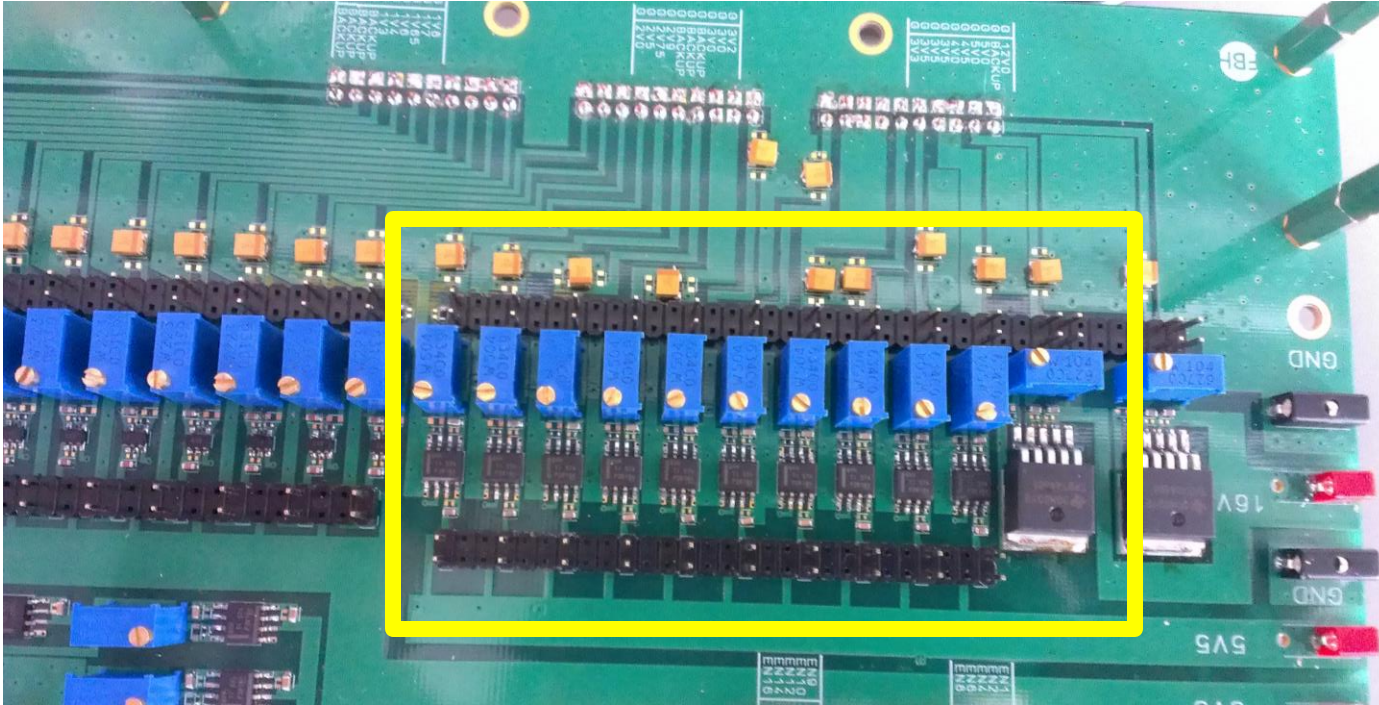
http://de.farnell.com/tdk/c1005x5r0j106m050bc/kondensator-mlcc-x5r-10uf-6-3v/dp/2346872?ost=2346872&searchView=table&isrfrnonsku=false&ddkey=http%3Ade-DE%2FElement14_Germany%2Fsearch

SUBSTITUTE BY THIS ONE ???

Murata, 10 μ F, 25V, X5R, 0603

http://de.farnell.com/murata/zrb18ar61e106me01l/kondensator-mlcc-x5r-10uf-25v/dp/2469398RL?ost=2469398RL&searchView=table&isrfrnonsku=false&ddkey=http%3Ade-DE%2FElement14_Germany%2Fsearch

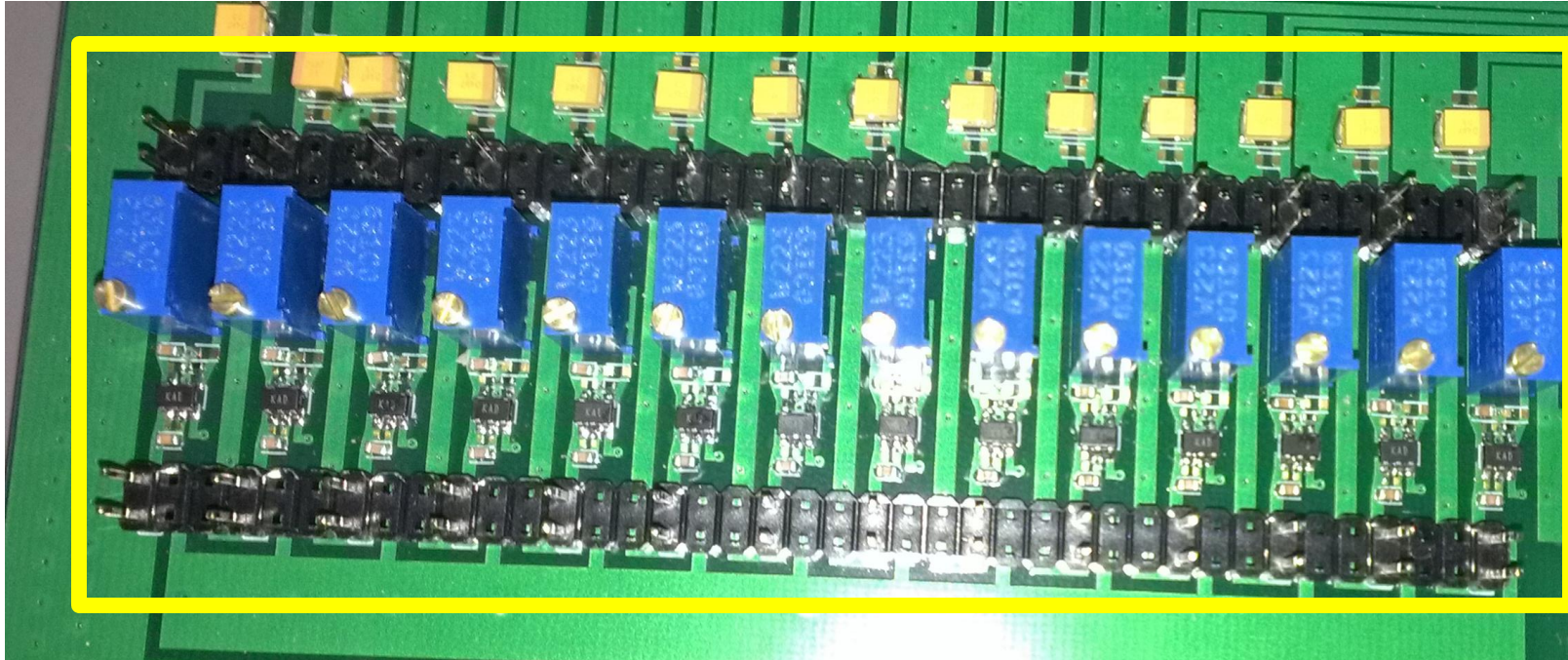
TPS7A7001



$R_{IN_DC_of_all_in_parallel} = 23.7\Omega, 4.5\Omega, 1.7\Omega$ of module 1,2,3 after crash

R_{OUT} (which also depends on Poti) = 4.6k, 30k..48k, not suspicious!

LDK130M-R



$R_{IN_DC_of_all_in_parallel} = 0.9\Omega, 1.2\Omega, 1.5\Omega$ of module 1,2,3 after crash

R_{OUT} (which also depends on Poti) = **$1.4\Omega, 100\Omega$** , 1.6k, 3.6k, 8k, 12k, 9.7k