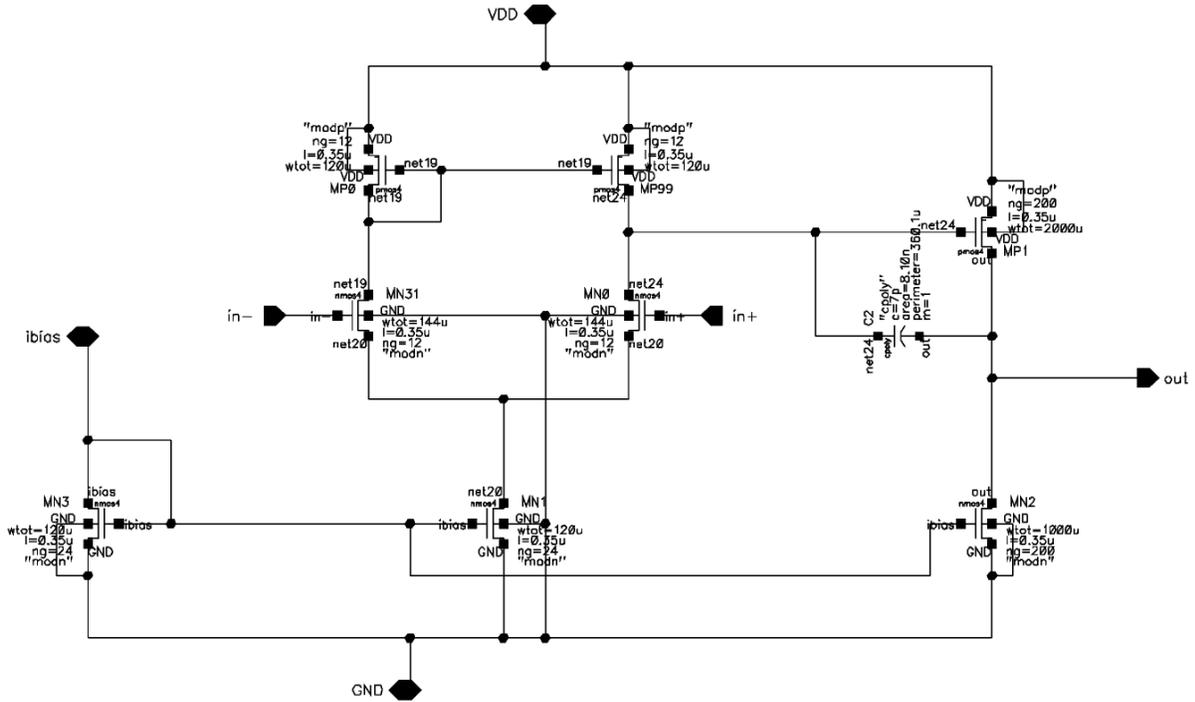
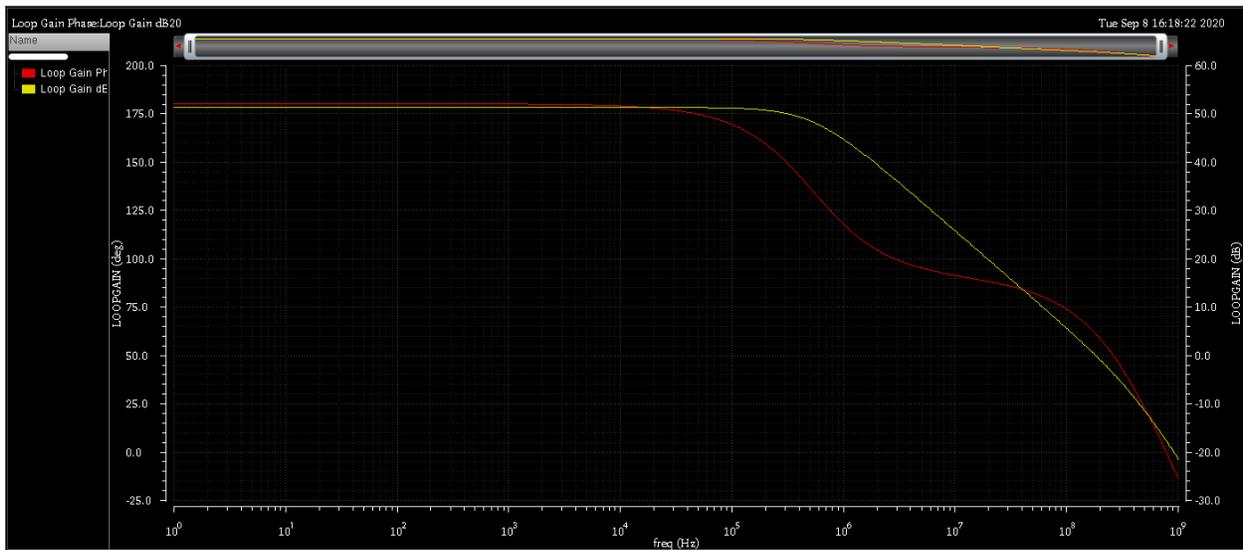


Dominik class A Circuit

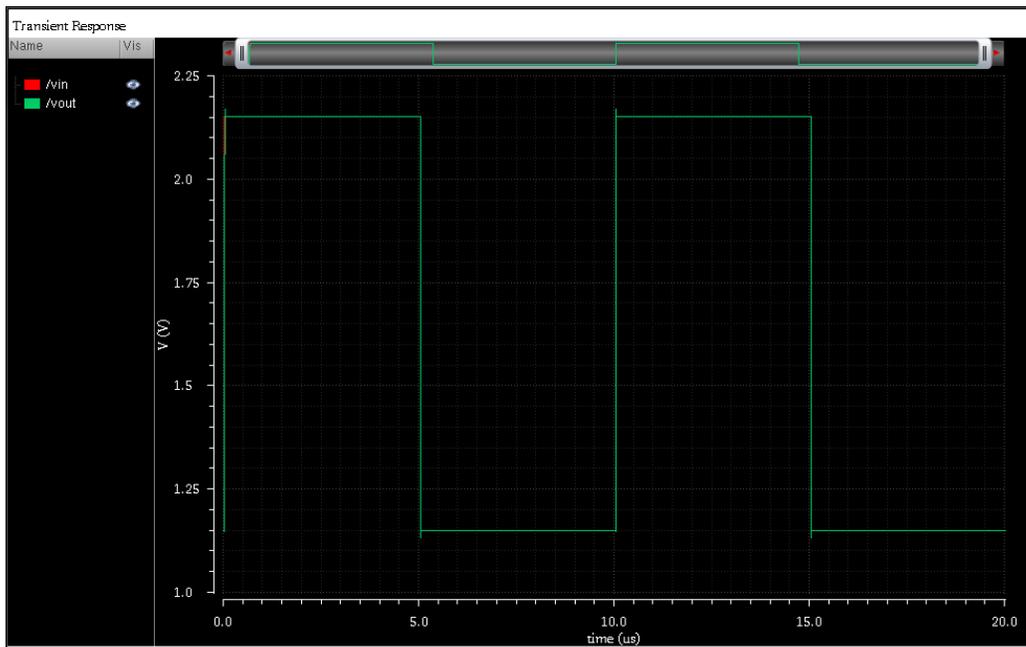


AC simulation with $C_L = 20$ pF and $I_{tail} = 2.4$ mA, $V_{DD} = 3.3$ V (single supply operation)



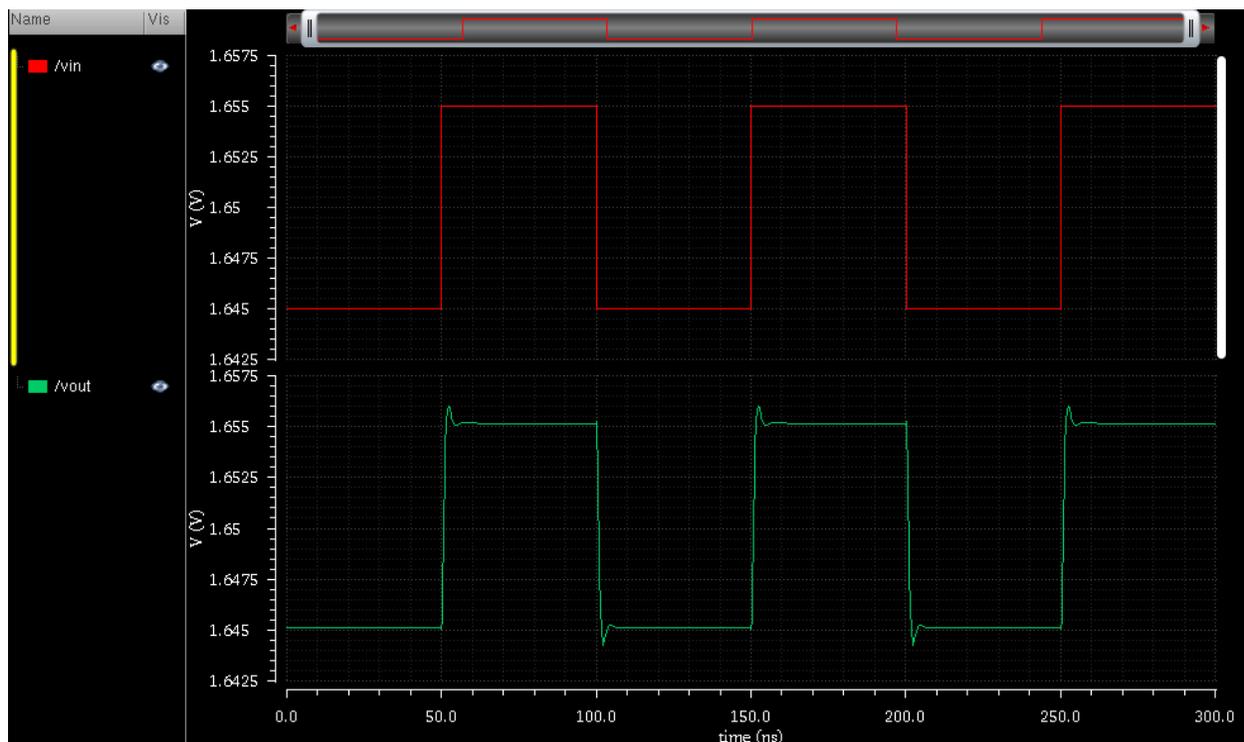
Result shows GBW = 181 MHz, Phase Margin = 61 deg

Large signal step response : $v_{in} = 1\text{ V}_{p-p}$



-SR = 262 V/ μ s, +SR = 299 V/ μ s

Small signal step response : $v_{in} = 10\text{m V}_{p-p}$



AC simulation closed loop test (unity gain)

