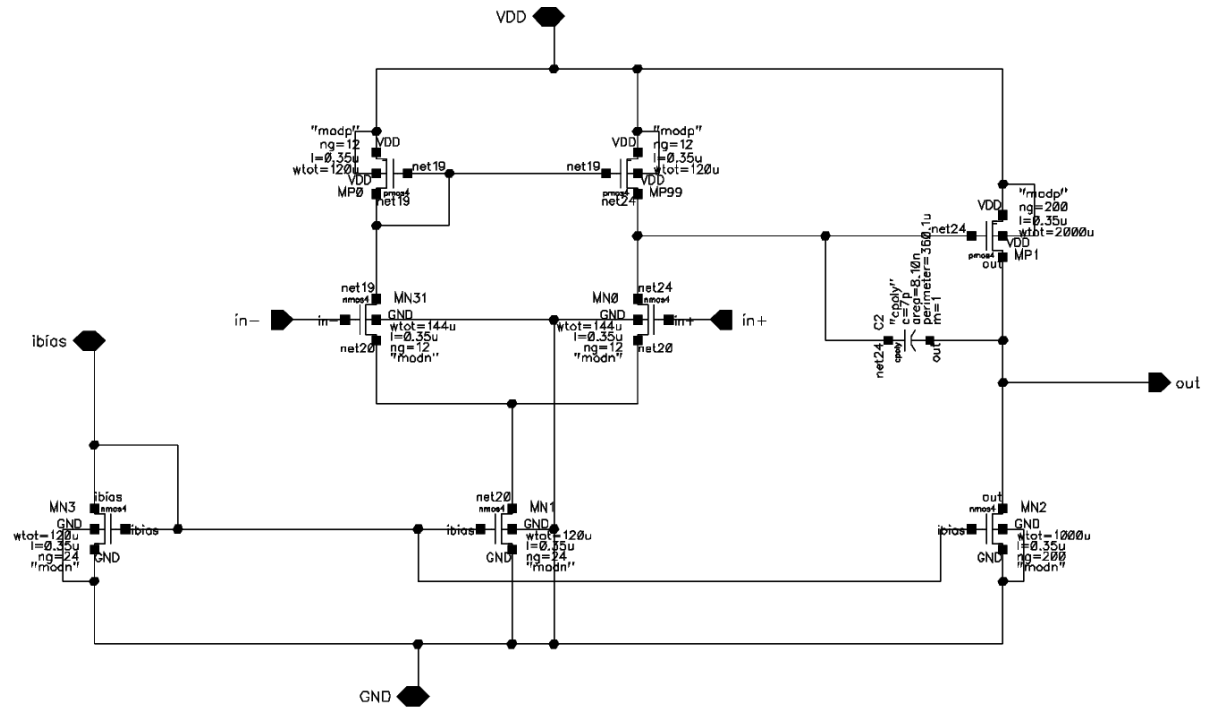
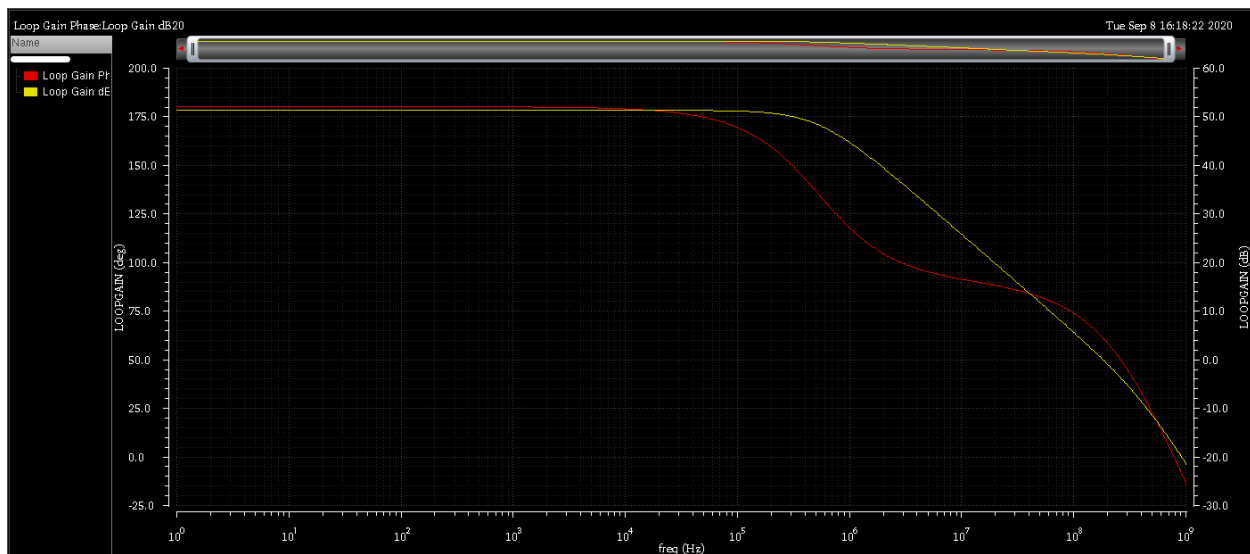


## Dominik class A Circuit

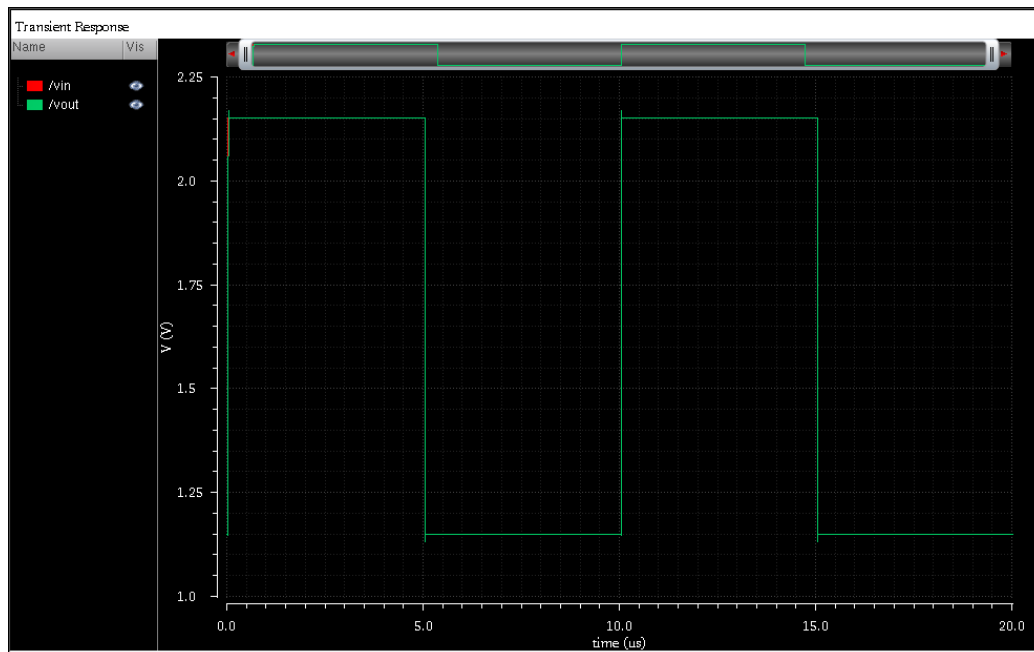


**AC simulation** with CL = 20 pF and I tail = 2.4 mA, VDD=3.3 V (single supply operation)



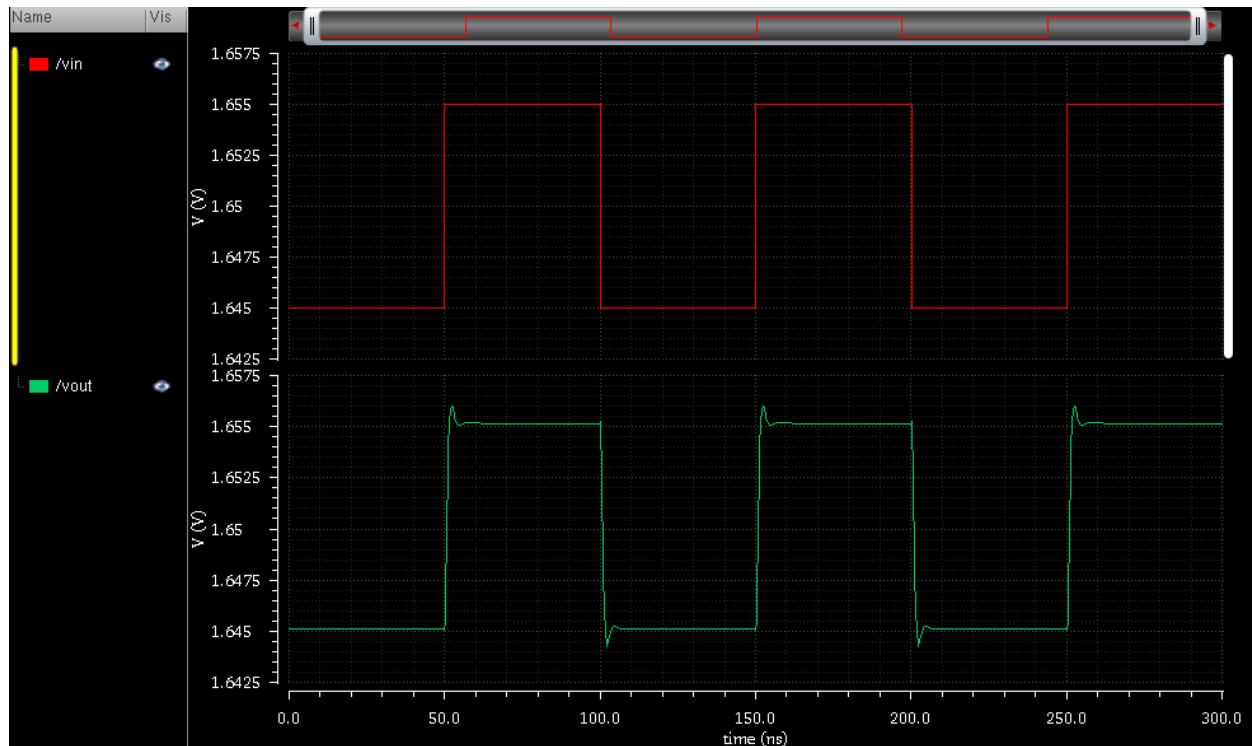
Result shows GBW = 181 MHz, Phase Margin = 61 deg

**Large signal step response :  $v_{in} = 1\text{ V}_{p-p}$**



$-SR = 262\text{ V}/\mu s$ ,  $+SR = 299\text{ V}/\mu s$

**Small signal step response :  $v_{in} = 10\text{ mV}_{p-p}$**



## AC simulation closed loop test (unity gain)

