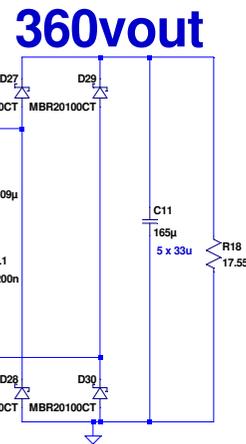
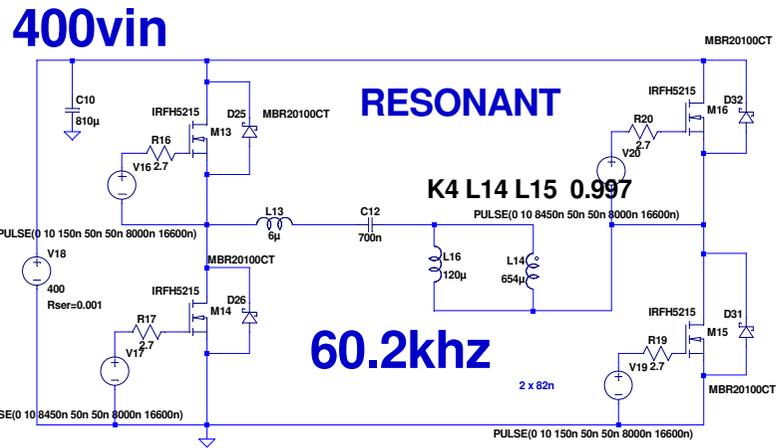


Output current regulated LLC converter (regulation via filtered output of current sense transformer)

K L1 L2 1



- I LR 24.4 //35.4
- I LPLEL 11.6 //19.1
- I L PRI 21.1//33.4
- I L SEC 23.5//37.8
- I DIO 10.9 // 16.7 // 37.6
- I FET 17.3 // 35.3
- I MAG PRI

.tran 0 40m 0 startup ulc

To Error Amplifier