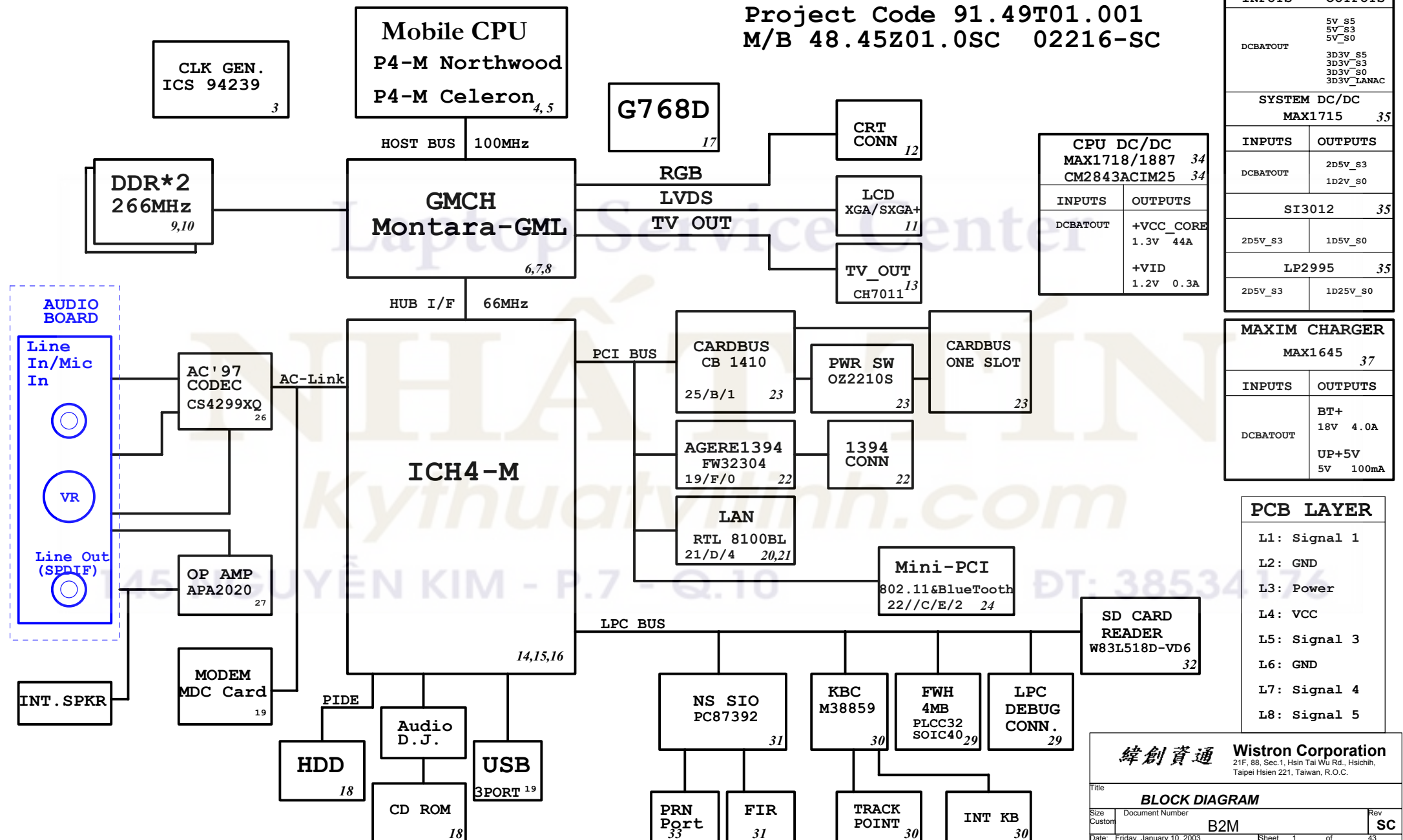


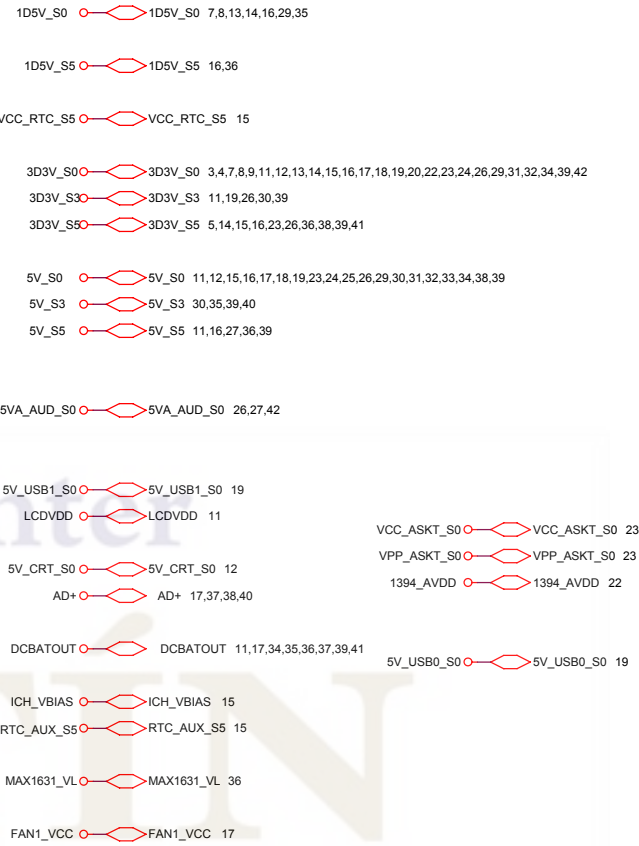
B2M Block Diagram

SC

2003/01/03

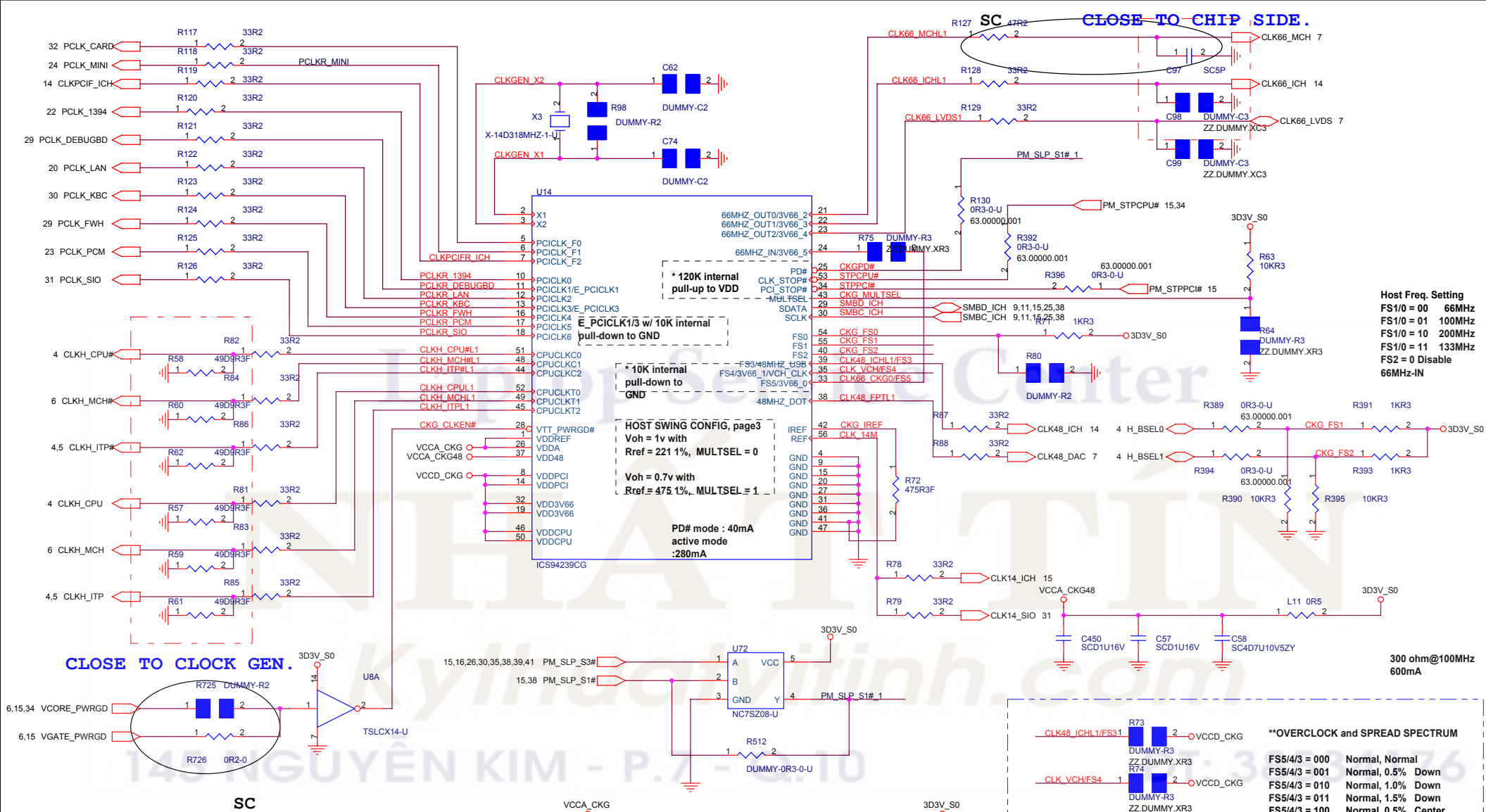
Project Code 91.49T01.001
M/B 48.45Z01.0SC 02216-SC





PCI DEVICE RESOURCE ASSIGNMENT

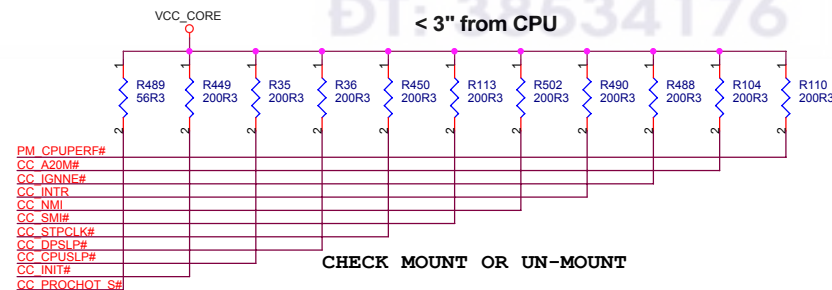
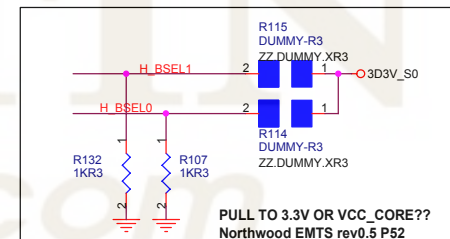
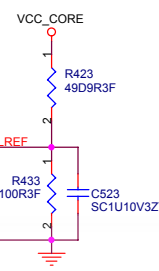
	BUS	DEVICE	IDSEL	PCI_REQ#	PCI_GNT#	INT_IRQ#
LAN	1	5	PCI_AD21	REQ#4	GNT#4	IRQD#
CardBus	1	9	PCI_AD25	REQ#1	GNT#1	IRQB#
VIA1394	1	3	PCI_AD19	REQ#0	GNT#0	IRQF#
MiniPCI	1	6	PCI_AD22	REQ#2	GNT#2	IRQC# / IRQE#

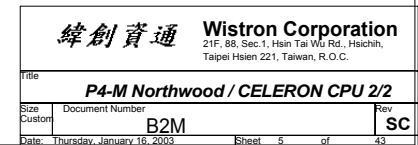


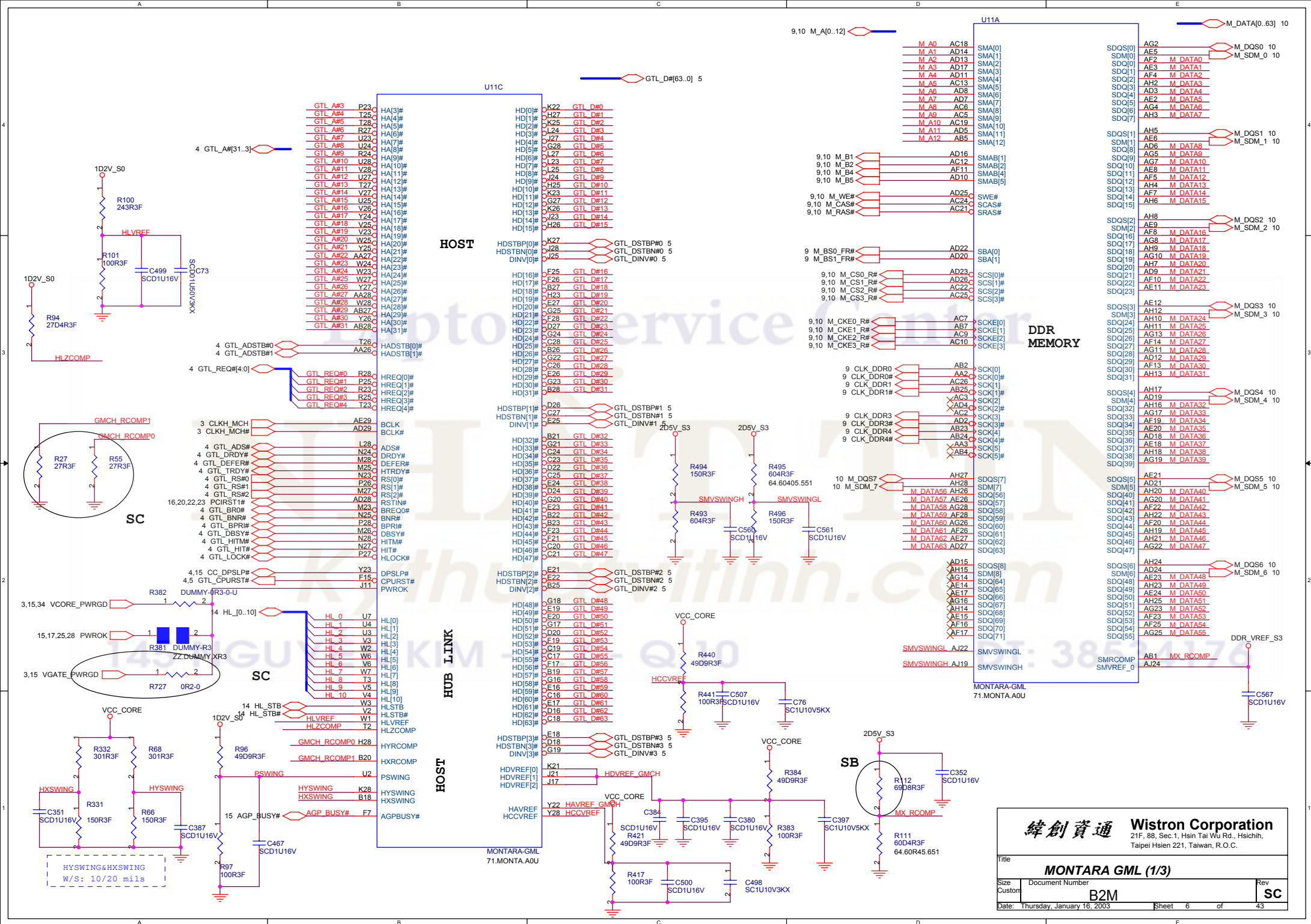
Functionality

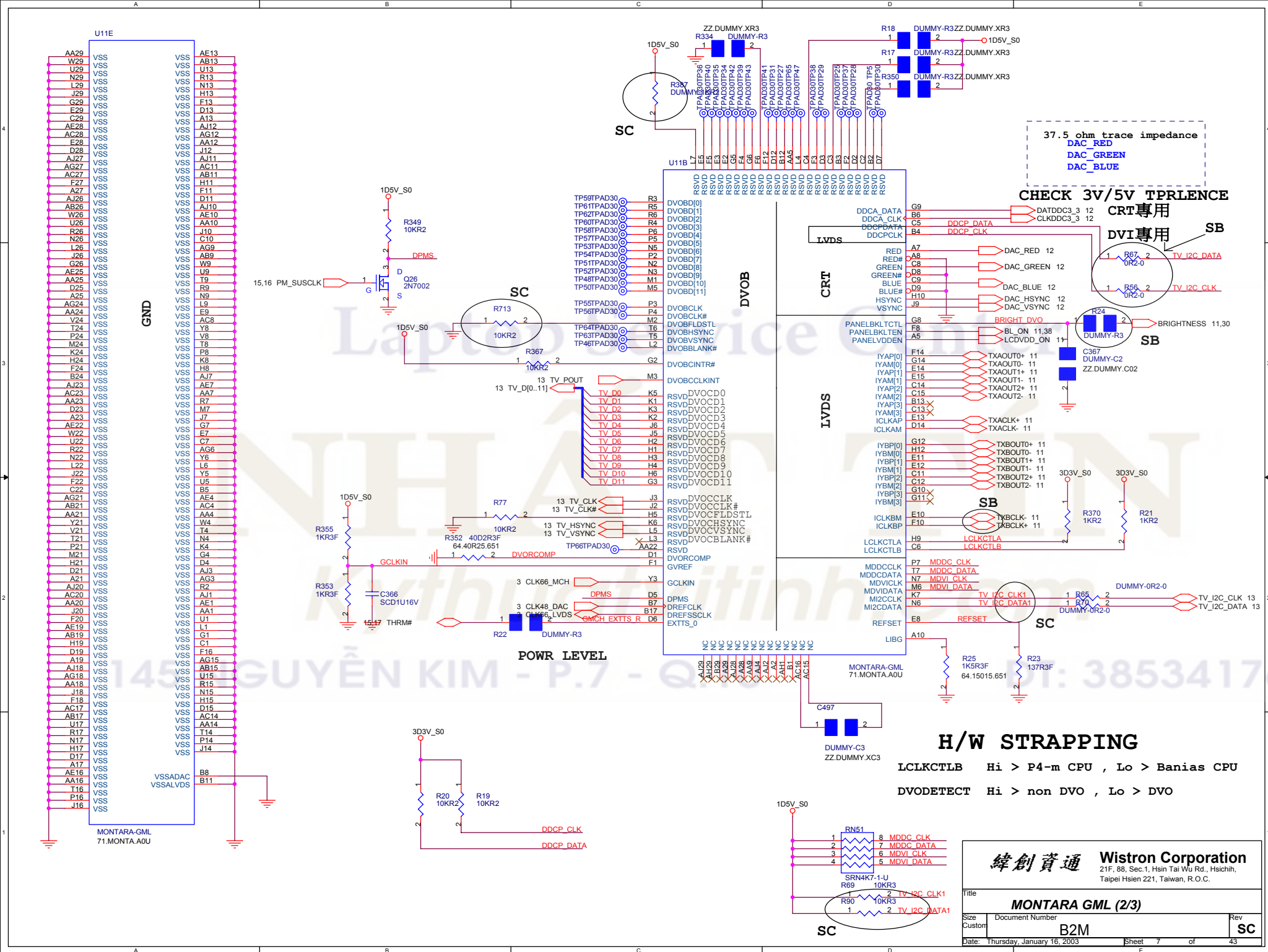
BIT			CPUCLK	3V66	66Buff[2:0]	66MHz_IN	PCICLK_F
FS2	FS1	FS0	MHz	MHz	MHz	MHz	MHz
0	0	0	66.66	66.66	66.66	33.33	33.33
0	0	1	100.00	66.66	66.66	33.33	33.33
0	1	0	200.00	66.66	66.66	33.33	33.33
0	1	1	133.33	66.66	66.66	33.33	33.33
1	0	0	66.66	66.66	66MHz_IN	Input	66MHz_IN/2
1	0	1	100.00	66.66	66MHz_IN	Input	66MHz_IN/2
1	1	0	200.00	66.66	66MHz_IN	Input	66MHz_IN/2
1	1	1	133.33	66.66	66MHz_IN	Input	66MHz_IN/2

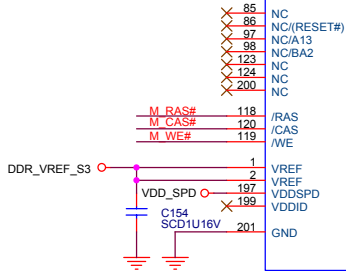
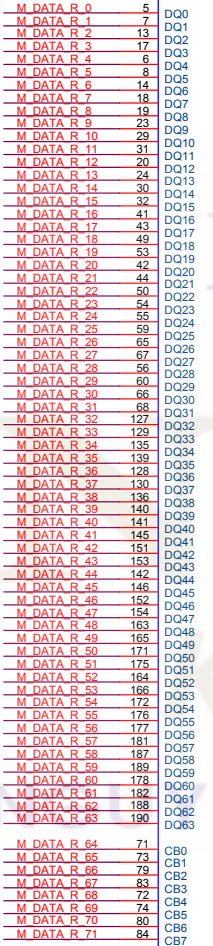
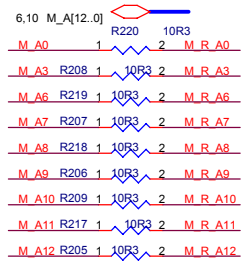
FOXCON: 62.10055.011





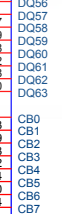
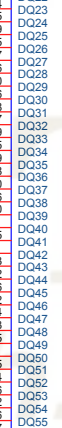
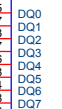
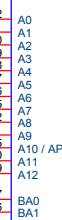
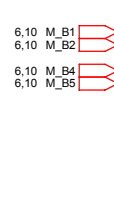
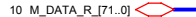
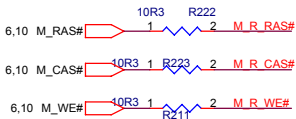
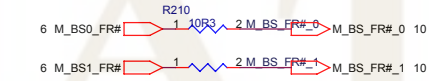
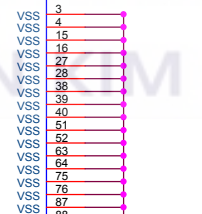
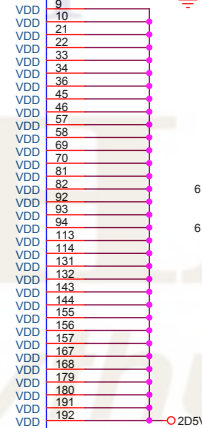
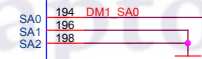
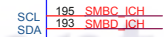
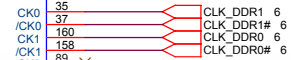
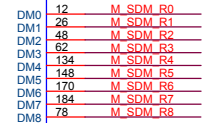
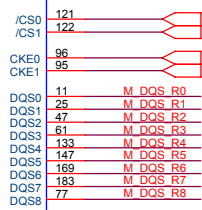




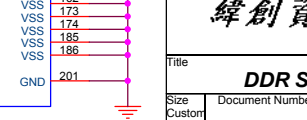
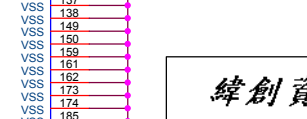
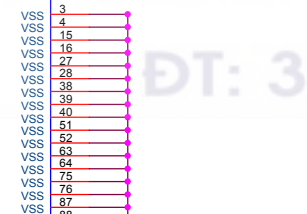
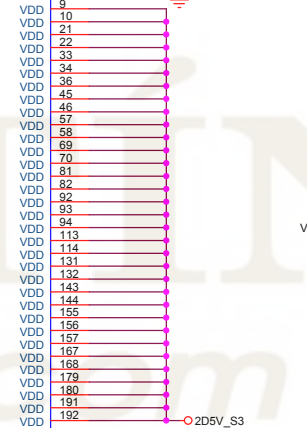
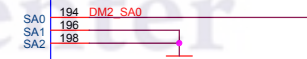
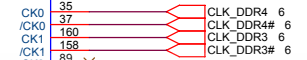
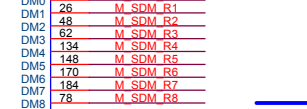
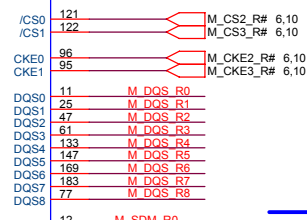


DDR-SODIMM-N

NORMAL TYPE



REVERSE TYPE



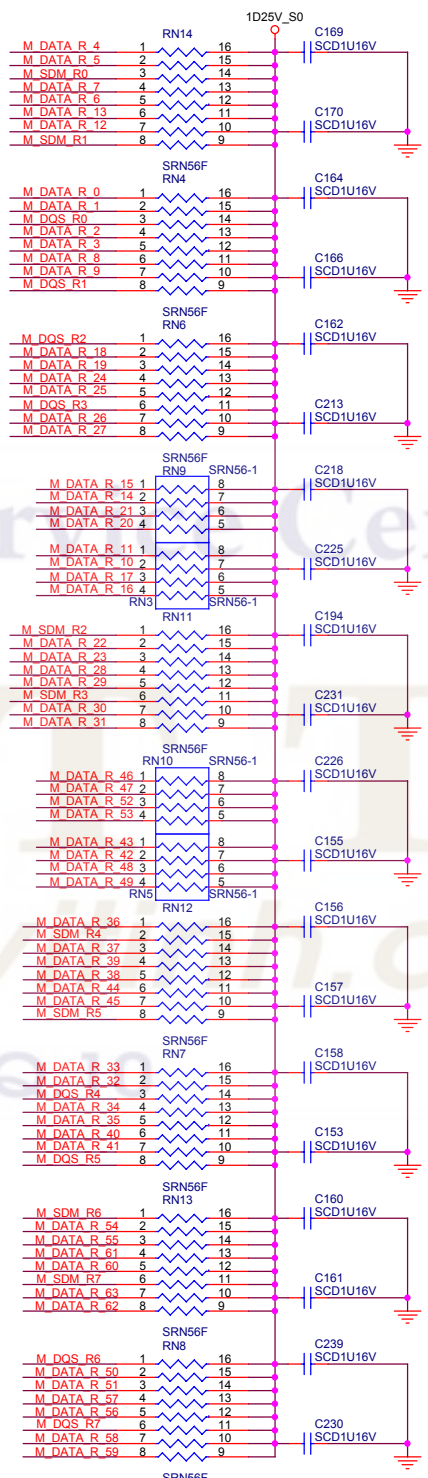
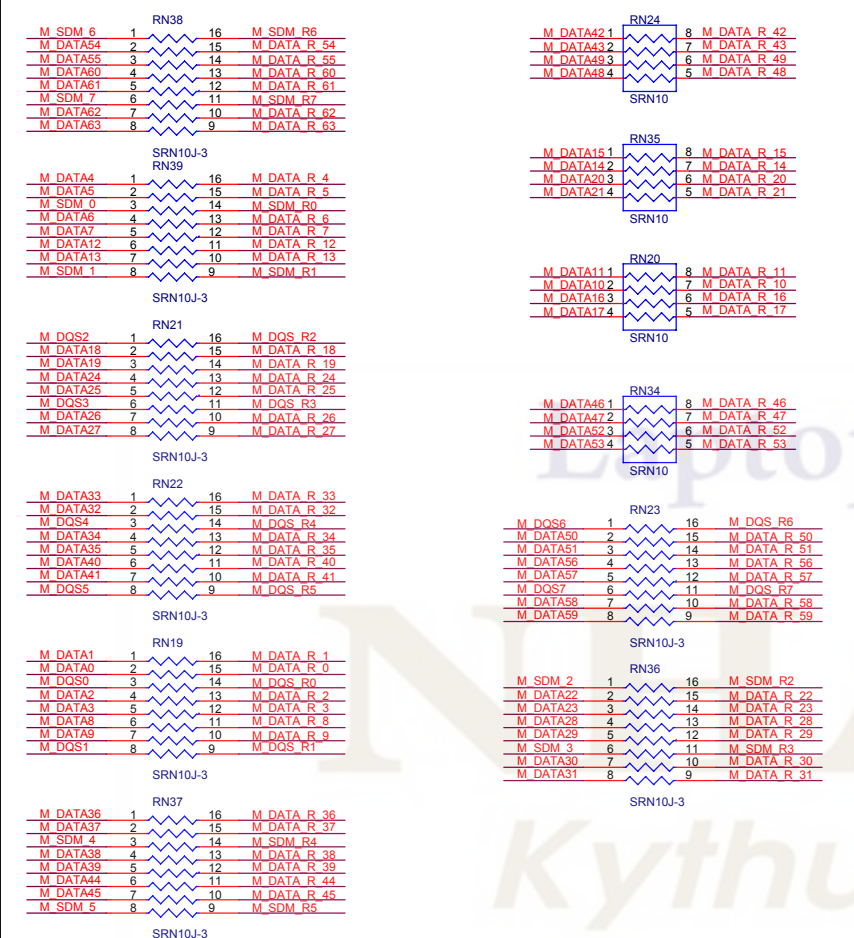
緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

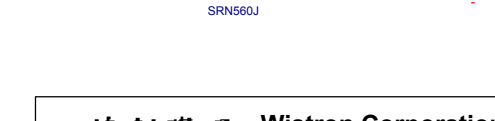
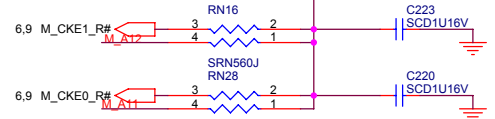
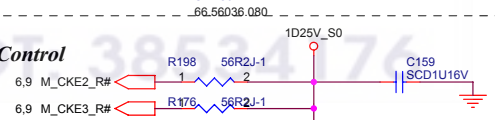
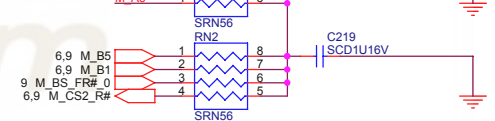
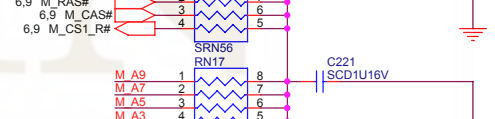
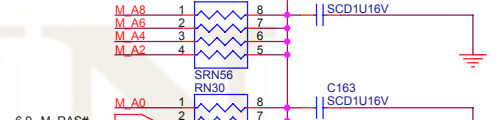
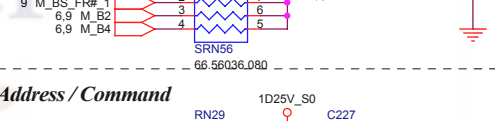
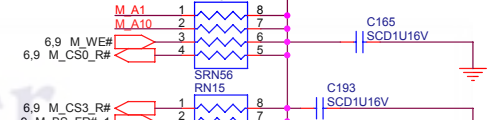
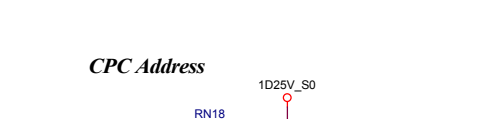
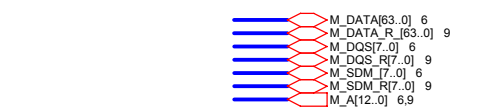
Title				
DDR Socket				
Size	Document Number			Rev
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SERIES DAMPING



SERIES DAMPING

PULL HIGH STUBS < 0.8", PLACE RPs CLOSE TO DM2
NO EQUAL LENGTH LIMITATION



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Title

DDR Serial/Terminator Resistor

Size

A3

Document Number

B2M

Rev

SC

Date: Thursday, January 16, 2003

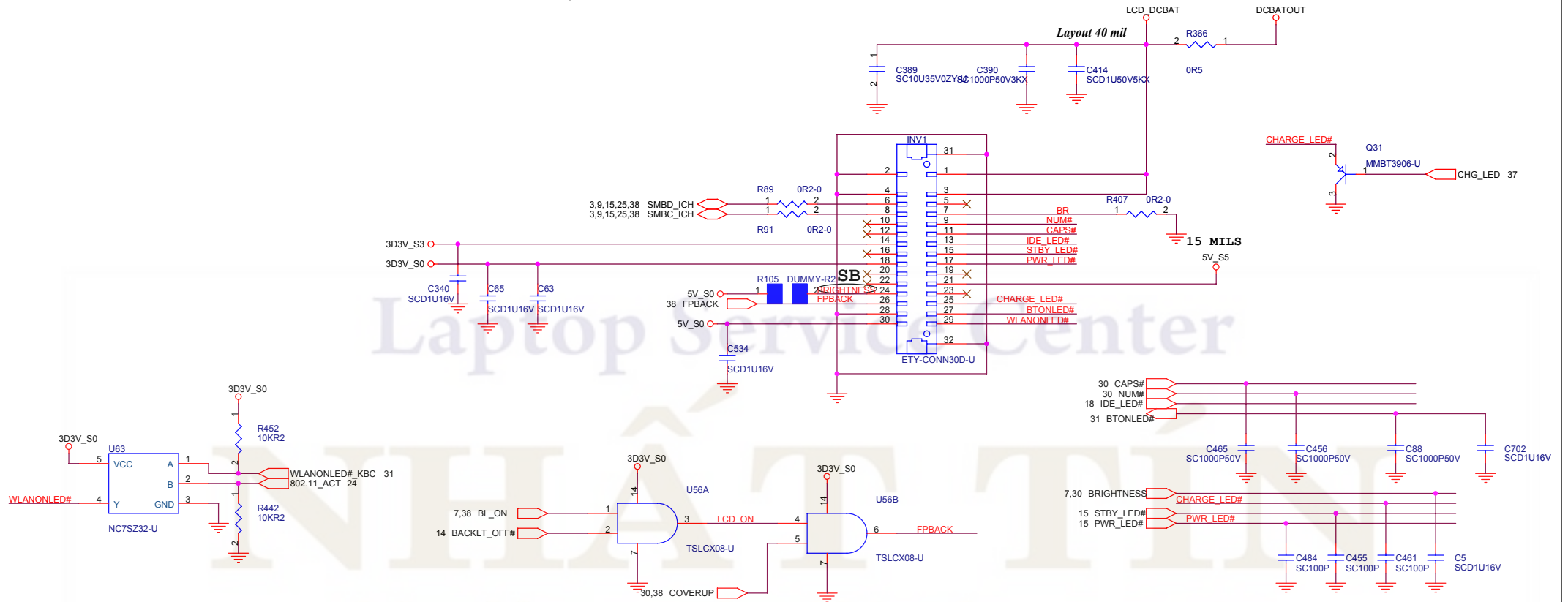
Sheet

10

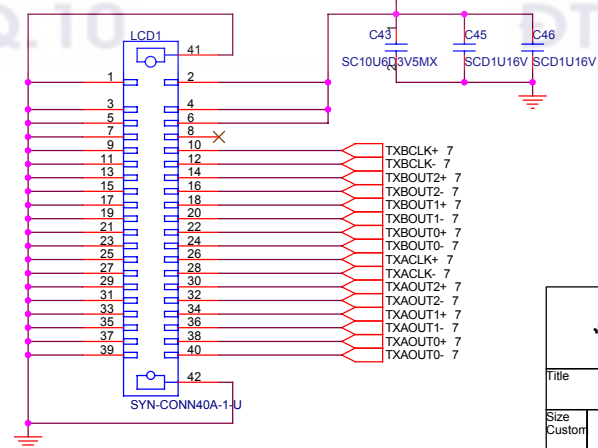
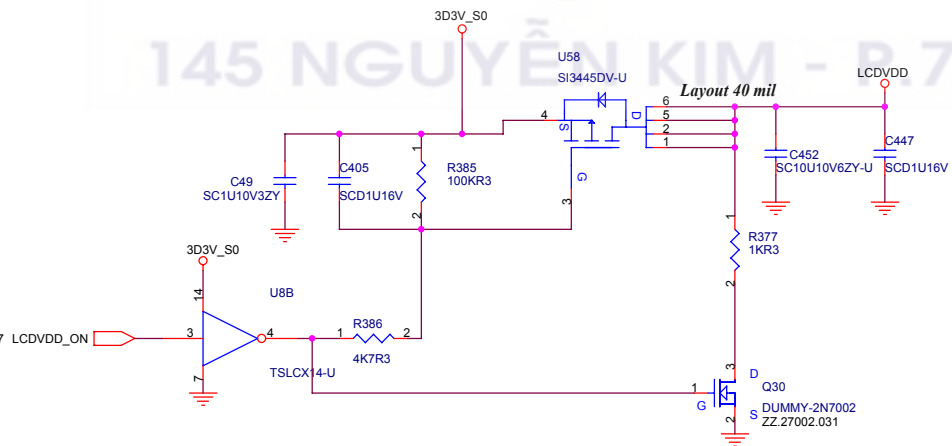
of

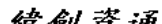
43

LCD / INVERTER INTERFACE

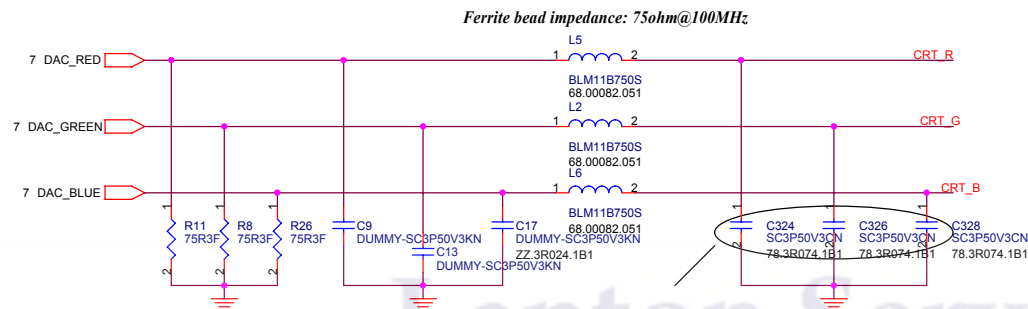


LCD CONN



 緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
LCD/Inverter Connector			
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CRT I/F & CONNECTOR

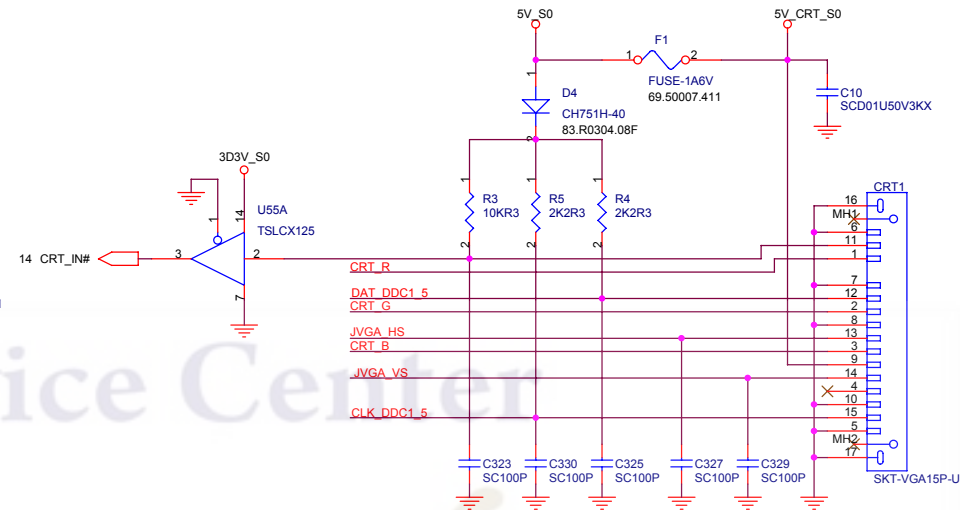


Layout Note:

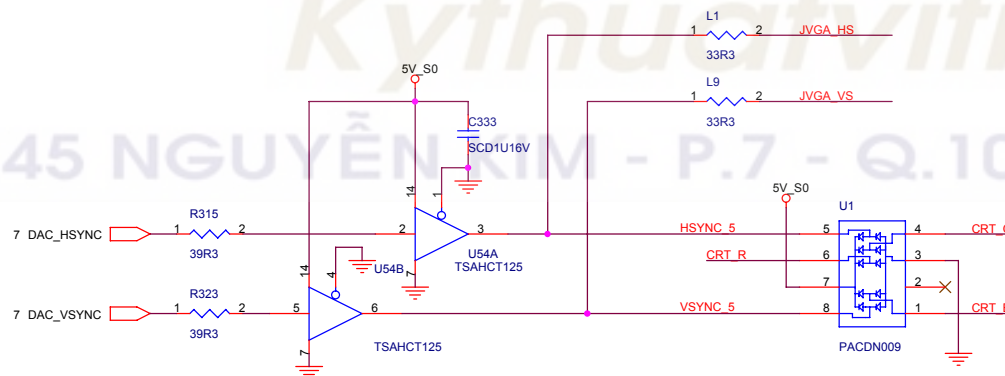
- * Must be a ground return path between this ground and the ground on the VGA connector.
- * 37.4 1% resistors must be placed at the same place as the RGB 75 Ohm pull-down resistors.

Pi-filter & 75 Ohm pull-down resistors should be as close as to CRT CONN. RGB will hit 75 Ohm first, pi-filter, then CRT CONN.

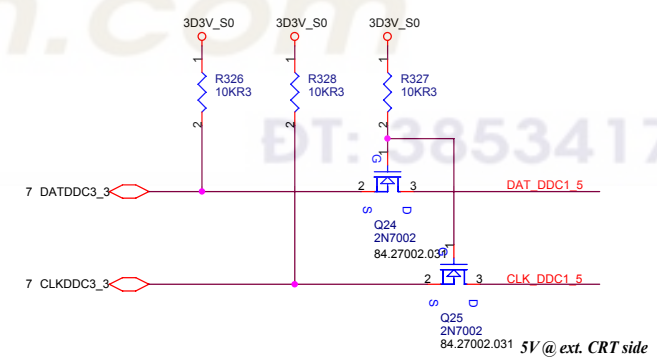
RDDP 1.0



Hsync & Vsync level shift

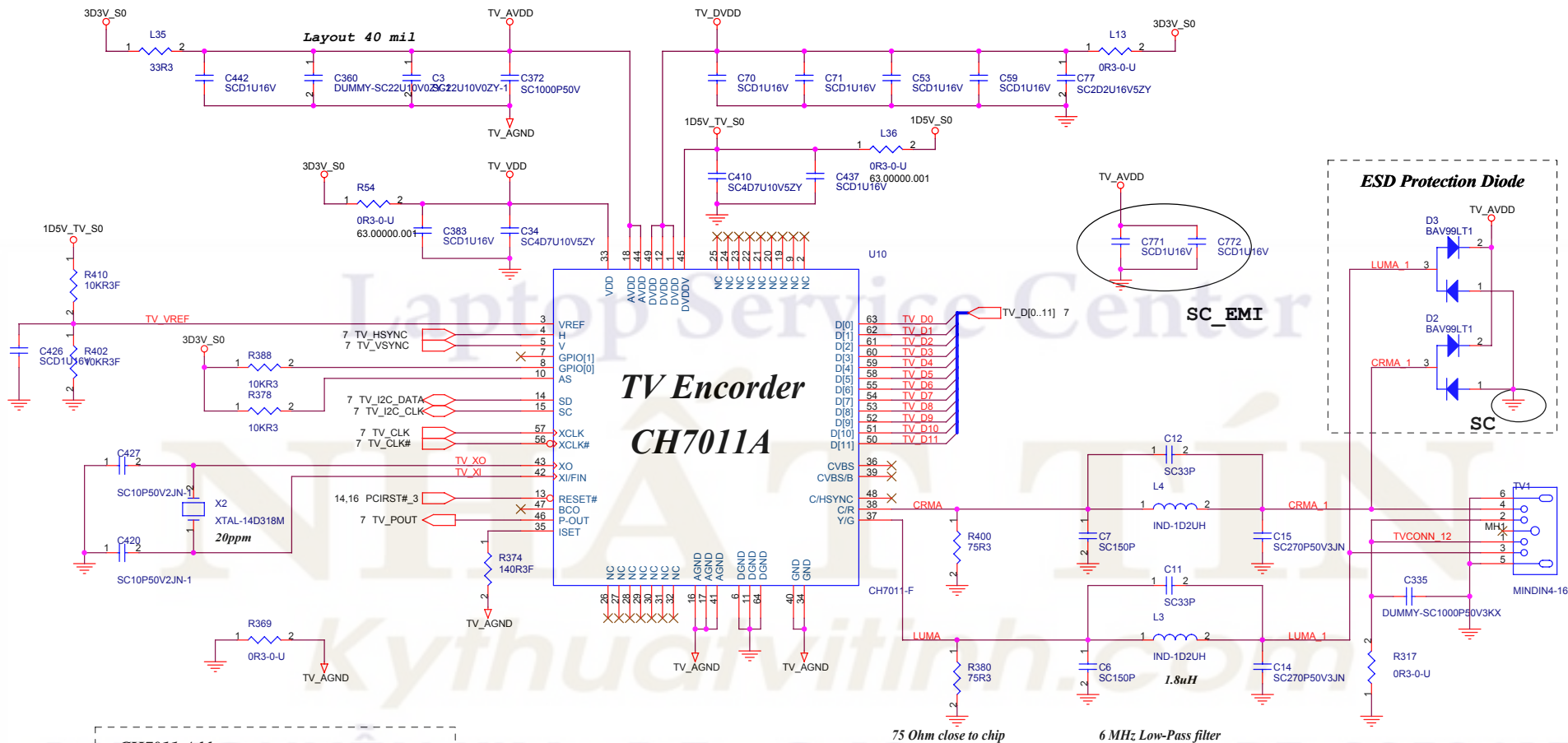


DDC_CLK & DATA level shift



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CRT Connector		
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CH7011 Addresss:

0X75	AS pull-up (int. pull-up)
0X76	AS pull-down

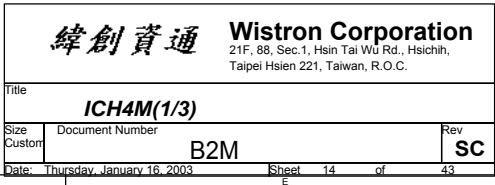
Power up default:

NTSC	GPIO0 pull-up (int. pull-up)
PAL	GPIO0 pull-down

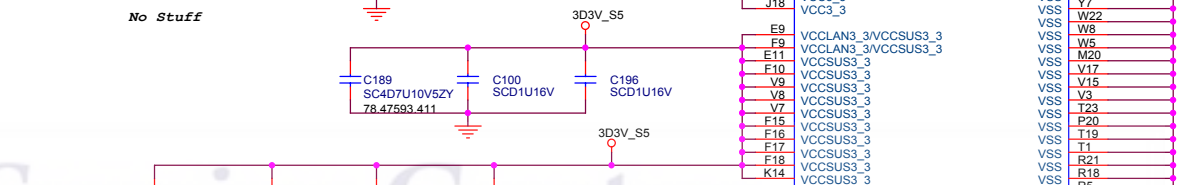
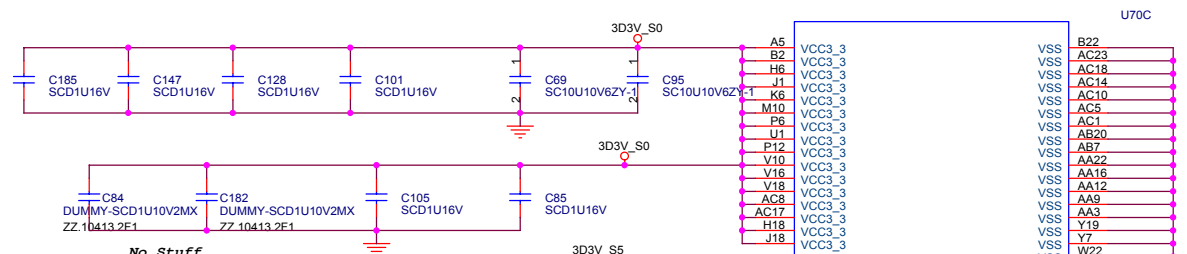
緯創資通

Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

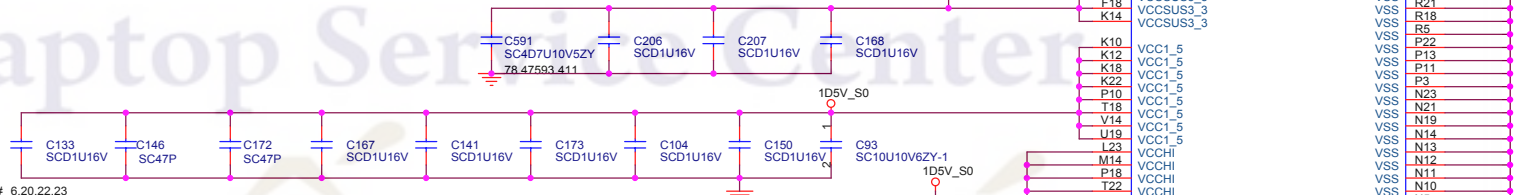
Title TV Encoder - Chronitel 7011A		
Size A3	Document Number B2M	Rev SC
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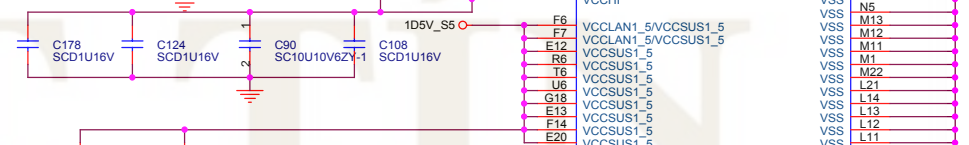
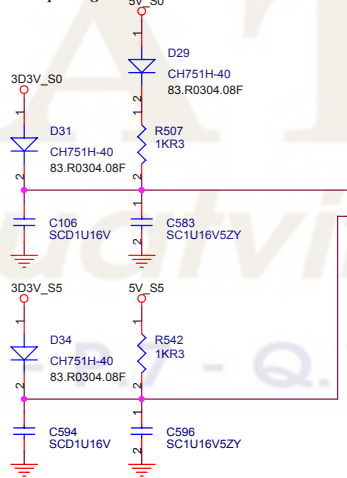




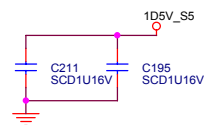
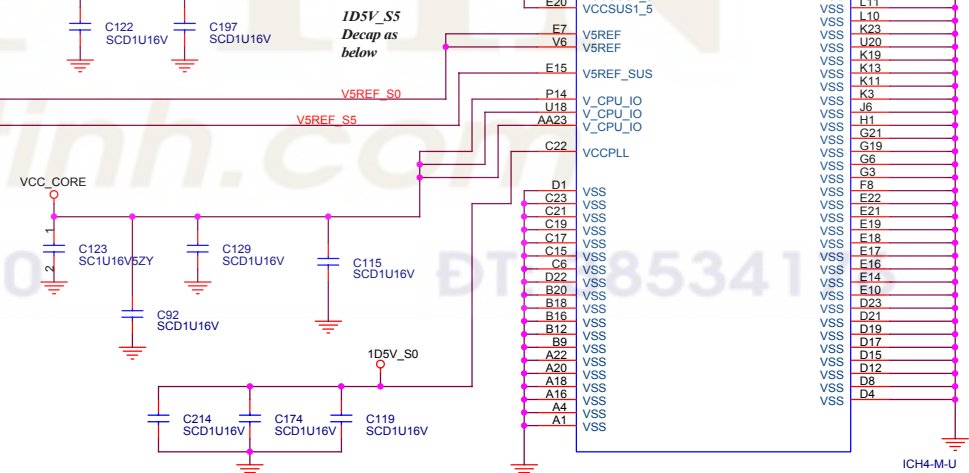
No Stuff



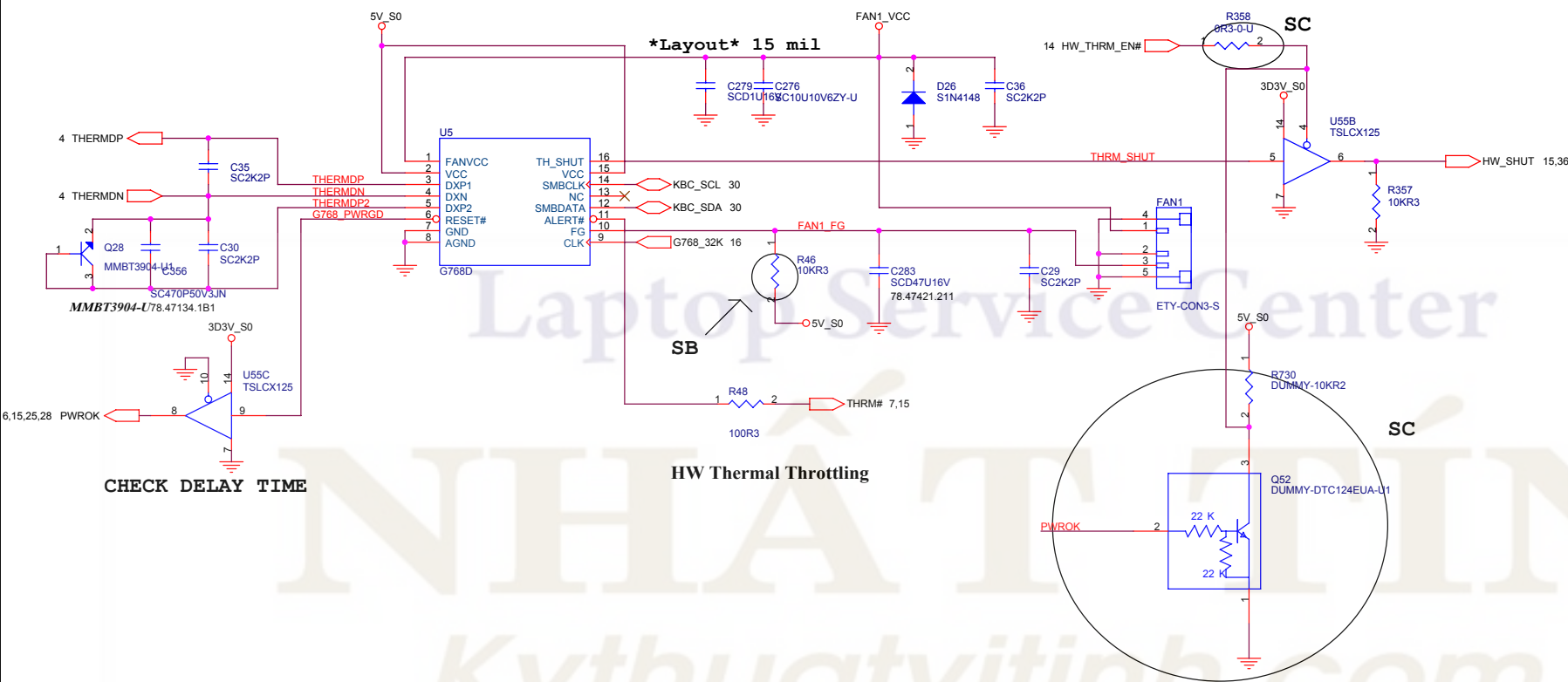
*Within a given well, 5VREF needs to be up before the corresponding 3.3V_{rail}.



*1D5V_S5
Decap as
below*

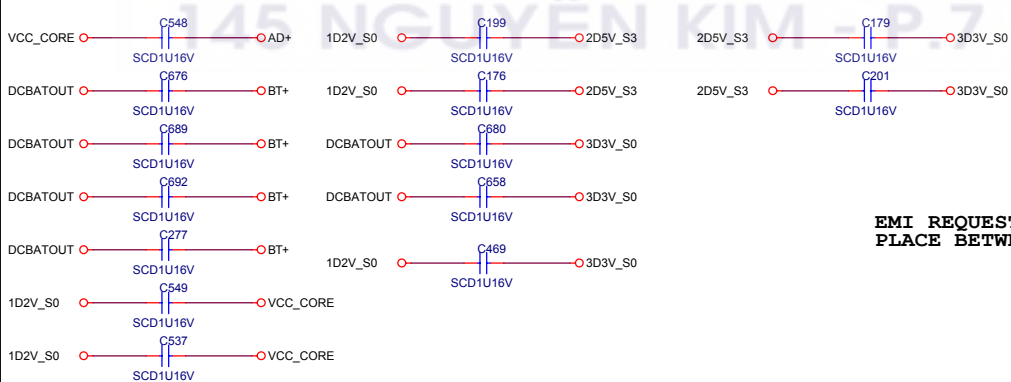


G768D thermal sensor & Fan controller



CHECK DELAY TIME

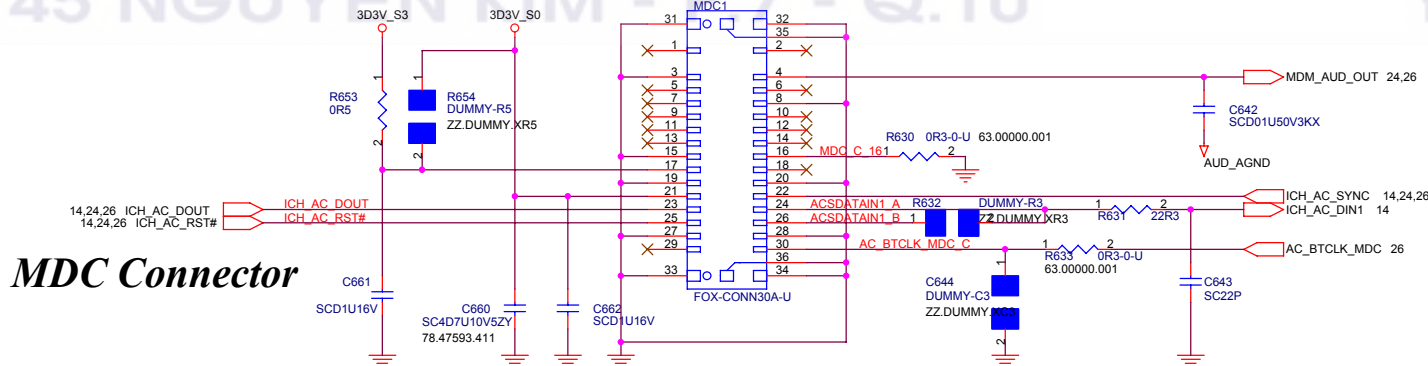
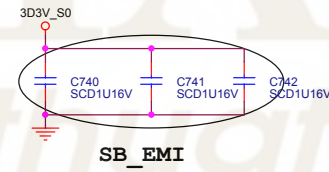
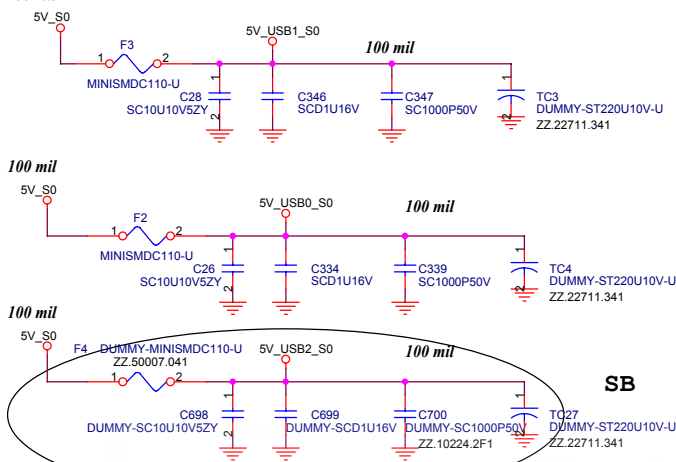
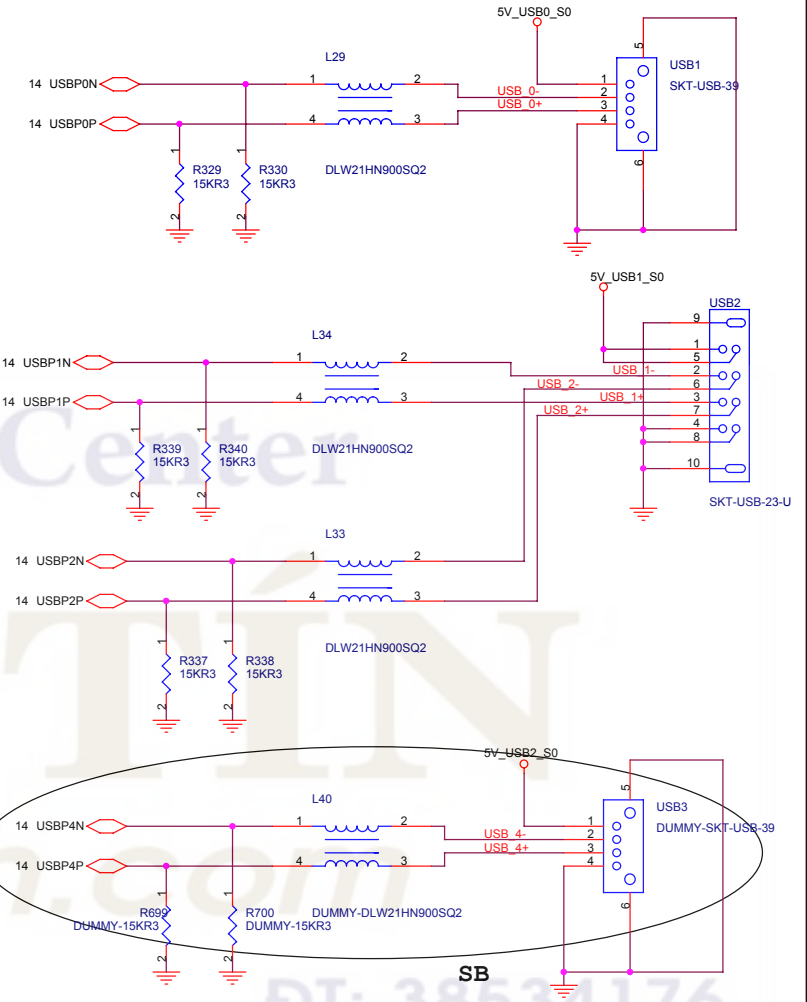
HW Thermal Throttling



EMI REQUEST,
PLACE BETWEEN DIFFERENT POWER PLANE.

Title		Wistron Corporation	
Size		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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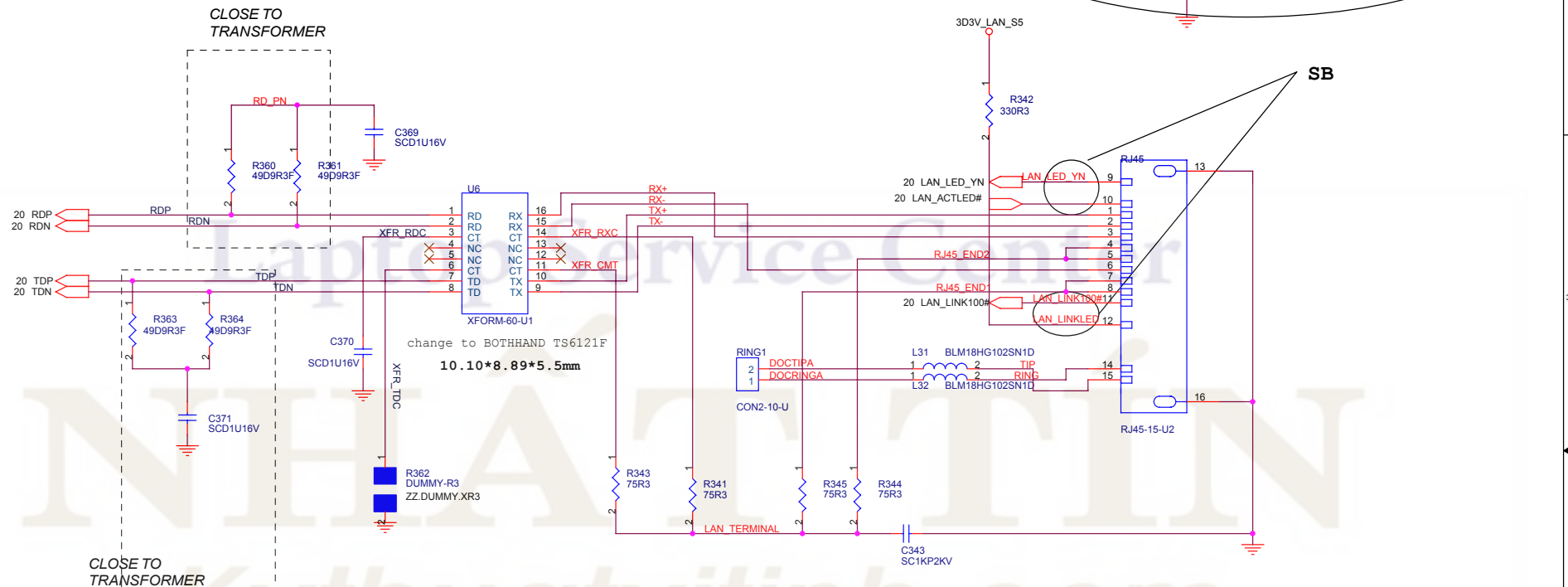
USB PORT



緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
USB and MDC I/F and LAN			
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LAN Connector



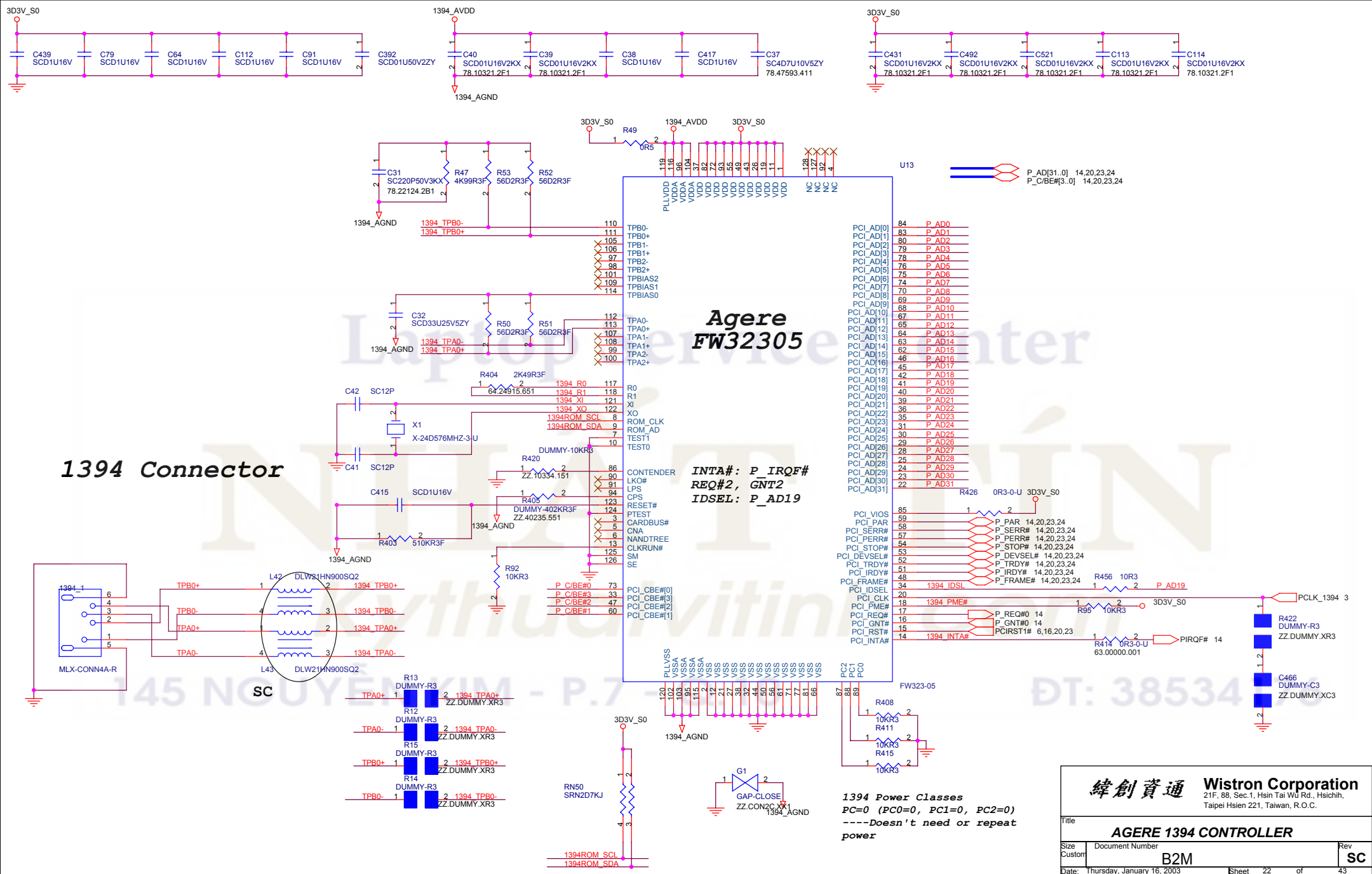
- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat, except RJ-45 moat.

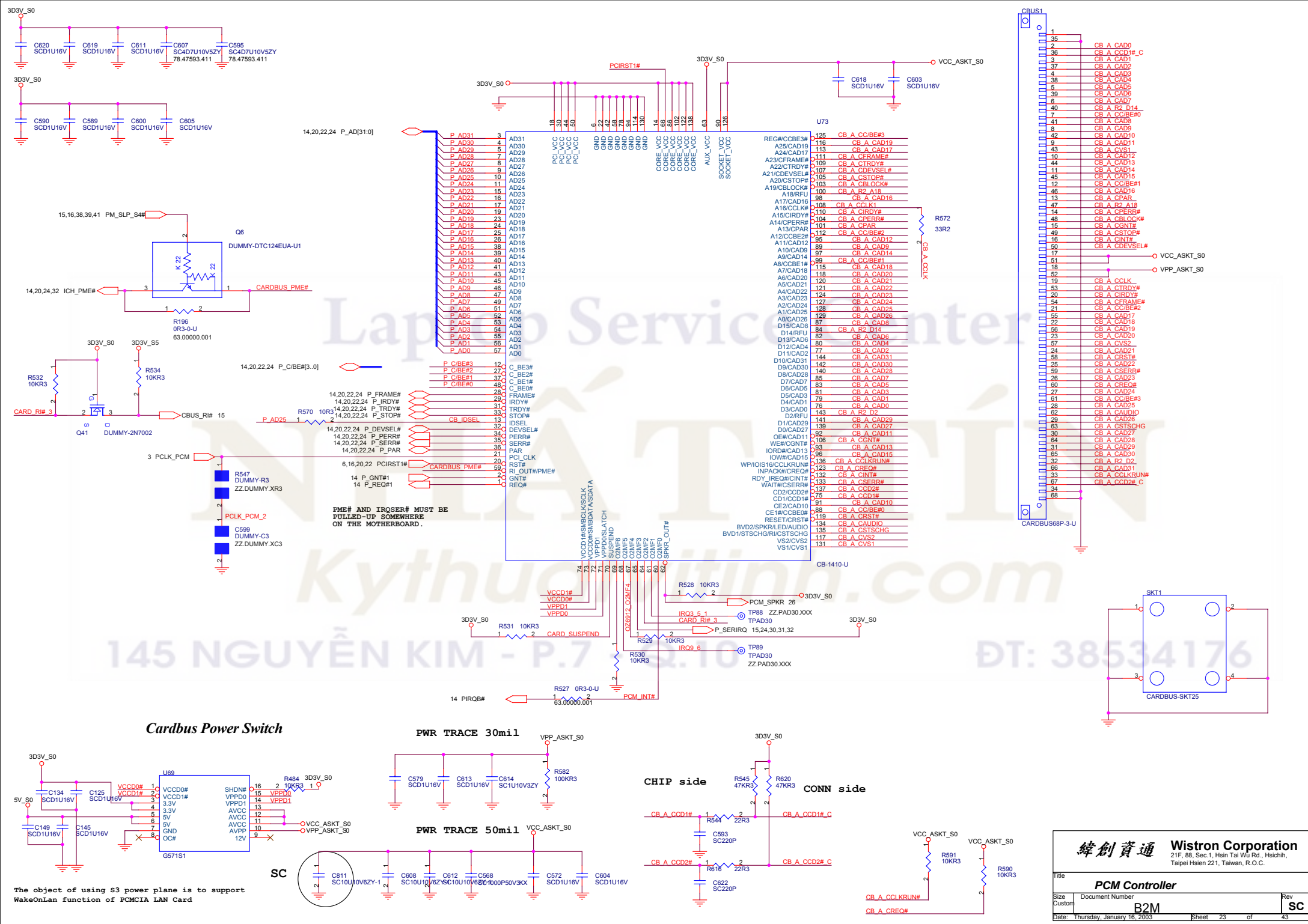
DOC_TIP,DOC_RING,TIP,RING:
W/S : 10/100 @ Surface layers
10/20 @ Inner layers

Green LED:Speed 100: ON/Speed 10:OFF
Yellow LED:Link : ON

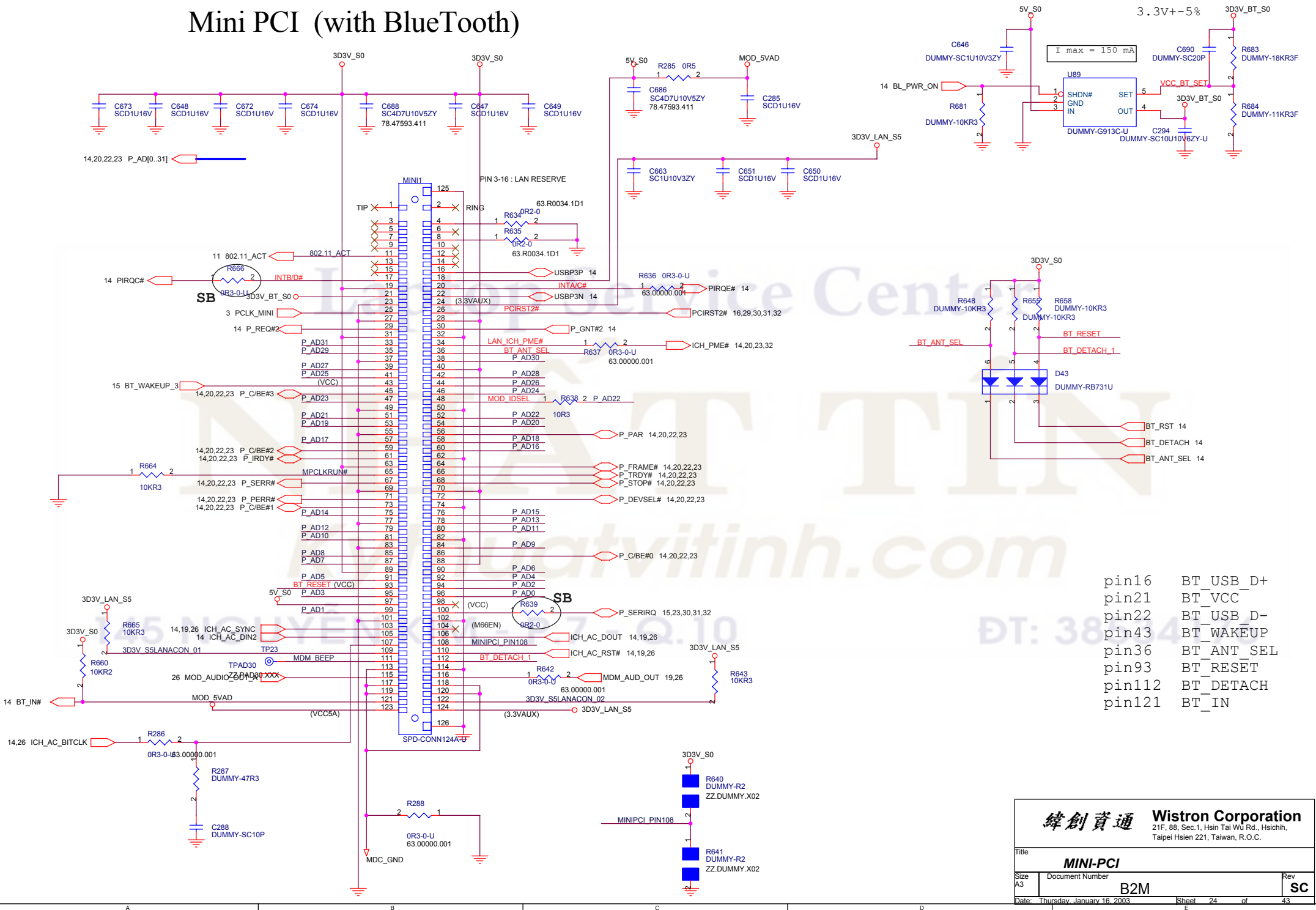
緯創資通 Wistron Corporation
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Taipai Hsien 221, Taiwan, R.O.C.

Title		
LAN Connector		
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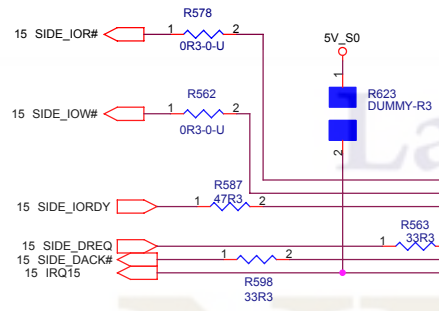
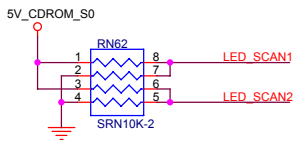
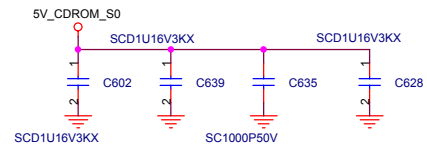
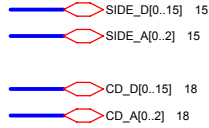




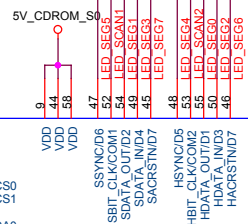
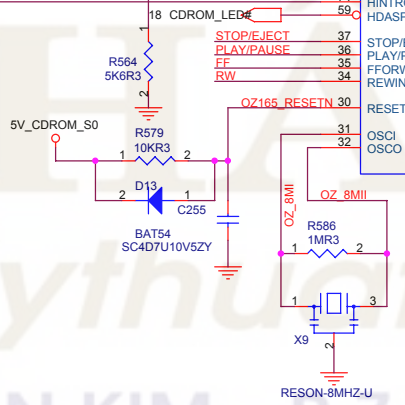
Mini PCI (with BlueTooth)



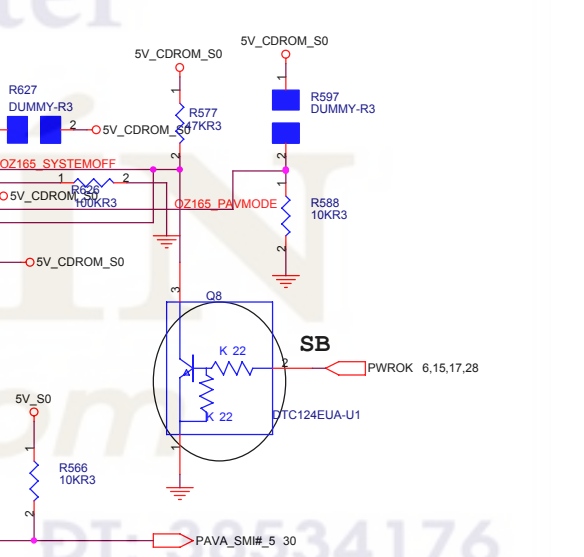
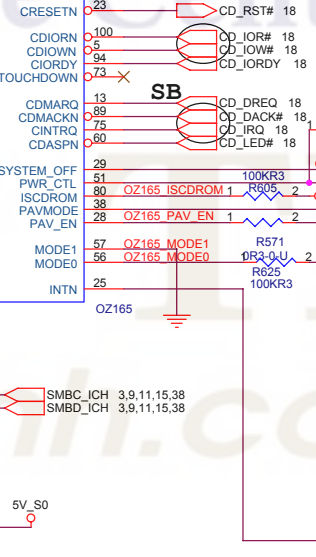
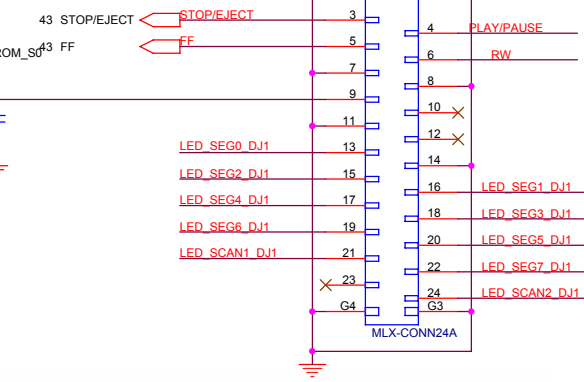
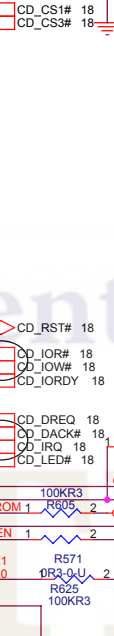
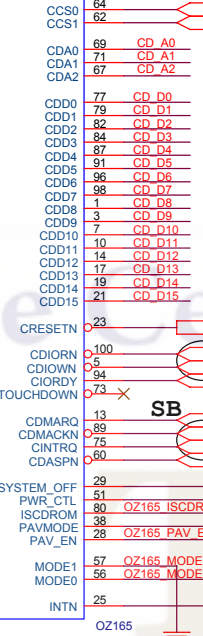
```
pin16    BT_USB_D+
pin21    BT_VCC
pin22    BT_USB_D-
pin43    BT_WAKEUP
pin36    BT_ANT_SEL
pin93    BT_RESET
pin112   BT_DETACH
pin121   BT_IN
```

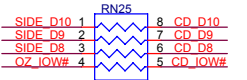
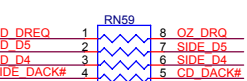
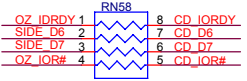
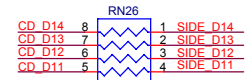
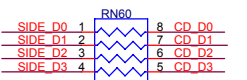
Always install
12/08



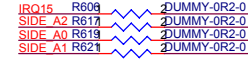
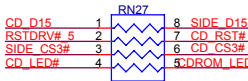
OZ165



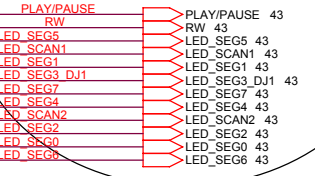
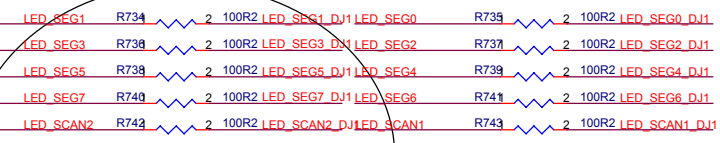
DEFAULT INSTALL



For Non-CDPLAY Model



SC



緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin, Taipei Hsien 221, Taiwan, R.O.C.

Title

CD Player Controller OZ165

Size

A3

Document Number

B2M

Rev

SC

Date

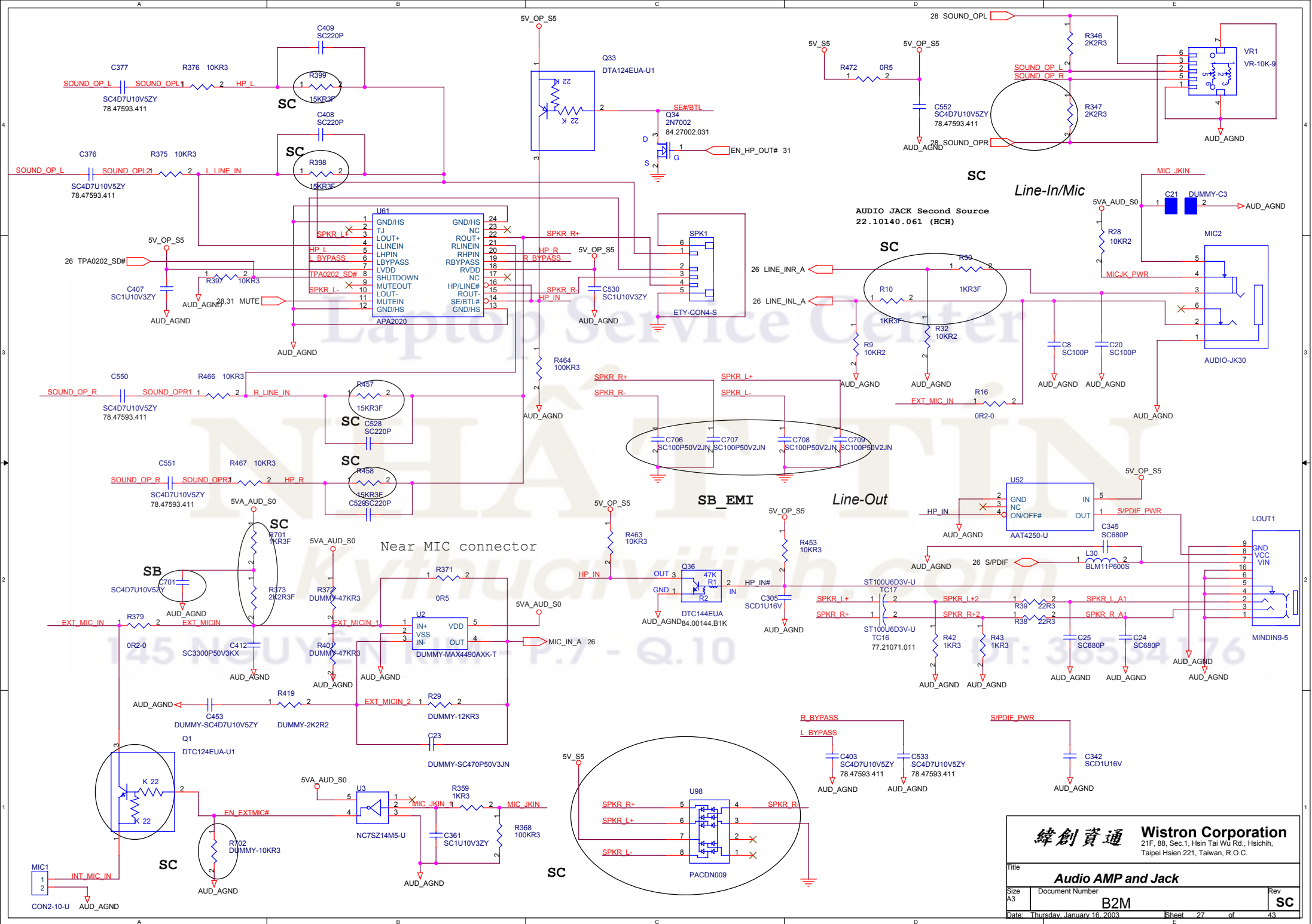
Thursday, January 16, 2003

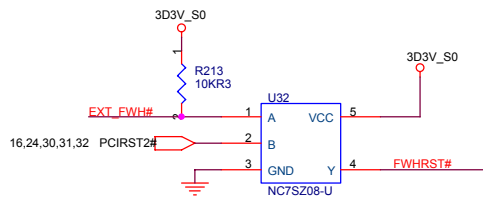
Sheet

25

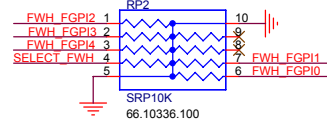
of

43



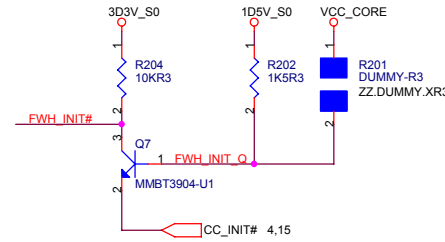


Unused FGPI pins must not be float



LPC_LAD[0..3] 15,30,31,32

CHANGE VCORE

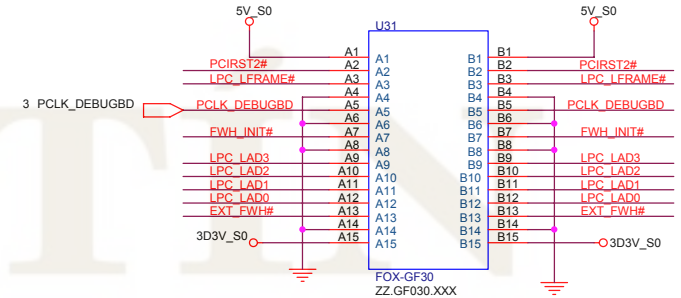


TOP VIEW

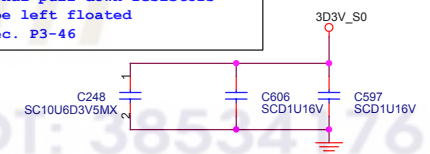
A15 (B1)
 A14 (B2)
 ...
 A2 (B14)
 A1 (B15)

(BOTTOM VIEW)

GOLDEN FINGER FOR DEBUG BOARD

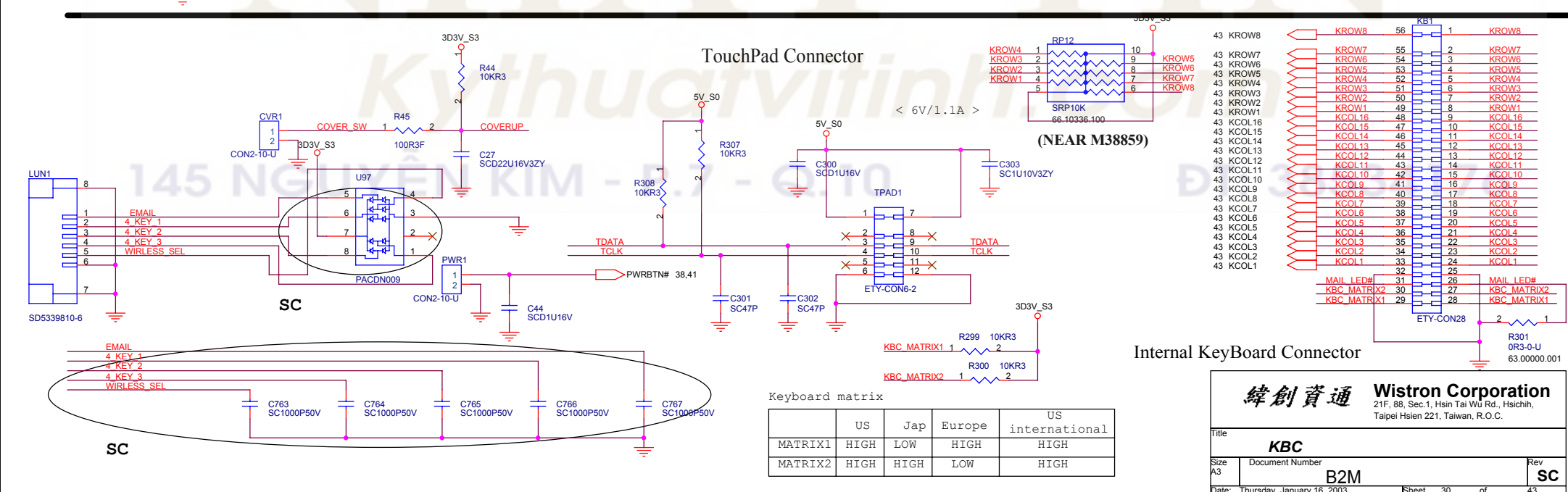
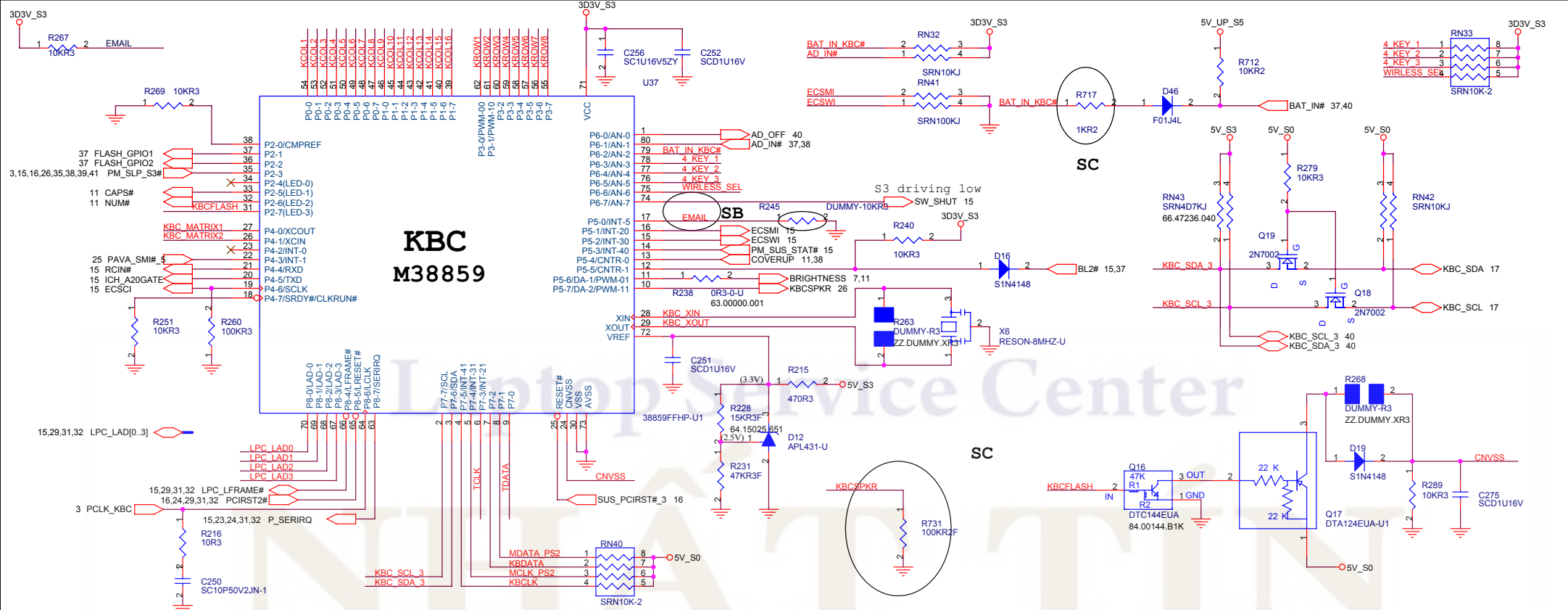


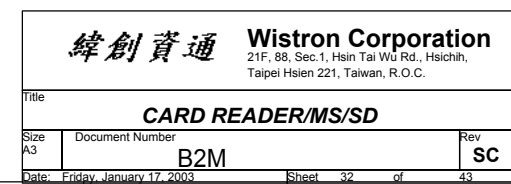
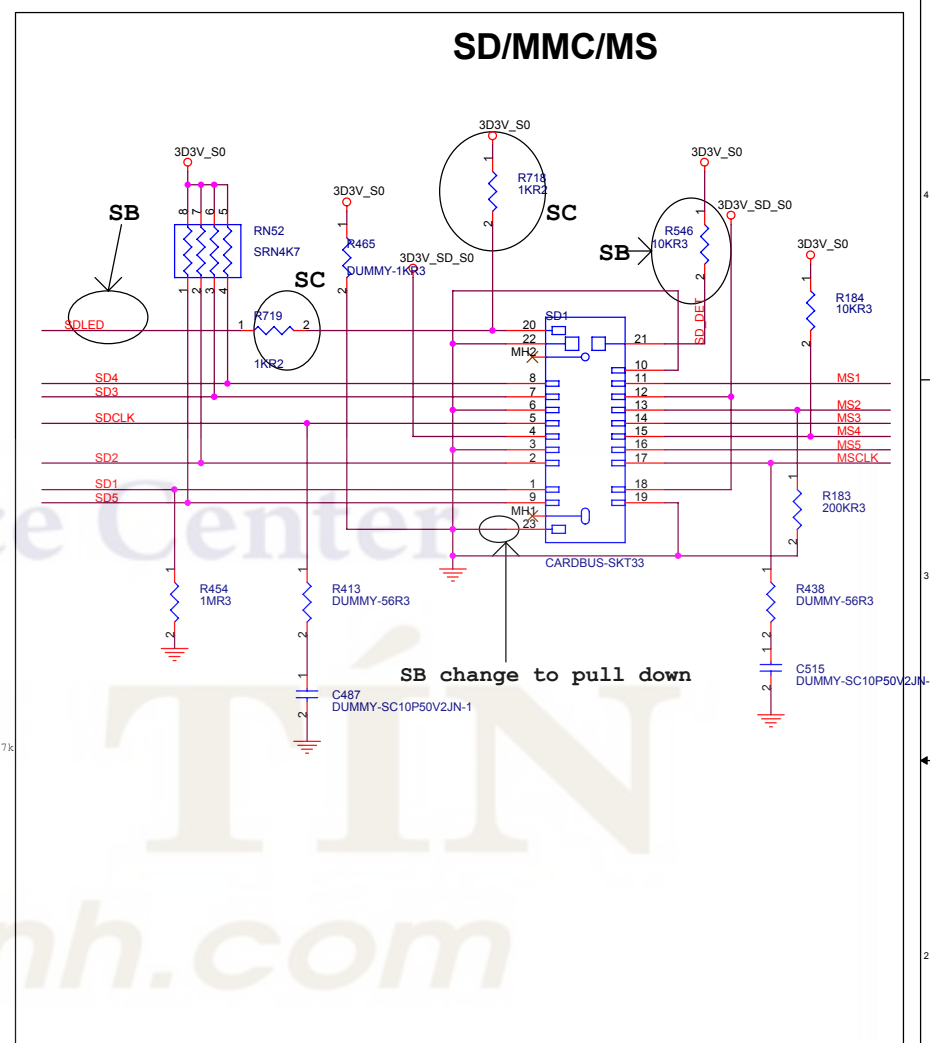
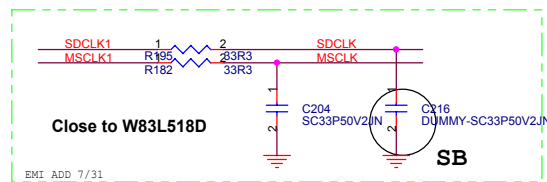
Boot Device must have ID[3:0] = 0000
 Has internal pull-down resistors
 All may be left floated
 FPET7 Elec. P3-46



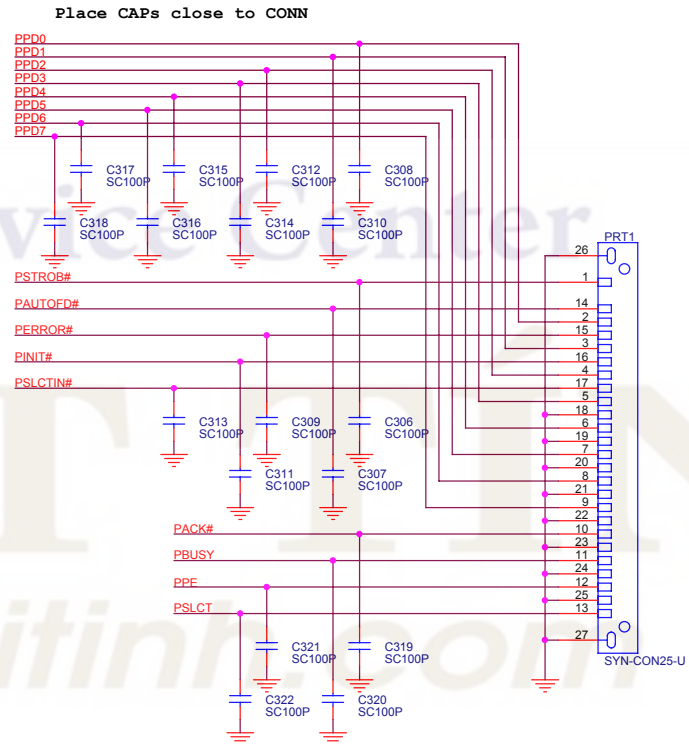
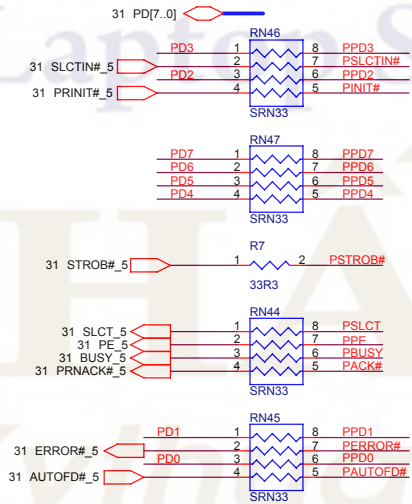
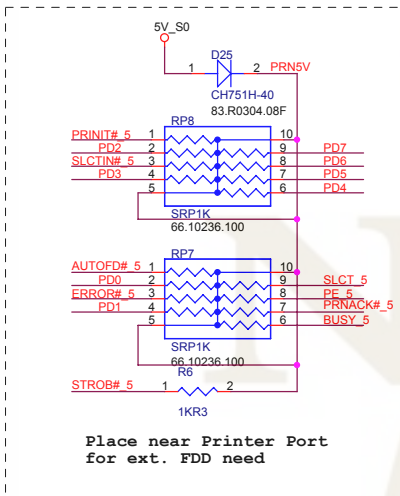
緯創資通 Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
 Taipei Hsien 221, Taiwan, R.O.C.

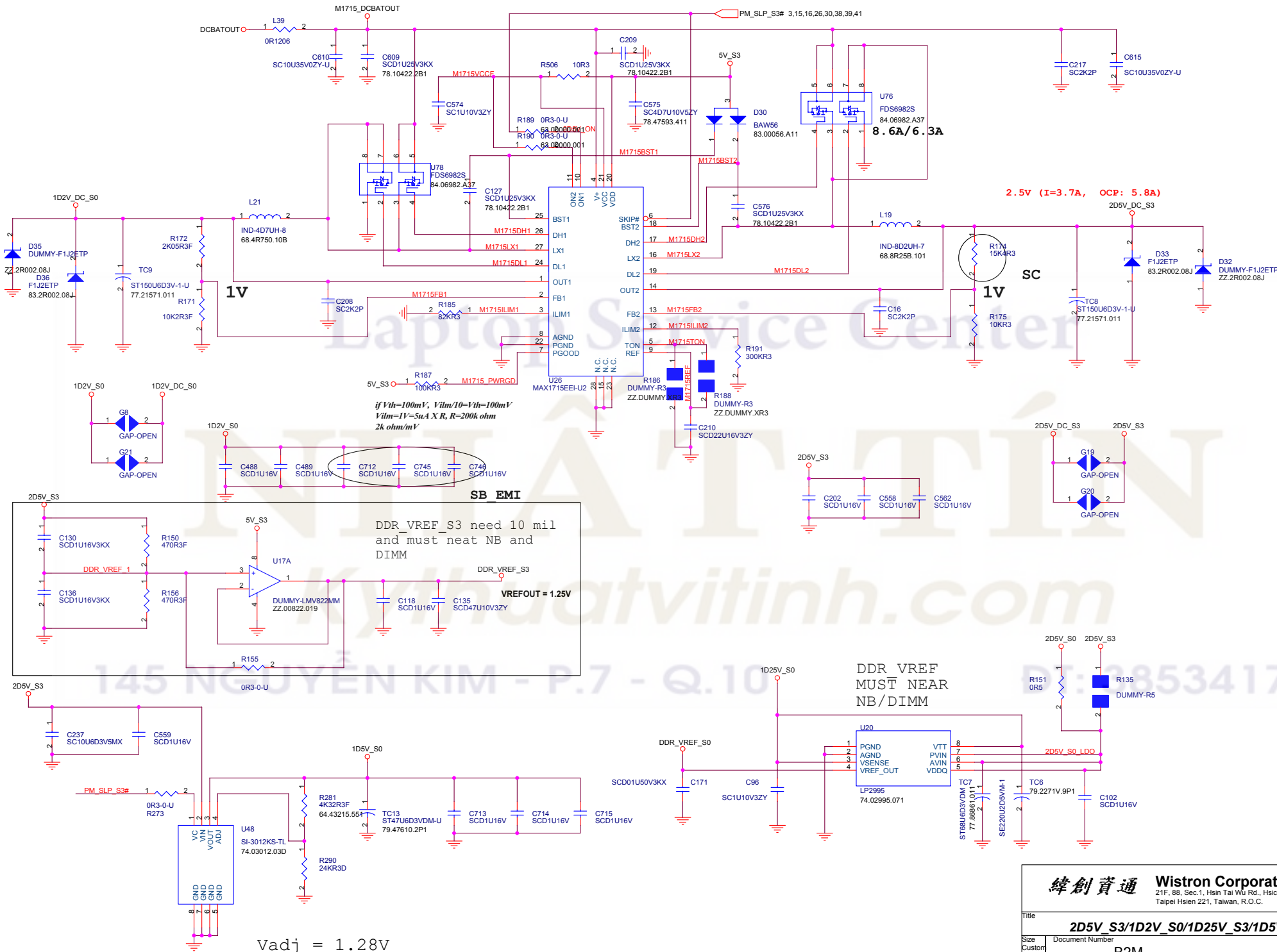
Title		
FWH and Debug		
Size	Document Number	Rev
A3	B2M	SC
Date:	Thursday, January 16, 2003	Sheet 29 of 43



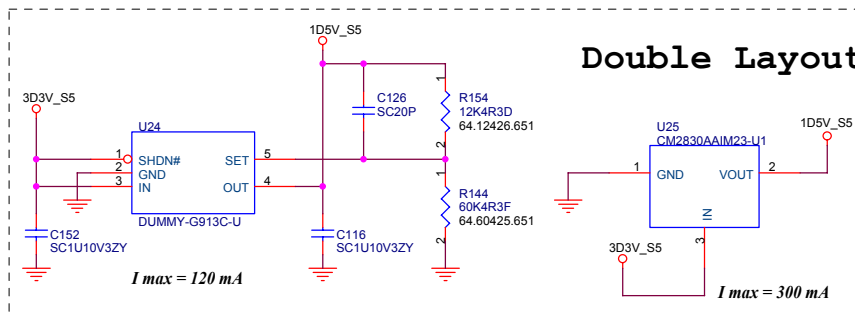
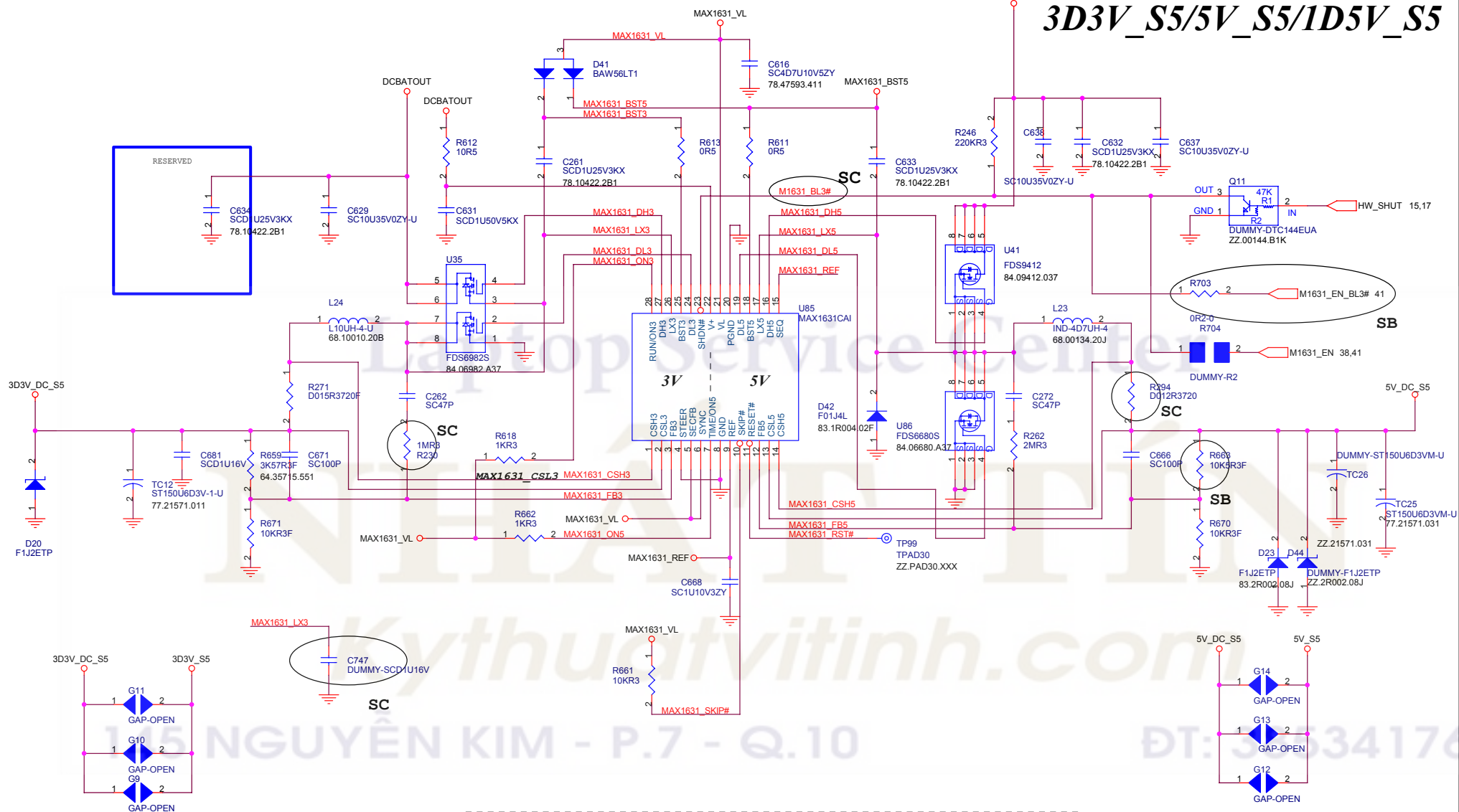



PRINTER PORT





SYSTEM DC/DC
3D3V_S5/5V_S5/1D5V_S5



 緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<i>UP+5V/1D5V_S5/3D3V_S5/5V_S5</i>			
Size	Document Number	Rev	
Custom	B2M	SC	
Date:	Thursday, January 16, 2003	Sheet	36 of 43

70W
R1469=100K, R1468=140K, R1465=75K
Imax=3.49A
AD POWER=19*3.49=66.31W

DCBATOUT

L22
0R1206

Rating 4A

C716
SCD1U

C717
SCD1U

S718
SCD1U

2

SB_EMI

1645_AD+_1 39

M1645_DH 39

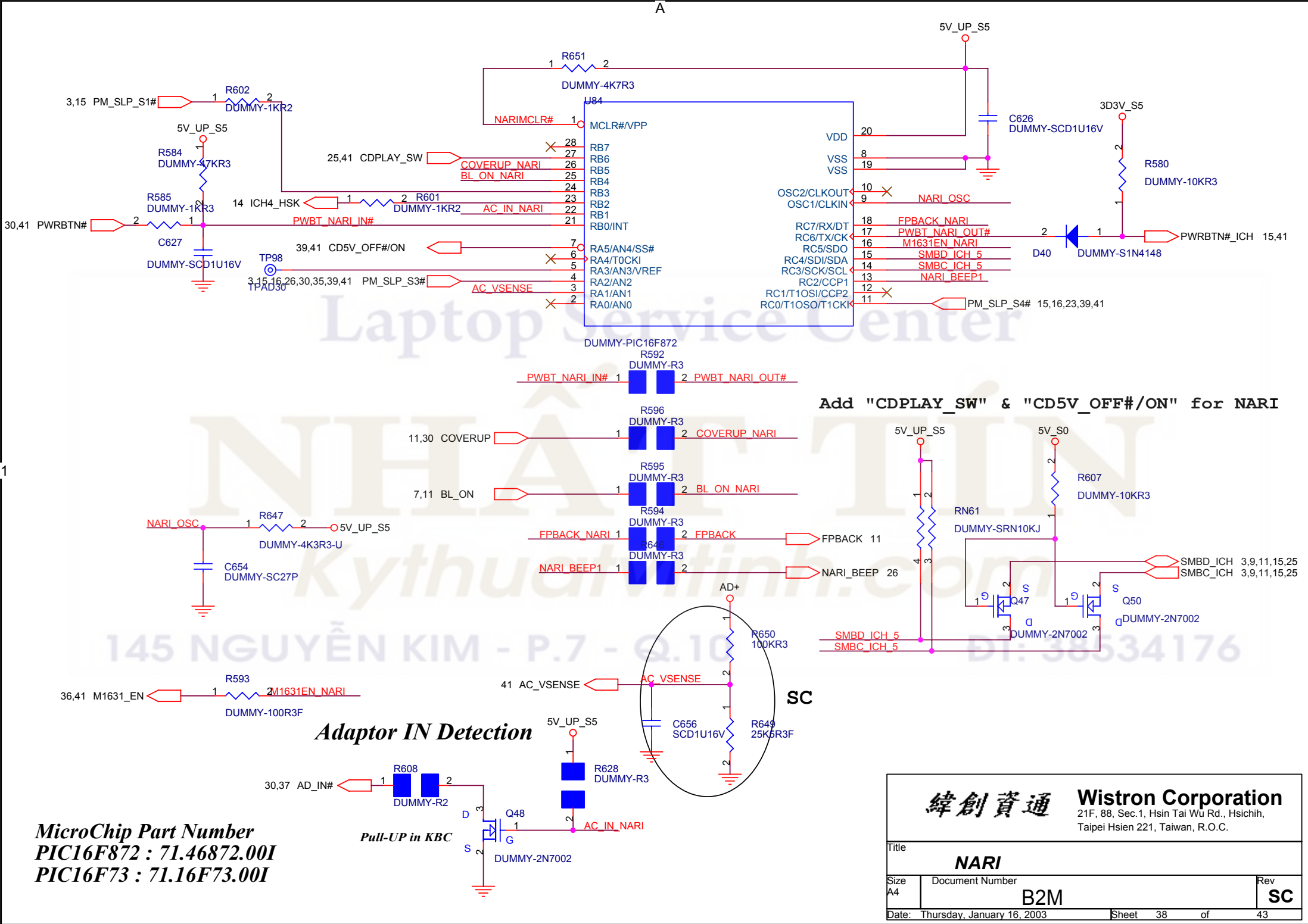
notice sense resistor noise and trace

CHANGE TO 33U

ATTINY12L-4SI(FLASH)
REPLACE PIC12C509

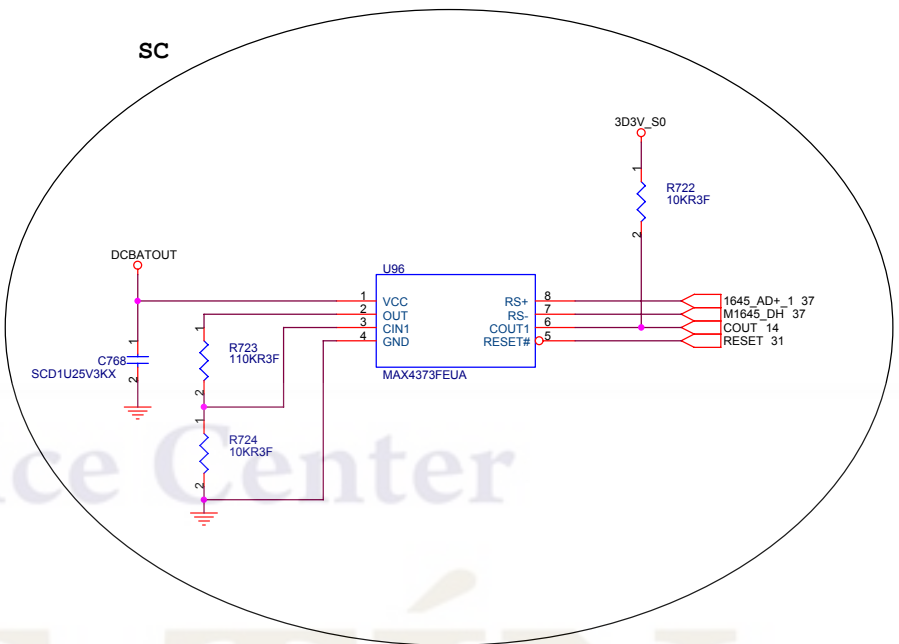
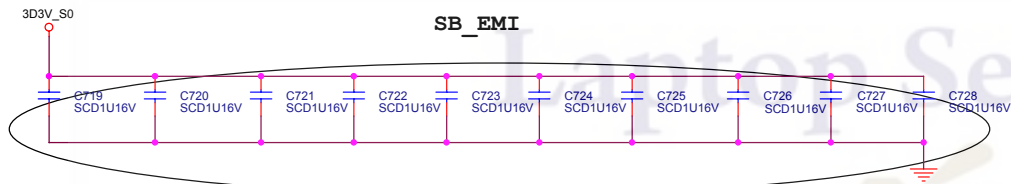
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
CHARGER&MicroP			
Size Custom	Document Number	Rev	
	B2M	SC	
Date: Friday, January 17, 2003	Sheet 37	of	43

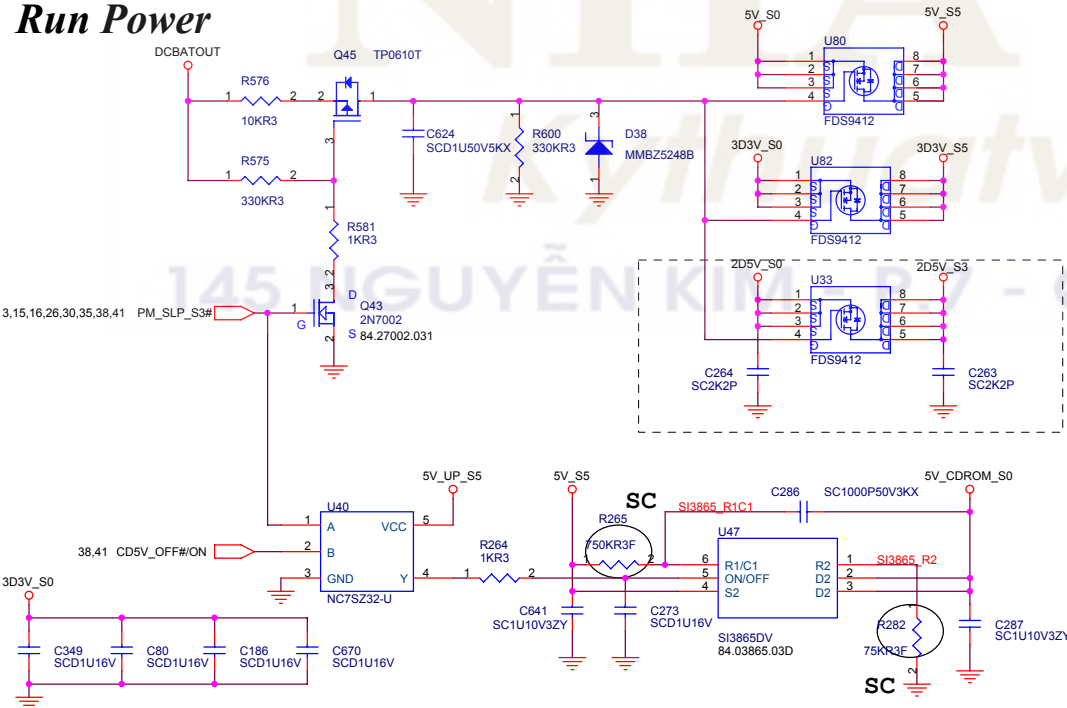


CPU VID(1D2V)

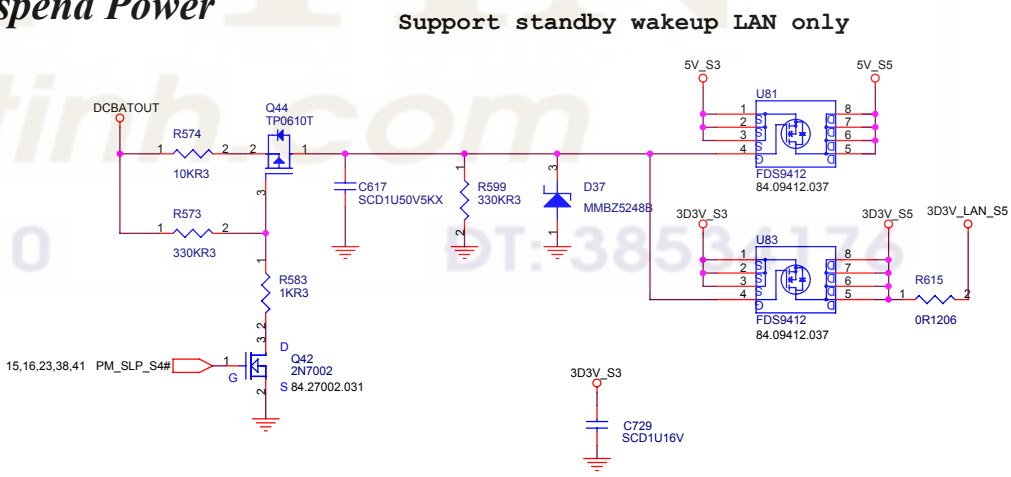
SPEC. FOR DT NORTHWOOD CPU =>150mA
FOR MB NORTHWOOD CPU =>300mA



Run Power



Suspend Power



Layout
150mil

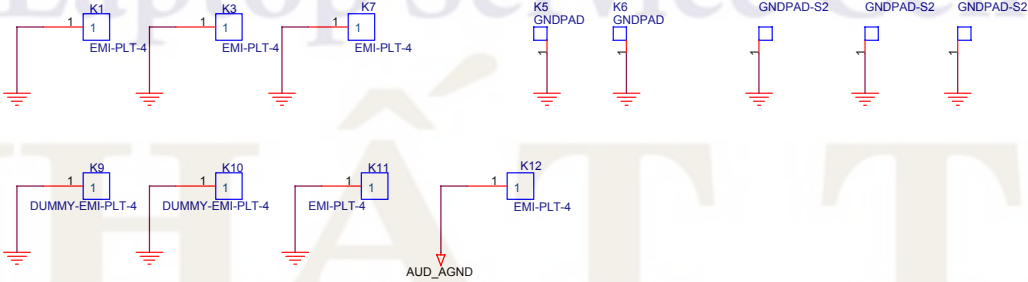


Title			
AD/BATT CONN			
Size A3	Document Number		Rev
	B2M		SO
Date:	Thursday, January 16, 2003	Sheet 40 of	43

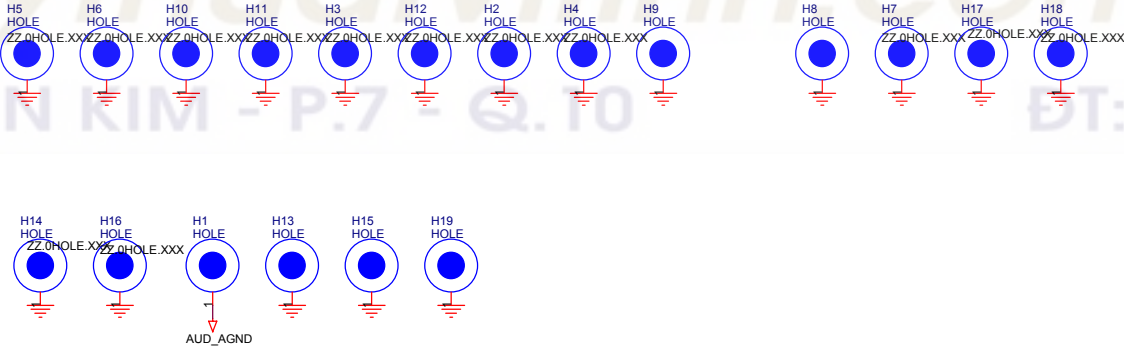
NO USE LOGIC

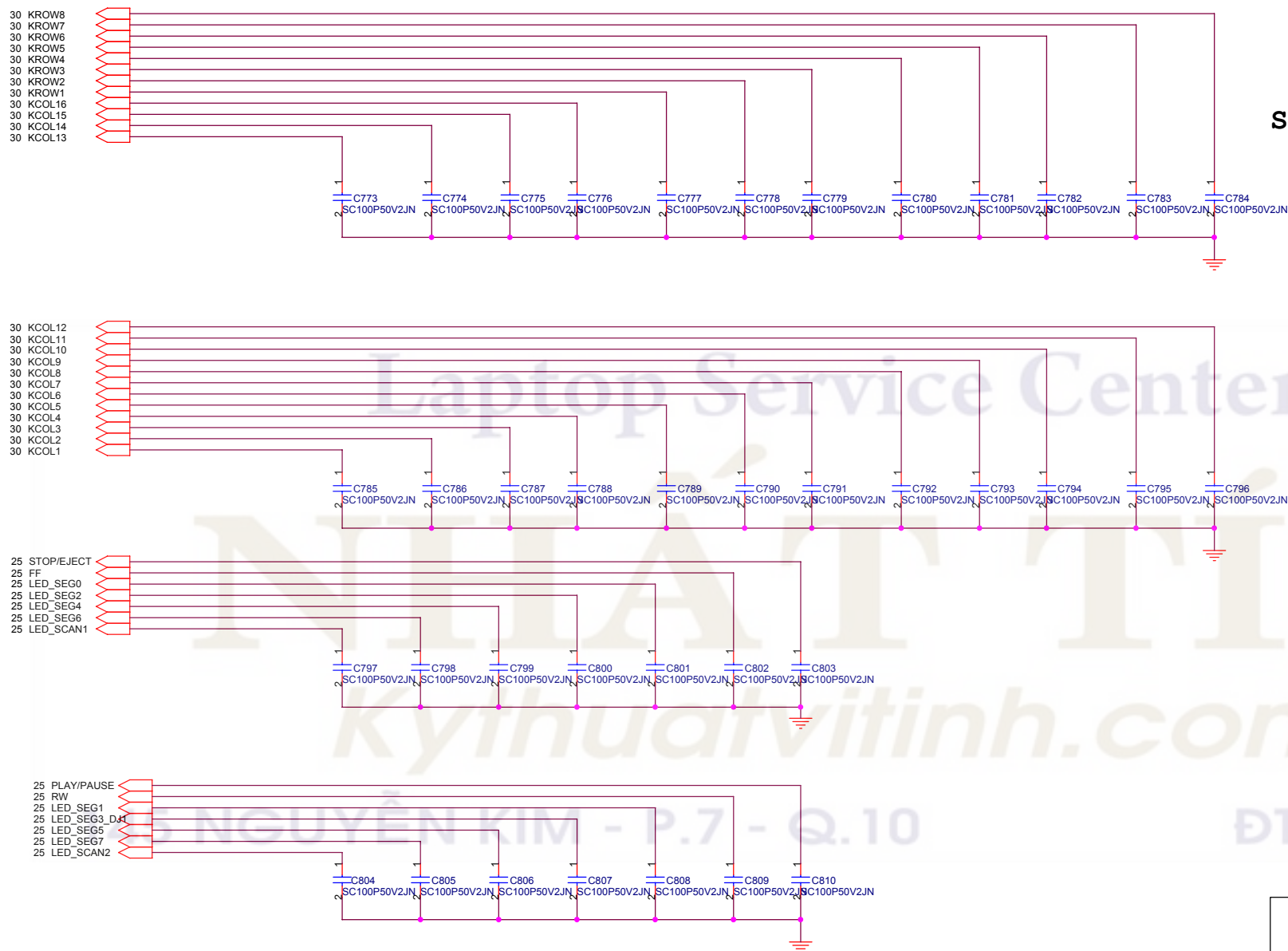


Laptop Service Center



SC





SC_EMI

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
TEST POINT			
Size	Document Number		Rev
Custom	B2M		SC
Date:	Thursday, January 16, 2003	Sheet 43 of	43