



## 12-bit 100 Msps Dual Analog-to-Digital Converter IP

### FEATURES

- UMC 65nm 1P5M CMOS
- 12-bit Dynamic Range
- Up to 100 Msps Conversion Rate
- Single 1.2V Power Supply
- Ultra Low Power Consumption  
20 mW @ 100 Msps  
14 mW @ 50 Msps
- 1.0 V<sub>p-p</sub> Differential Input
- Excellent Dynamic Performance  
68 dBfs Dynamic Range  
64 dBc SINAD at F<sub>IN</sub> = 10 MHz
- Analog Input Bandwidth > 200MHz
- Dynamic Power Scaling
- Power Saving Idle Modes
- Internal Voltage Reference
- 0.31 mm<sup>2</sup> Core Area

### Applications

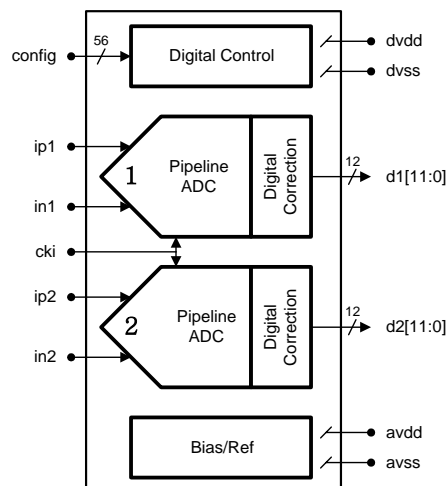
- Communication Receive Channel  
WiFi / WiMAX / DVB
- Digital Imaging / Video
- TV / Video / Radio Decoders
- Graphic Capture

### Supported Foundries

- UMC, IBM, TSMC

### About IQ-Analog

IQ-Analog is a privately held company with headquarters in San Diego focusing on the development of high performance analog interface components.



### General Description

The ADC uses a fully differential pipeline architecture with proprietary digital correction techniques to achieve both the lowest power consumption and the smallest floorplan in its class.

The core is fully self contained with integrated biasing and voltage references which operate without external components.

Dynamic biasing dependent on the clock rate is provided to achieve optimal efficiency for multiple clock rates.

For more information about this product, please contact us at  
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