

## Pinout Report - Sat Mar 15 14:19:55 2014

Pin Number	Signal Name	Pin Usage	Pin Name	Direction	IO Standard	IO Bank Number	Drive (mA)	Slew Rate	Termination	IOB Delay	Voltage	Constraint	IO Register	Signal Integrity
A1			GND											
A2			TDI											
A3		IBUF	IP	UNUSED		0								
A4		DIFFM	IO_L24P_0	UNUSED		0								
A5		DIFFM	IP_L22P_0	UNUSED		0								
A6		DIFFS	IO_L20N_0	UNUSED		0								
A7		IBUF	IP	UNUSED		0								
A8		IOB	IO	UNUSED		0								
A9			VCCO_0			0					2.50			
A10		DIFFS	IO_L12N_0/GCLK7	UNUSED		0								
A11		IOB	IO	UNUSED		0								
A12			NC											
A13		DIFFM	IO_L05P_0	UNUSED		0								
A14		DIFFS	IO_L04N_0	UNUSED		0								
A15		DIFFSI	IP_L02N_0	UNUSED		0								
A16		DIFFS	IO_L01N_0	UNUSED		0								
A17			TCK											
A18			GND											
B1			PROG_B											
B2			GND											
B3		DIFFS	IO_L25N_0/HSWAP	UNUSED		0								
B4		DIFFS	IO_L24N_0	UNUSED		0								
B5		DIFFSI	IP_L22N_0	UNUSED		0								
B6		DIFFM	IO_L20P_0	UNUSED		0								
B7			VCCAUX								2.5			
B8		DIFFM	IP_L13P_0/GCLK8	UNUSED		0								
B9	clock	IBUF	IP_L13N_0/GCLK9	INPUT	LVC MOS25*	0				NONE		UNLOCATED	NO	NONE
B10		DIFFM	IO_L12P_0/GCLK6	UNUSED		0								
B11		IOB	IO/VREF_0	UNUSED		0								
B12			VCCAUX								2.5			
B13		DIFFS	IO_L05N_0/VREF_0	UNUSED		0								
B14		DIFFM	IO_L04P_0	UNUSED		0								
B15		DIFFM	IP_L02P_0	UNUSED		0								
B16		DIFFM	IO_L01P_0	UNUSED		0								
B17			GND											
B18		IBUF	IP	UNUSED		1								
C1	imginput<6>	IBUF	IO_L01P_3	INPUT	LVC MOS25*	3				NONE		UNLOCATED	NO	NONE
C2	imginput<0>	IBUF	IO_L01N_3	INPUT	LVC MOS25*	3				NONE		UNLOCATED	NO	NONE
C3		DIFFM	IO_L25P_0	UNUSED		0								
C4		IOB	IO	UNUSED		0								
C5		DIFFM	IO_L23P_0	UNUSED		0								
C6			VCCO_0			0					2.50			
C7		DIFFM	IO_L18P_0	UNUSED		0								
C8		DIFFM	IP_L16P_0	UNUSED		0								
C9		DIFFM	IO_L14P_0/GCLK10	UNUSED		0								
C10			GND											
C11		DIFFM	IO_L09P_0	UNUSED		0								
C12		DIFFM	IP_L07P_0	UNUSED		0								
C13			VCCO_0			0					2.50			
C14		DIFFS	IO_L03N_0/VREF_0	UNUSED		0								
C15		IBUF	IP	UNUSED		0								
C16			TDO											
C17		DIFFS	IO_L24N_1/LDC2	UNUSED		1								
C18		DIFFM	IO_L24P_1/LDC1	UNUSED		1								
D1	imginput<5>	IBUF	IO_L02P_3	INPUT	LVC MOS25*	3				NONE		UNLOCATED	NO	NONE
D2	imginput<2>	IBUF	IO_L02N_3/VREF_3	INPUT	LVC MOS25*	3				NONE		UNLOCATED	NO	NONE
D3	imginput<3>	IBUF	IP	INPUT	LVC MOS25*	3				NONE		UNLOCATED	NO	NONE
D4			NC											
D5		DIFFS	IO_L23N_0/VREF_0	UNUSED		0								
D6			NC											
D7		DIFFS	IO_L18N_0/VREF_0	UNUSED		0								
D8		DIFFSI	IP_L16N_0	UNUSED		0								
D9		DIFFS	IO_L14N_0/GCLK11	UNUSED		0								
D10		DIFFM	IO_L11P_0/GCLK4	UNUSED		0								
D11		DIFFS	IO_L09N_0	UNUSED		0								
D12		DIFFSI	IP_L07N_0	UNUSED		0								

D13		IBUF	IP	UNUSED		0							
D14		DIFFM	IO_L03P_0	UNUSED		0							
D15			TMS										
D16		DIFFS	IO_L23N_1/LDC0	UNUSED		1							
D17		DIFFM	IO_L23P_1/HDC	UNUSED		1							
D18		IBUF	IP/VREF_1	UNUSED		1							
E1	imginput<1>	IBUF	IO_L03N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
E2	imginput<4>	IBUF	IO_L03P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
E3			NC										
E4			NC										
E5			VCCINT							1.2			
E6			NC										
E7		DIFFS	IO_L19N_0/VREF_0	UNUSED		0							
E8		DIFFM	IO_L17P_0	UNUSED		0							
E9		DIFFM	IO_L15P_0	UNUSED		0							
E10		DIFFS	IO_L11N_0/GCLK5	UNUSED		0							
E11		DIFFM	IO_L08P_0	UNUSED		0							
E12		DIFFS	IO_L06N_0	UNUSED		0							
E13		IOB	IO	UNUSED		0							
E14			VCCINT							1.2			
E15			NC										
E16			NC										
E17		IOB	IO	UNUSED		1							
E18		IBUF	IP	UNUSED		1							
F1	imginput<13>	IBUF	IO_L05P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
F2	imginput<11>	IBUF	IO_L05N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
F3			VCCO_3			3				2.50			
F4	arst_n	IBUF	IO	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
F5	imginput<7>	IBUF	IP	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
F6			VCCINT							1.2			
F7		DIFFM	IO_L19P_0	UNUSED		0							
F8		DIFFS	IO_L17N_0	UNUSED		0							
F9		DIFFS	IO_L15N_0	UNUSED		0							
F10		DIFFM	IP_L10P_0	UNUSED		0							
F11		DIFFS	IO_L08N_0	UNUSED		0							
F12		DIFFM	IO_L06P_0	UNUSED		0							
F13			VCCINT							1.2			
F14		DIFFS	IO_L21N_1	UNUSED		1							
F15		DIFFM	IO_L21P_1	UNUSED		1							
F16			VCCO_1			1				any*****			
F17		DIFFS	IO_L19N_1	UNUSED		1							
F18		DIFFM	IO_L19P_1	UNUSED		1							
G1	imginput<12>	IBUF	IP	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
G2			VCCAUX							2.5			
G3	imginput<8>	IBUF	IO_L06P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
G4	imginput<9>	IBUF	IO_L06N_3/VREF_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
G5	imginput<14>	IBUF	IO_L07N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
G6	imginput<10>	IBUF	IO_L07P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
G7			GND										
G8			VCCO_0			0				2.50			
G9		IOB	IO	UNUSED		0							
G10		DIFFSI	IP_L10N_0	UNUSED		0							
G11			VCCO_0			0				2.50			
G12			GND										
G13		DIFFS	IO_L20N_1	UNUSED		1							
G14		DIFFM	IO_L20P_1	UNUSED		1							
G15		DIFFM	IO_L18P_1	UNUSED		1							
G16		DIFFS	IO_L18N_1	UNUSED		1							
G17			VCCAUX							2.5			
G18		IBUF	IP	UNUSED		1							
H1	imginput<18>	IBUF	IO_L10N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H2	imginput<19>	IBUF	IO_L10P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H3	imginput<16>	IBUF	IO_L09N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H4	imginput<21>	IBUF	IO_L09P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H5	imginput<20>	IBUF	IO_L08N_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H6	imginput<15>	IBUF	IO_L08P_3	INPUT	LVC MOS25*	3			NONE		UNLOCATED	NO	NONE
H7			VCCO_3			3				2.50			
H8			GND										
H9			GND										
H10			GND										
H11			GND										
H12			VCCO_1			1				any*****			

## Index

			VCCAUX							2.5			
M18		DIFFS	IO_L08N_1	UNUSED		1							
N1		IBUF	IP	UNUSED		3							
N2		IBUF	IP	UNUSED		3							
N3			VCCO_3			3				2.50			
N4	imgoutput<3>	IOB	IO_L20P_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
N5	imgoutput<15>	IOB	IO_L20N_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
N6			VCCINT							1.2			
N7	realoutput<10>	IOB	IO_L07P_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
N8	realoutput<13>	IOB	IO_L09N_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
N9	realoutput<17>	IOB	IO_L12P_2/D7/GCLK12	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
N10	realinput<12>	IBUF	IO_L16N_2/DIN/D0	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
N11	realinput<16>	IBUF	IO_L18N_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
N12			NC										
N13			VCCINT							1.2			
N14			NC										
N15			NC										
N16			VCCO_1			1				any*****			
N17		IBUF	IP	UNUSED		1							
N18		DIFFM	IO_L08P_1	UNUSED		1							
P1	imgoutput<2>	IOB	IO_L21N_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
P2	imgoutput<1>	IOB	IO_L21P_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
P3			NC										
P4			NC										
P5			VCCINT							1.2			
P6	realoutput<9>	IOB	IO_L05N_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
P7	realoutput<11>	IOB	IO_L07N_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
P8	realoutput<12>	IOB	IO_L09P_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
P9	realoutput<14>	IOB	IO	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
P10	realoutput<23>	IOB	IO_L15N_2/D1/GCLK3	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
P11	realinput<15>	IBUF	IO_L18P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
P12			NC										
P13	realinput<23>	IBUF	IO_L22P_2/A23	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
P14			VCCINT							1.2			
P15		IOB	IO	UNUSED		1							
P16			NC										
P17		DIFFM	IO_L06P_1	UNUSED		1							
P18		DIFFS	IO_L06N_1	UNUSED		1							
R1		IBUF	IP	UNUSED		3							
R2	imgoutput<0>	IOB	IO_L23N_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
R3	imgoutput<6>	IOB	IO_L23P_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
R4	imgoutput<4>	IOB	IO/VREF_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
R5	realoutput<6>	IOB	IO_L04P_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
R6	realoutput<8>	IOB	IO_L05P_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
R7	realinput<5>	IBUF	IP_L08N_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
R8	realoutput<15>	IOB	IO_L10P_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
R9	realoutput<19>	IOB	IO/D5	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
R10	realoutput<22>	IOB	IO_L15P_2/D2/GCLK2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
R11	realinput<17>	IBUF	IO	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
R12	realinput<18>	IBUF	IO_L20N_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
R13		DIFFS	IO_L22N_2/A22	UNUSED		2							
R14		DIFFS	IO_L24N_2/A20	UNUSED		2							
R15		DIFFM	IO_L03P_1	UNUSED		1							
R16		DIFFS	IO_L03N_1/VREF_1	UNUSED		1							
R17		IBUF	IP	UNUSED		1							
R18		DIFFM	IO_L02P_1/A14	UNUSED		1							
T1	realoutput<5>	IOB	IO_L24N_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
T2	imgoutput<5>	IOB	IO_L24P_3	OUTPUT	LVC MOS25*	3	12	SLOW	NONE**		UNLOCATED	NO	NONE
T3	realoutput<2>	IOB	IO_L01N_2/INIT_B	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
T4	realoutput<4>	IOB	IO_L03N_2/MOSI/CS1_B	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
T5	realoutput<7>	IOB	IO_L04N_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
T6			VCCO_2			2				2.50			
T7	realinput<4>	IBUF	IP_L08P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
T8	realoutput<16>	IOB	IO_L10N_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
T9			GND										
T10	realinput<9>	IBUF	IP_L14N_2/M2/GCLK1	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
T11	realinput<13>	IBUF	IP_L17P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
T12	realinput<19>	IBUF	IO_L20P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
T13			VCCO_2			2				2.50			
T14		DIFFM	IO_L24P_2/A21	UNUSED		2							
T15		IOB	IO/VREF_2	UNUSED									
T16		DIFFM	IO_L26P_2/VS0/A17	UNUSED		2							

		DIFFS	IO_L01N_1/A15	UNUSED		1							
T18		DIFFS	IO_L02N_1/A13	UNUSED		1							
U1		IBUF	IP	UNUSED		3							
U2			GND										
U3	realoutput<1>	IOB	IO_L01P_2/CSO_B	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
U4	realoutput<3>	IOB	IO_L03P_2/DOUT/BUSY	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
U5	realoutput<0>	IOB	IO/VREF_2	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
U6	realinput<3>	IBUF	IP	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
U7			VCCAUX							2.5			
U8	realinput<6>	IBUF	IP_L11P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
U9	realoutput<20>	IOB	IO_L13P_2/D4/GCLK14	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
U10	realinput<8>	IBUF	IP_L14P_2/RDWR_B/GCLK0	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
U11	realinput<14>	IBUF	IP_L17N_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
U12			VCCAUX							2.5			
U13	realinput<22>	IBUF	IP	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
U14		DIFFSI	IP_L23N_2	UNUSED		2							
U15		DIFFS	IO_L25N_2/VS1/A18	UNUSED		2							
U16		DIFFS	IO_L26N_2/CCLK	UNUSED		2							
U17			GND										
U18		DIFFM	IO_L01P_1/A16	UNUSED		1							
V1			GND										
V2	realinput<0>	IBUF	IP	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V3	realinput<2>	IBUF	IP_L02N_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V4	realinput<1>	IBUF	IP_L02P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V5			NC										
V6			NC										
V7			NC										
V8	realinput<7>	IBUF	IP_L11N_2/VREF_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V9	realoutput<21>	IOB	IO_L13N_2/D3/GCLK15	OUTPUT	LVC MOS25*	2	12	SLOW	NONE**		UNLOCATED	NO	NONE
V10			VCCO_2			2				2.50			
V11	realinput<10>	IBUF	IO/M1	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V12	realinput<21>	IBUF	IO_L19P_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V13	realinput<20>	IBUF	IO_L19N_2/VREF_2	INPUT	LVC MOS25*	2				NONE	UNLOCATED	NO	NONE
V14		DIFFM1	IP_L23P_2	UNUSED		2							
V15		DIFFM	IO_L25P_2/VS2/A19	UNUSED		2							
V16		IBUF	IP	UNUSED		2							
V17			DONE										
V18			GND										