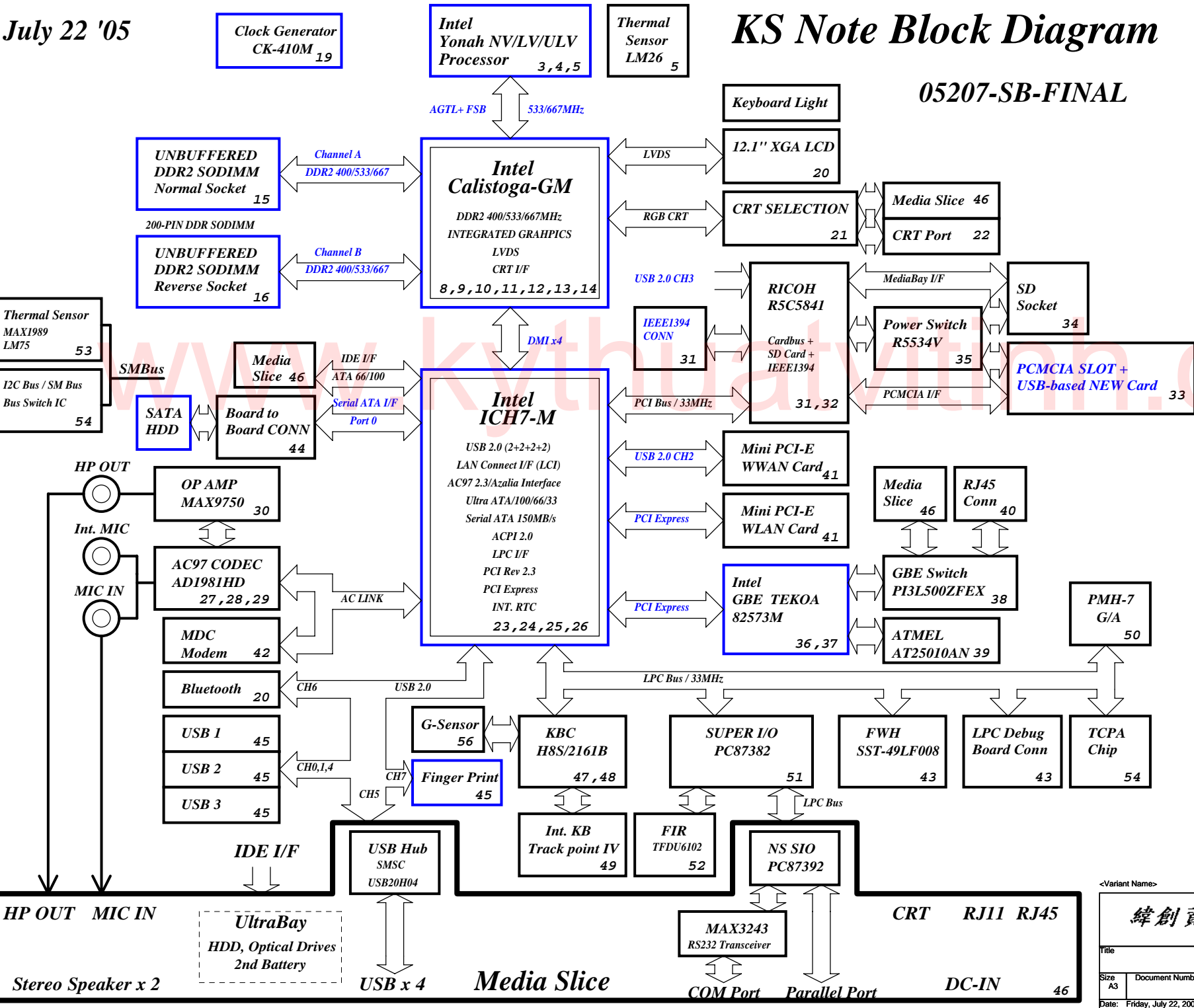


July 22 '05



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

| Block Diagram | | | |
|-----------------------------|-----------------|---------|--------|
| Size A3 | Document Number | KS Note | Rev SB |
| Date: Friday, July 22, 2005 | Sheet 1 | of 73 | |

| Symbol name | Value | Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %) | Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V | Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210 |
|-------------|----------|--|--|--|
| 10KR3 | 10K Ohm | If no letter, it means J: 5% | 1/16W, 75V | 0603 |
| 33D3R5 | 33.3 Ohm | If no letter, it means J: 5% | 1/10W, 100V | 0805 |
| 1KR3F | 1K Ohm | F: 1% | 1/16W, 75V | 0603 |


| Symbol name | Value | Tolerance (M: +/-20, K: +/-10, Z: +80/-20) | Rating | Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210 |
|---------------|-------|---|--------|--|
| SCD1U10V2MX-1 | 0.1uF | M/X5R | 10V | 0402 |
| SC10U6D3V5MX | 10uF | M/X5R | 6.3V | 0805 |
| SC2D2U16V5ZY | 2.2uF | Z/Y5V | 16V | 0805 |

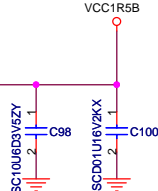
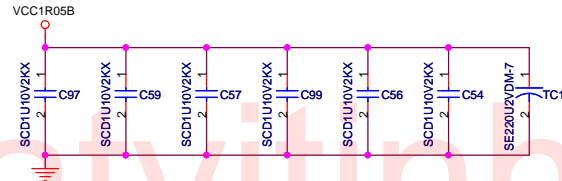
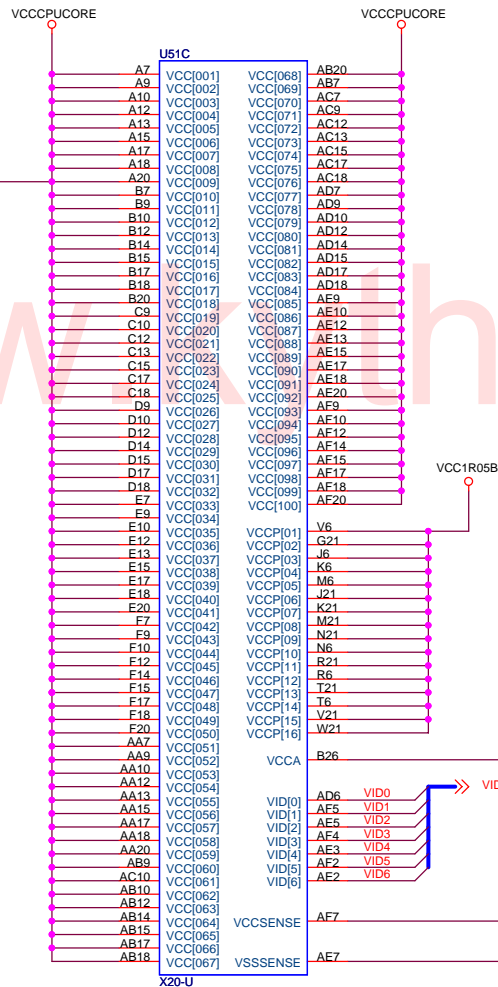
| ICH7-M GPION | 39 | 38 | 37 | 36 | Planar ID Version | Planar PCB Version |
|--------------|----|----|----|----|-------------------|--------------------|
| PLANAR_IDn | 3 | 2 | 1 | 0 | | |
| | 0 | 0 | 0 | 0 | SDV | SA |
| | 0 | 0 | 0 | 1 | SIV | SB |
| | 0 | 0 | 1 | 0 | | |
| | 0 | 0 | 1 | 1 | | |
| | 0 | 1 | 0 | 0 | | |
| | 0 | 1 | 0 | 1 | | |
| | 0 | 1 | 1 | 0 | | |
| | 0 | 1 | 1 | 1 | | |
| | 1 | 0 | 0 | 0 | | |

| SDV | | |
|----------|------------|---------------|
| VER.0.00 | 01/19/2005 | INITIAL LOGIC |
| SDV EC | 02/22/2005 | EC001, EC002 |
| SDV EC | 02/24/2005 | EC003 |
| SDV EC | 03/07/2005 | EC004 |
| SDV EC | 03/15/2005 | EC005, EC006 |
| SDV EC | 03/17/2005 | EC007 |
| SDV EC | 03/18/2005 | EC008 |
| SDV EC | 03/21/2005 | EC009 |
| SDV EC | 03/25/2005 | EC010 |
| SDV EC | 03/30/2005 | EC011 |
| SDV EC | 04/06/2005 | EC012 |
| SDV EC | 04/13/2005 | EC013 |
| SDV EC | 04/14/2005 | EC014 |
| SDV EC | 04/15/2005 | EC015 |
| SDV EC | 04/19/2005 | EC016 |
| SDV EC | 04/21/2005 | EC017 |
| SDV EC | 04/25/2005 | EC018 |
| SDV EC | 04/25/2005 | EC019 |
| SDV EC | 04/27/2005 | EC020 |
| SDV EC | 04/28/2005 | EC021, EC022 |
| SDV EC | 05/02/2005 | EC023 |
| SDV EC | 05/13/2005 | EC024 |
| SDV EC | 05/16/2005 | EC025 |

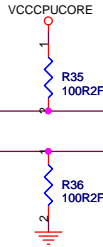
[illegible]

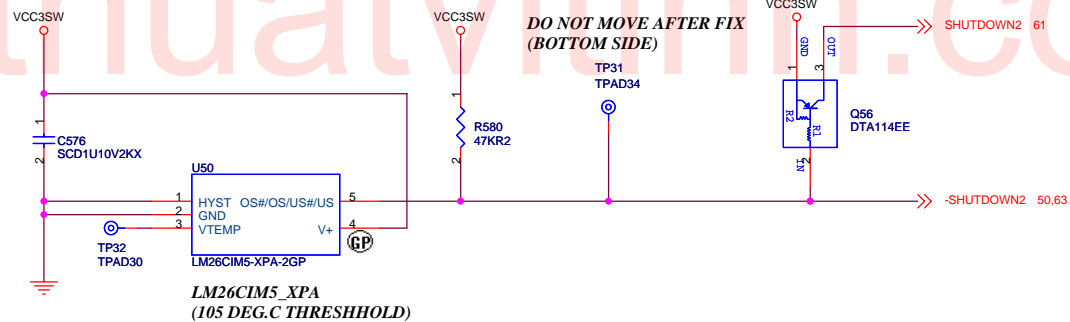
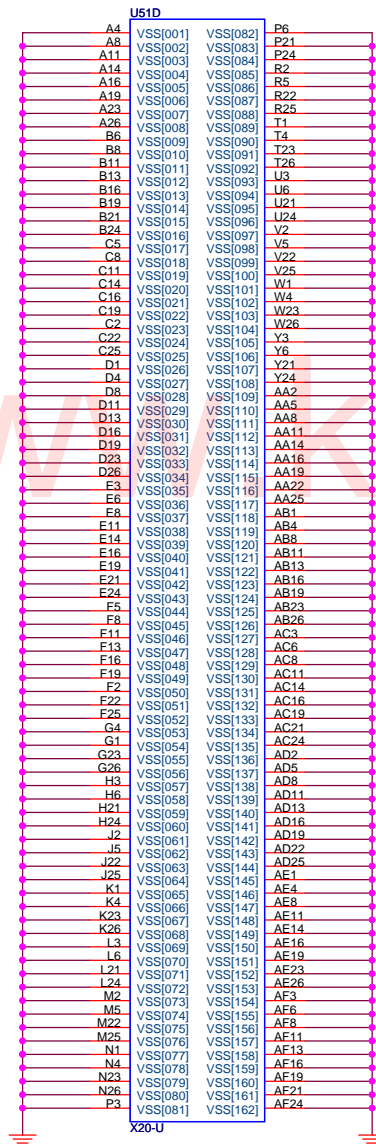
| DEVICE | IDSEL | IRQ (Default) | REQ# / GNT# |
|--|-------|---------------|---------------|
| MINIPCI SLOT | AD18 | F, G | REQ# 3/ GNT#3 |
| CARDBUS R5C811 | AD16 | SERIRQ | REQ#0 / GNT#0 |
| USB UHCI | AD29 | A, C, D | |
| USB 2.0 EHCI | AD29 | H | |
| DMI-to-PCI/ AC97 Modem/ AC97 Audio | AD30 | B B | |
| LPC Bridge IDE SATA SMBus | AD31 | C C B | |
| PCI Express | AD28 | A, B, C, D | |

| | | | |
|---|-----------------|---|----|
|  | | Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C. | |
| Title | | | |
| Reference | | | |
| Size A3 | Document Number | Rev SB | |
| KS Note | | | |
| Date: Friday, July 22, 2005 | Sheet | 2 of | 73 |



PLACE ONE 10UF
AND 0.01UF NEAR
VCCA PIN.





<Variant Name>

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title

YONAH CPU(3/3)

Size
A3

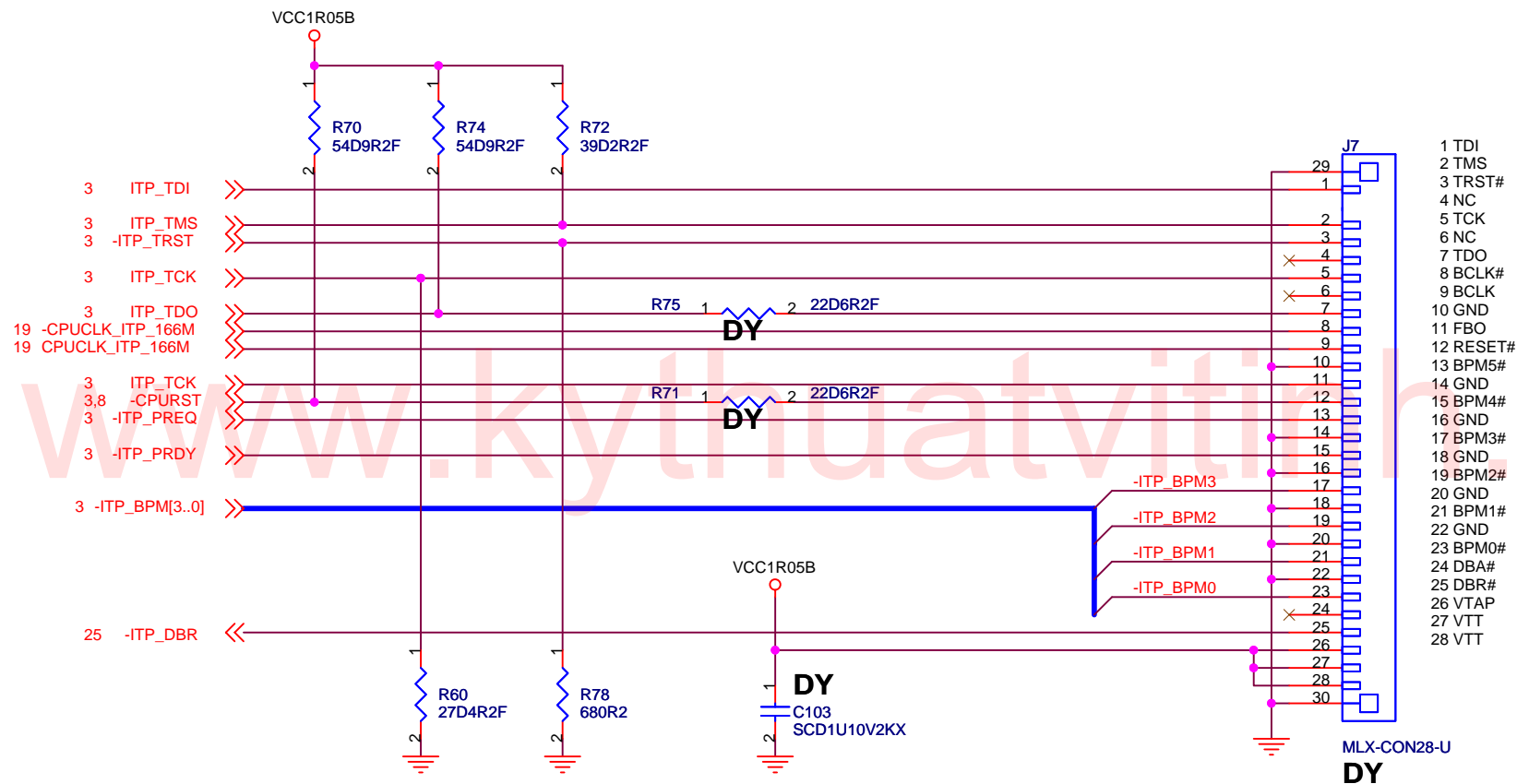
Document Number

KS Note

Rev
SB

Date: Friday, July 22, 2005

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(*1) TCK SIGNAL IS BRANCHED AT DOTHAN PIN

(*2) CPURST# SIGNAL IS BRANCHED AT ALVISO PIN

<Variant Name>

緯創資通

Wistron Corporation

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Title

ITP CONN

Size
A4

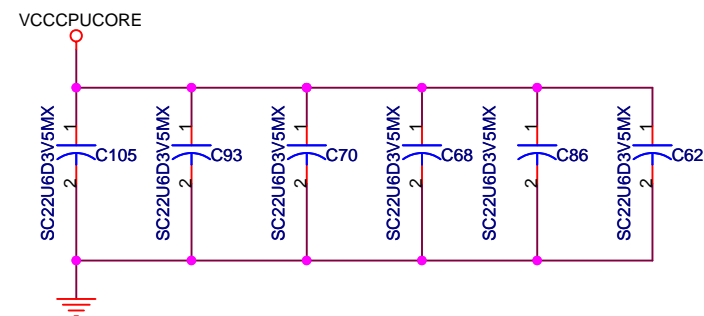
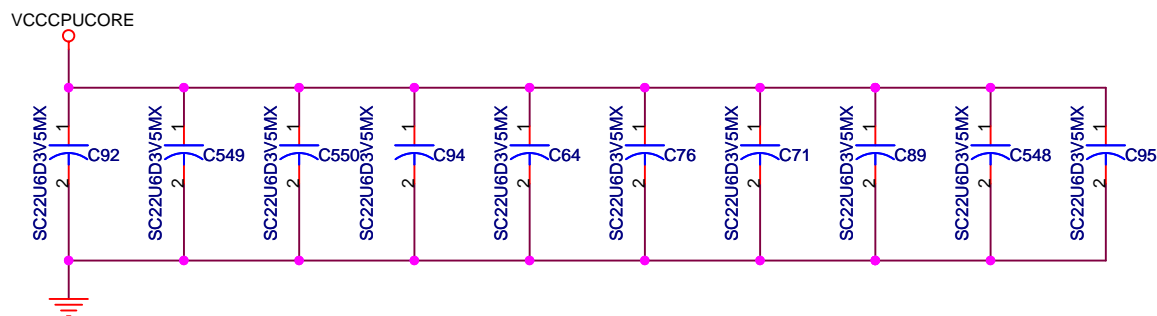
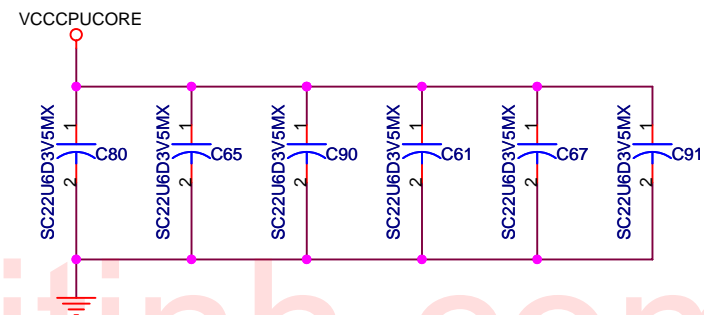
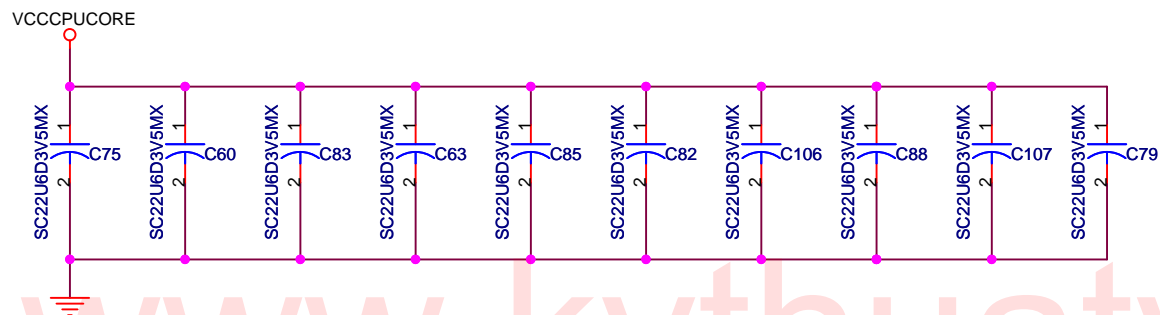
Document Number

KS Note

Rev
SB

Date: Friday, July 22, 2005

Sheet 6 of 73



<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title **VCCCPUCORE DECOUPLING**

Size A4 Document Number

KS Note

Rev
SB

Date: Friday, July 22, 2005

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VCC1R05B

R91
221R2F-2-GP

H_XSWING

R97
100R2FC139
SCD1U10V2KX

VCC1R05B

R79
221R2F-2-GP

H_YSWING

R80
100R2FC112
SCD1U10V2KX

Route H_XSWING & H_YSWING
10 mil wide / 20 mil spacing

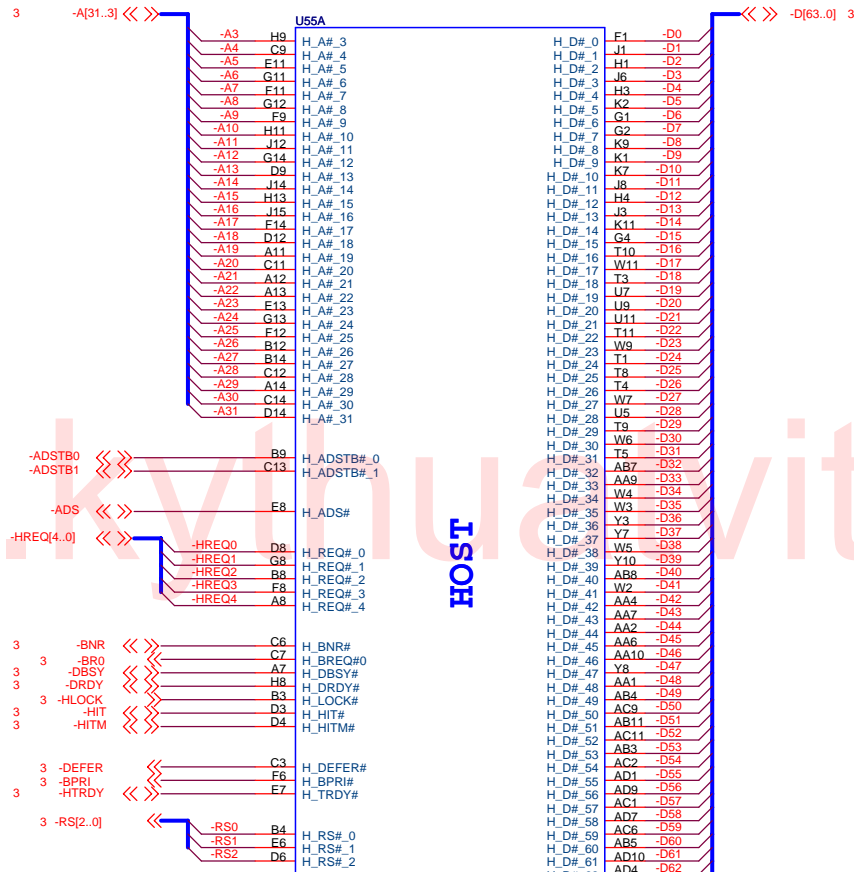
VCC1R05B

R89
54D9R2FR87
54D9R2F

Place these parts
near to the pins

Route H_XRCOMP &
H_YRCOMP 10 mil wide /
20 mil spacing

Route H_XRCOMP &
H_YRCOMP 10 mil wide /
20 mil spacing



H_DSTBN#_0 K4 -DSTBN0
H_DSTBN#_1 I7 -DSTBN1
H_DSTBN#_2 Y5 -DSTBN2
H_DSTBN#_3 AC4 -DSTBN3

H_DSTBP#_0 K3 -DSTBP0
H_DSTBP#_1 T6 -DSTBP1
H_DSTBP#_2 AA5 -DSTBP2
H_DSTBP#_3 AC5 -DSTBP3

H_DINV#_0 J7 -DINV0
H_DINV#_1 W8 -DINV1
H_DINV#_2 U3 -DINV2
H_DINV#_3 AB10 -DINV3

H_SLPCPU# E3 -SLP

H_VREF_0 J13 -VREF
H_VREF_1 K13 -VREF

<Variant Name>

緯創資通 Wistron Corporation
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Title

CALISTOGA(17):HOST I/F

Size A3 Document Number

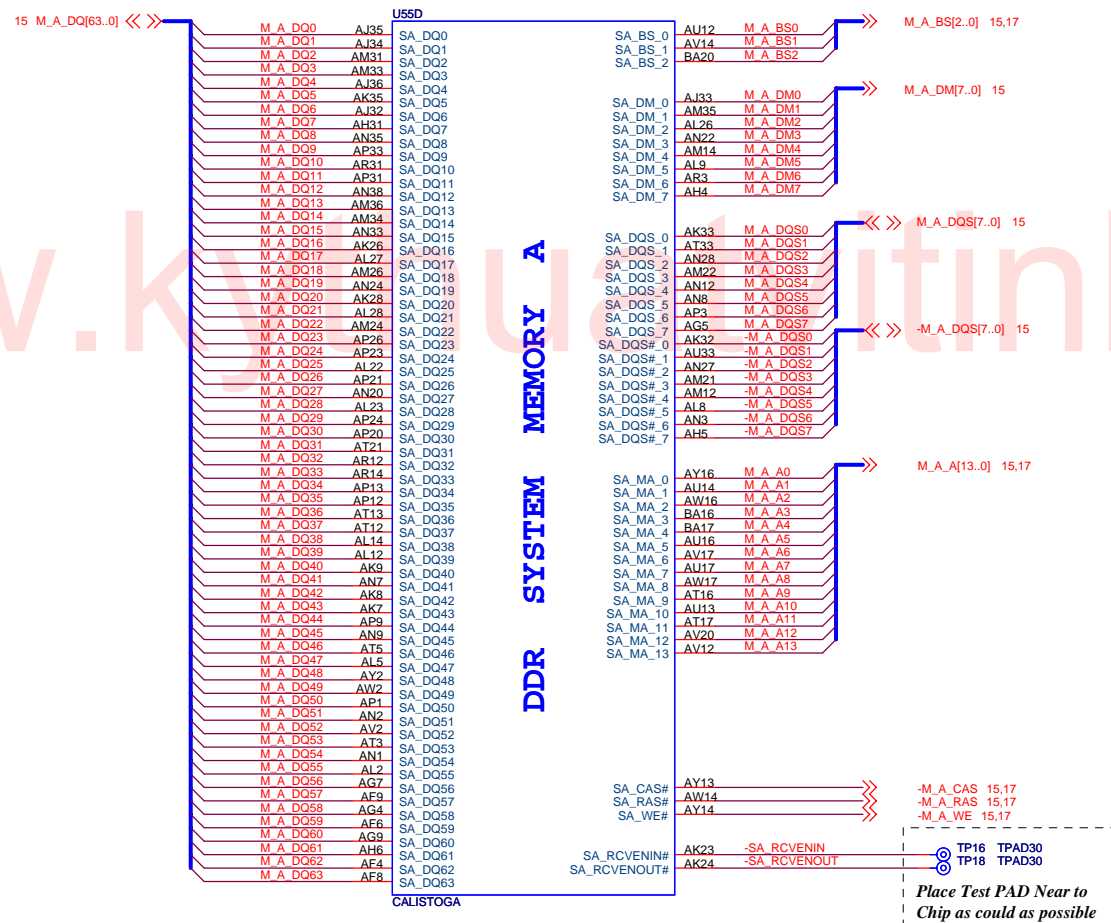
KS Note

Rev

SB

Date: Friday, July 22, 2005 Sheet 8 of 73

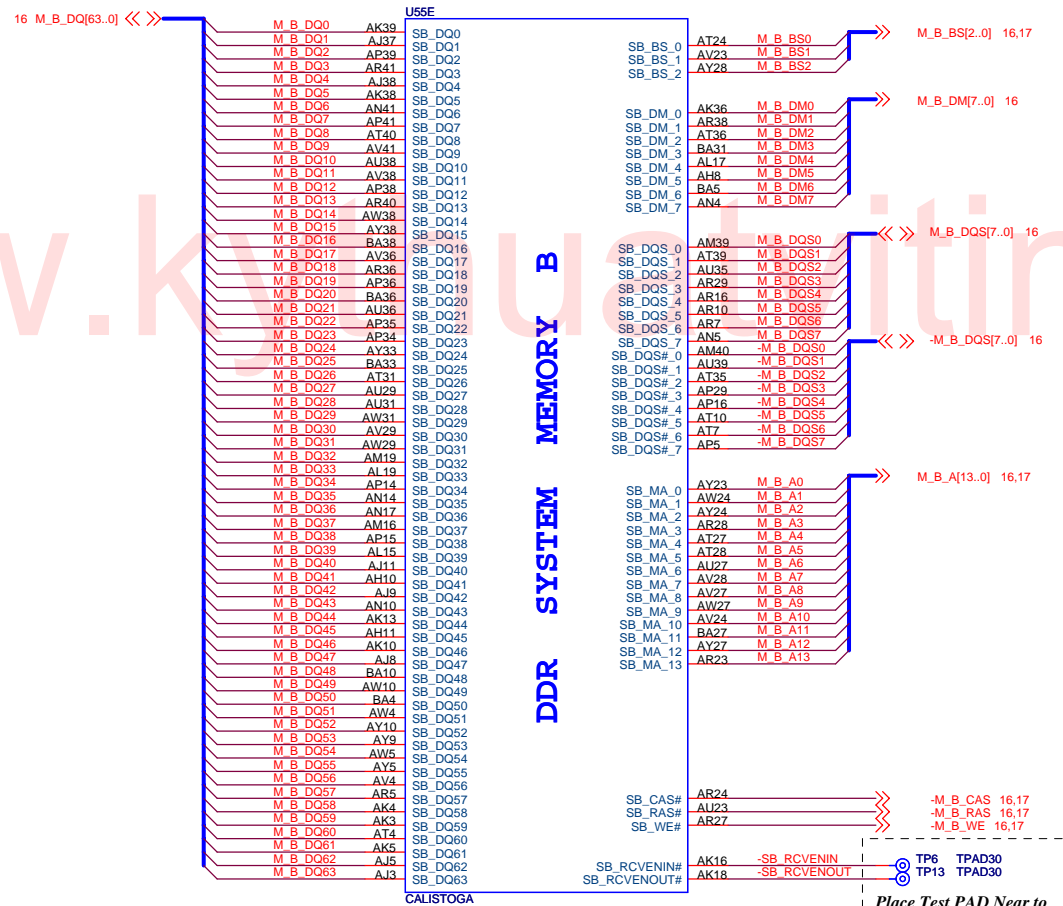
www.kv.com



<Variant Name>

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Taipei Hsien 221, Taiwan, R.O.C.

| | | | |
|-----------------------|-----------------------|---------|---------|
| Title | | | Rev |
| CALISTOGA(2/7):DDR2-A | | | SB |
| Size | Document Number | KS Note | |
| A3 | | | |
| Date: | Friday, July 22, 2005 | Sheet | 9 of 73 |



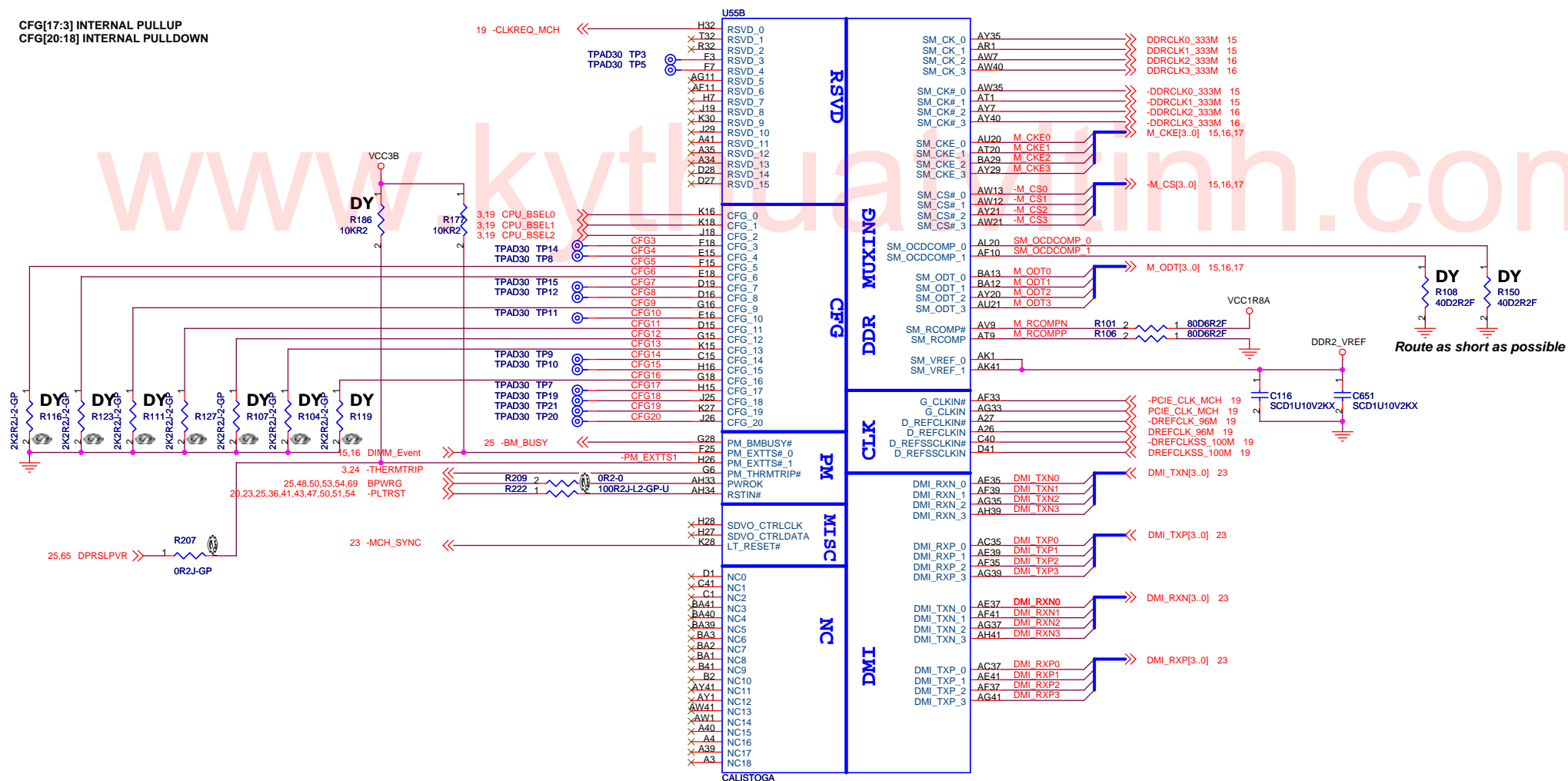
<Variant Name>

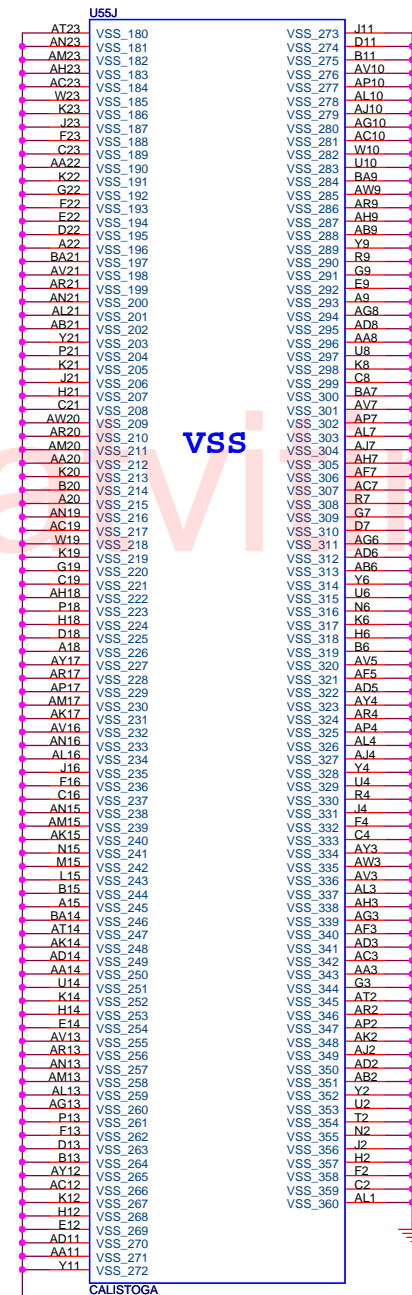
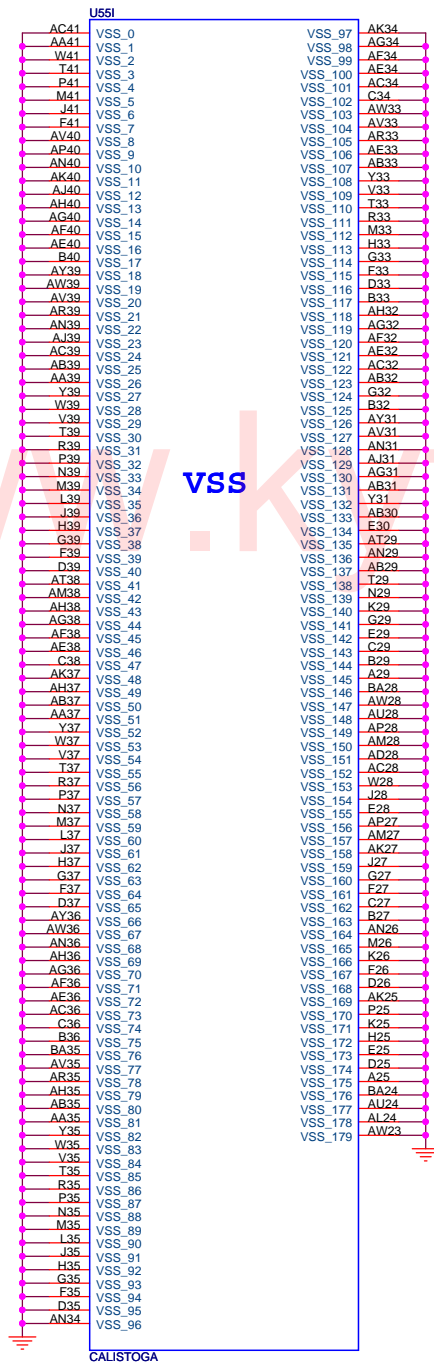
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,
Taipei Hsien 221, Taiwan, R.O.C.

| | | | |
|-------|-----------------------|-----------------------|----------|
| Title | | CALISTOGA(3/7):DDR2-B | |
| Size | Document Number | KS Note | |
| A3 | | Rev SB | |
| Date: | Friday, July 22, 2005 | Sheet | 10 of 73 |

| LEVEL | 0 | 1 |
|-------|----------------------|-------------------------------|
| CFG5 | DMI x2 | DMI x4 (default) |
| CFG6 | Moby Dick | Calistoga |
| CFG9 | Reverse Lanes | Normal Operation (default) |
| CFG11 | Calistoga | Moby Dick |
| CFG12 | Reserved | Reserved |
| CFG13 | Reserved | Reserved |
| CFG16 | Dynamic ODT Disabled | Dynamic ODT Enabled (default) |

CFG[17:3] INTERNAL PULLUP
CFG[20:18] INTERNAL PULLDOWN

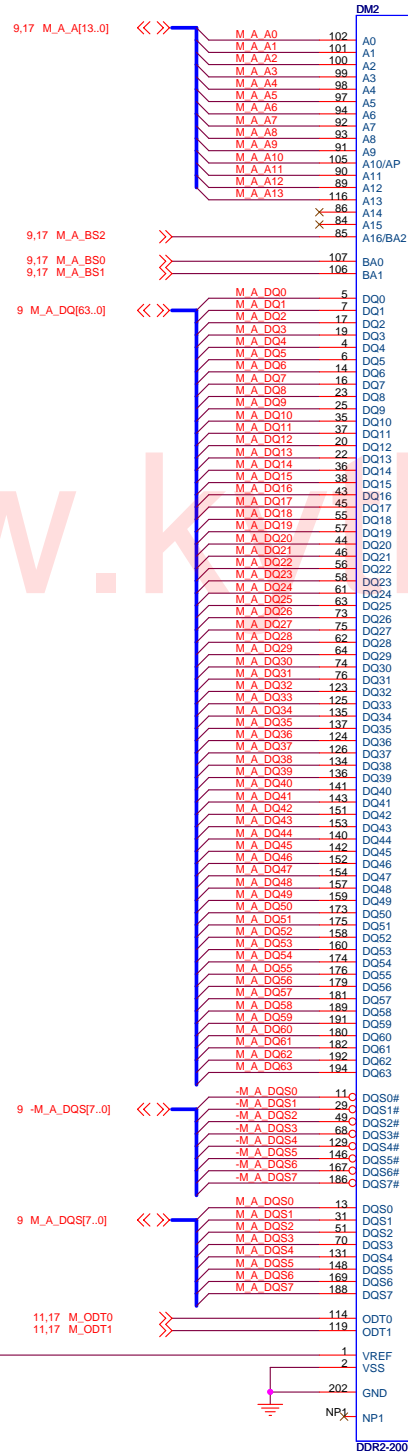
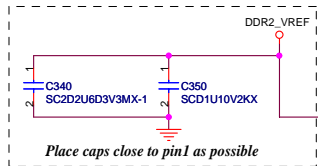




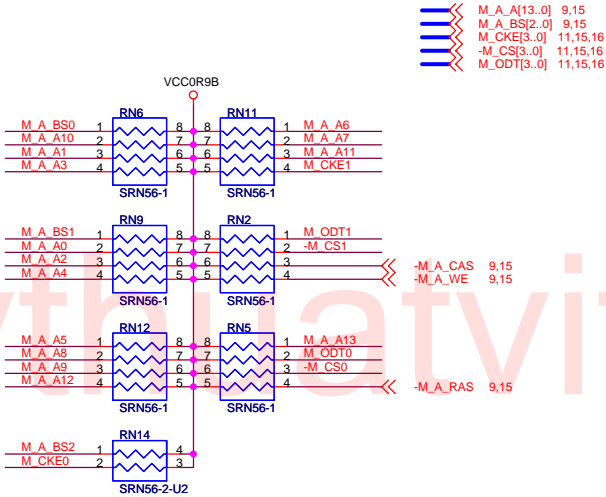
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,
Taipei Hsien 221, Taiwan, R.O.C.

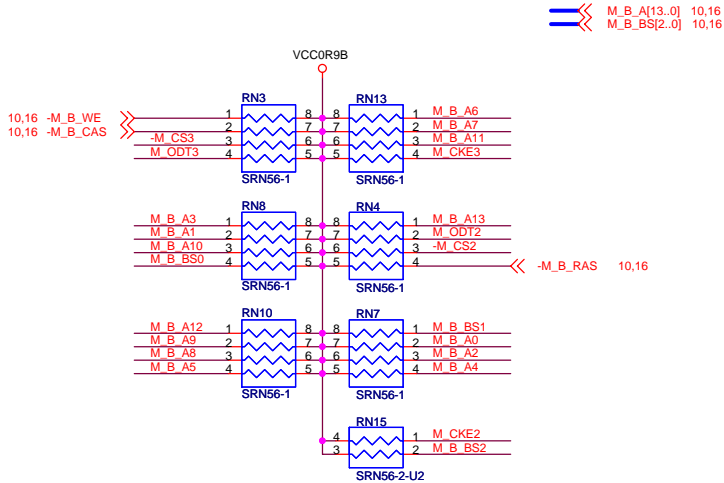
| | | | |
|---------------------------|-----------------------|----------------|-----------|
| Title | | | |
| CALISTOGA(7/7):GND | | | |
| Size | Document Number | | Rev |
| A3 | KS Note | | SB |
| Date: | Friday, July 22, 2005 | Sheet 14 of 73 | |



CHANNEL A PARALLEL TERMINATION

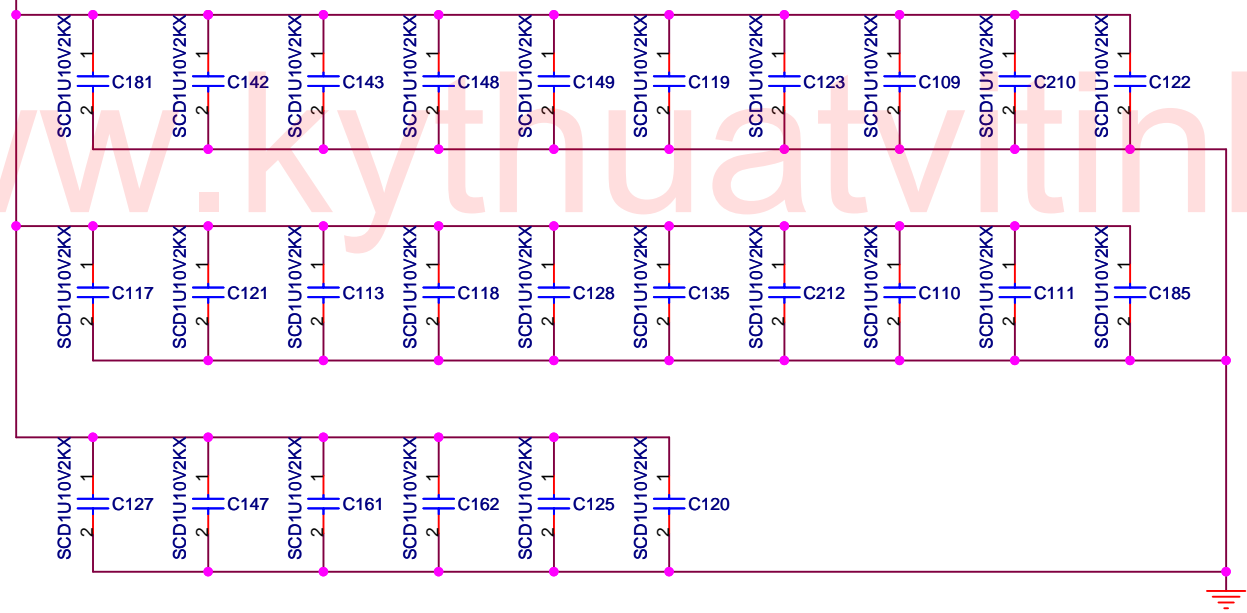


CHANNEL B PARALLEL TERMINATION



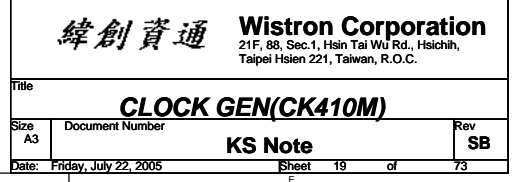
VCC0R9B

PLACE 1 CAP FOR EVERY 2 BITS TERMINATION TO VCC0R9B.

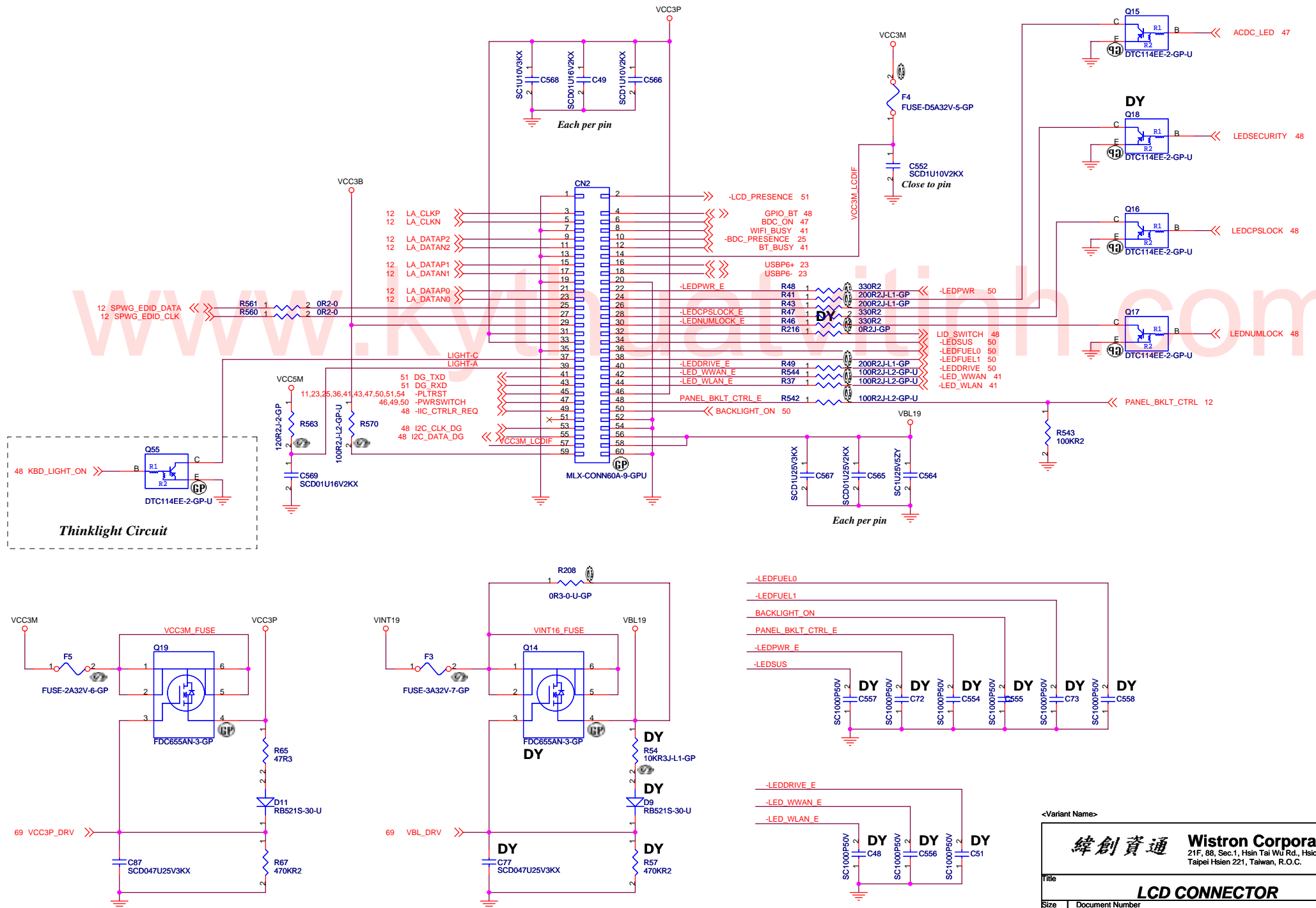


<Variant Name>

| | | | |
|-----------------------------|--------------------------------|---|-----------|
| 緯創資通 | | Wistron Corporation | |
| | | 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C. | |
| Title | | | |
| DDR-2 DECOUPLING | | | |
| Size A4 | Document Number KS Note | | Rev SB |
| Date: Friday, July 22, 2005 | | Sheet 18 of | 73 |



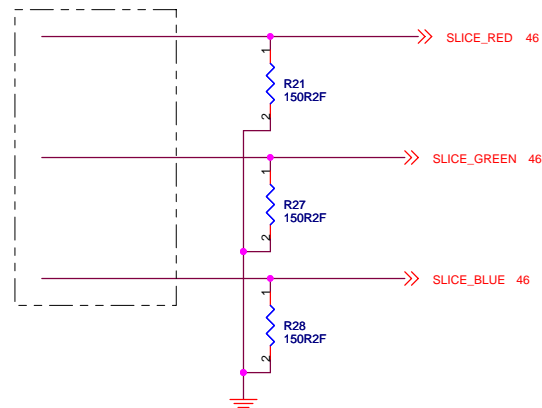
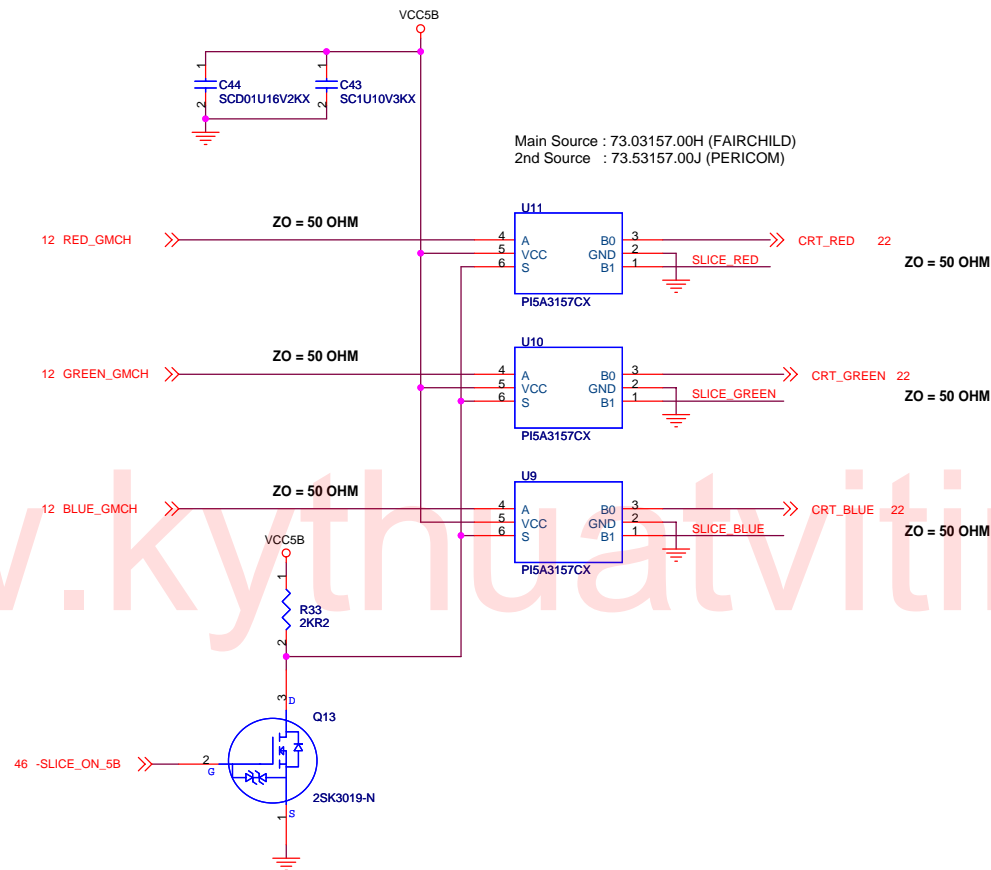
LCD / Inverter Connector



<Variant Name>

緯創資通 Wistron Corporation
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| LCD CONNECTOR | | | |
|-----------------------------|----------------------------|-----------|--|
| Size A3 | Document Number KS Note | Rev SB | |
| Date: Friday, July 22, 2005 | Sheet 20 of 73 | | |



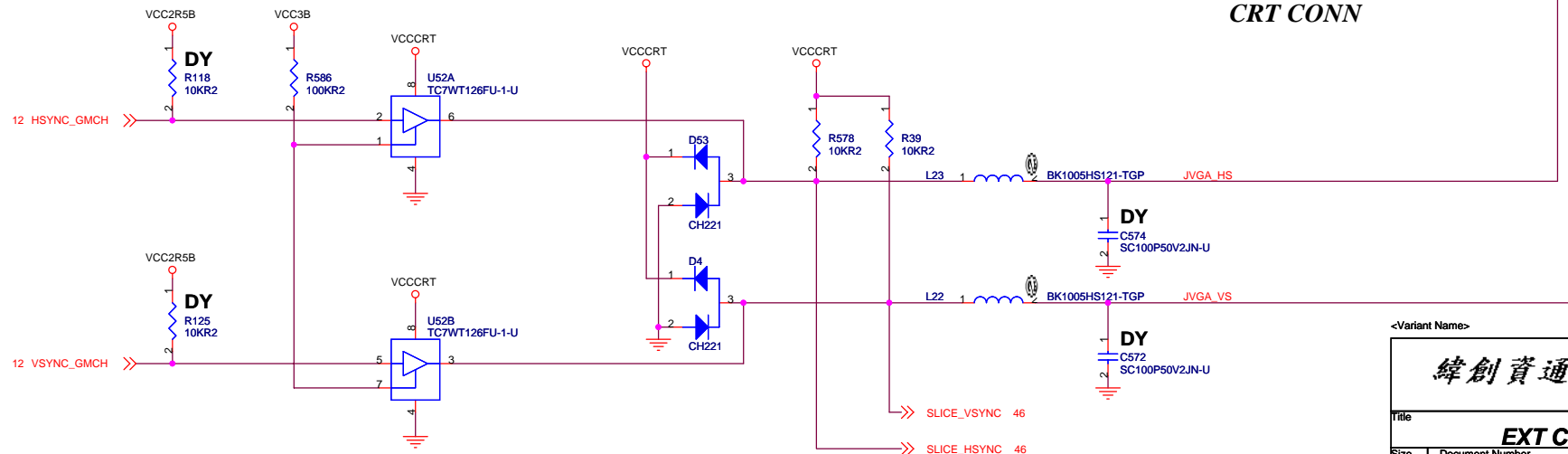
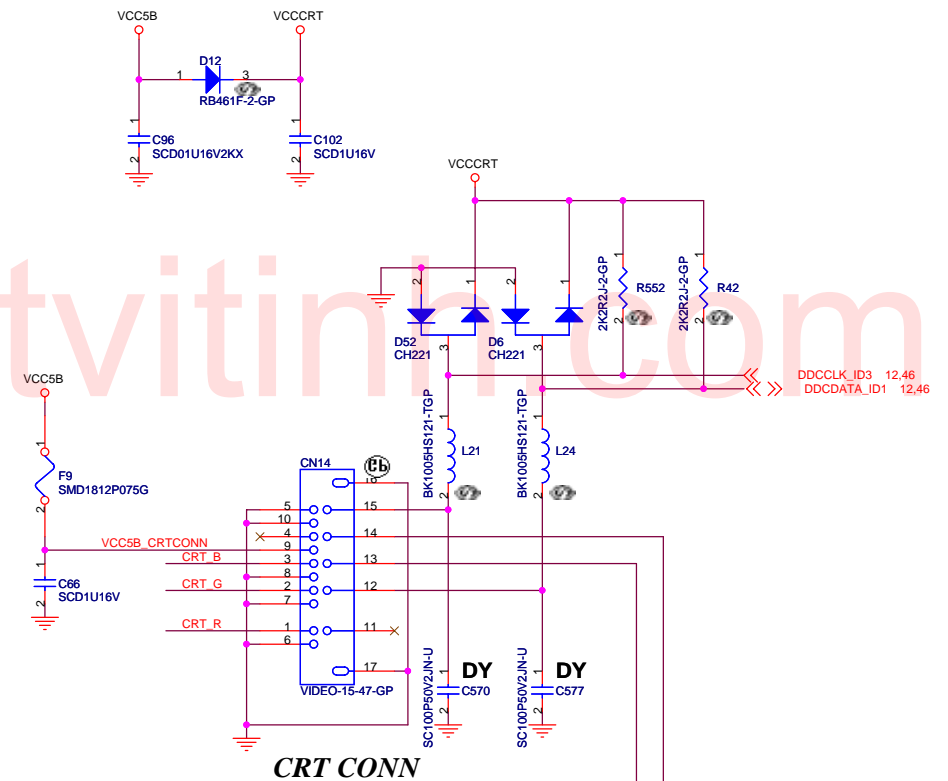
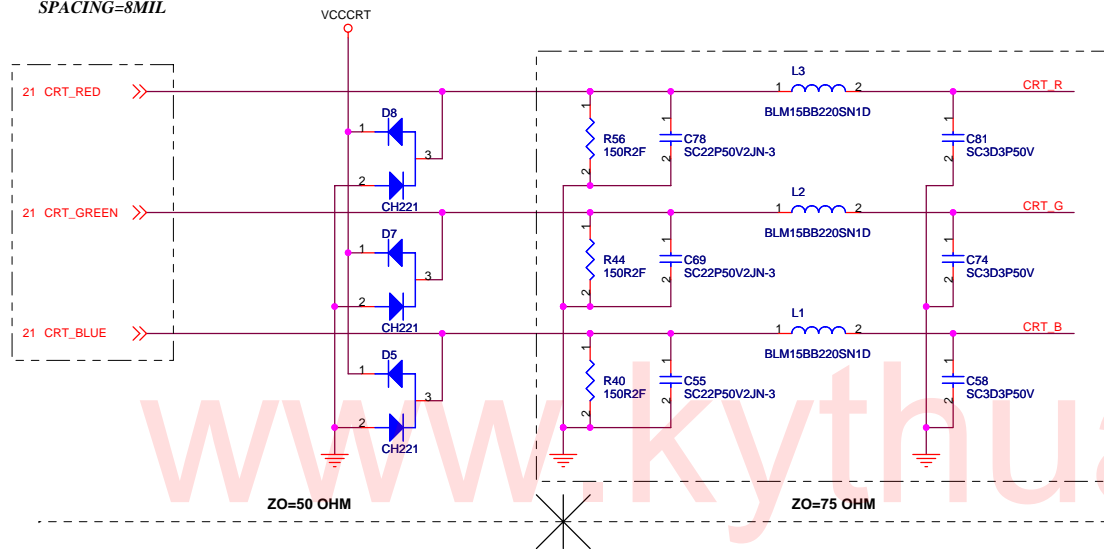
----- ZO=50.OHM ----- X ----- ZO=75 OHM -----

<Variant Name>

緯創資通 **Wistron Corporation**
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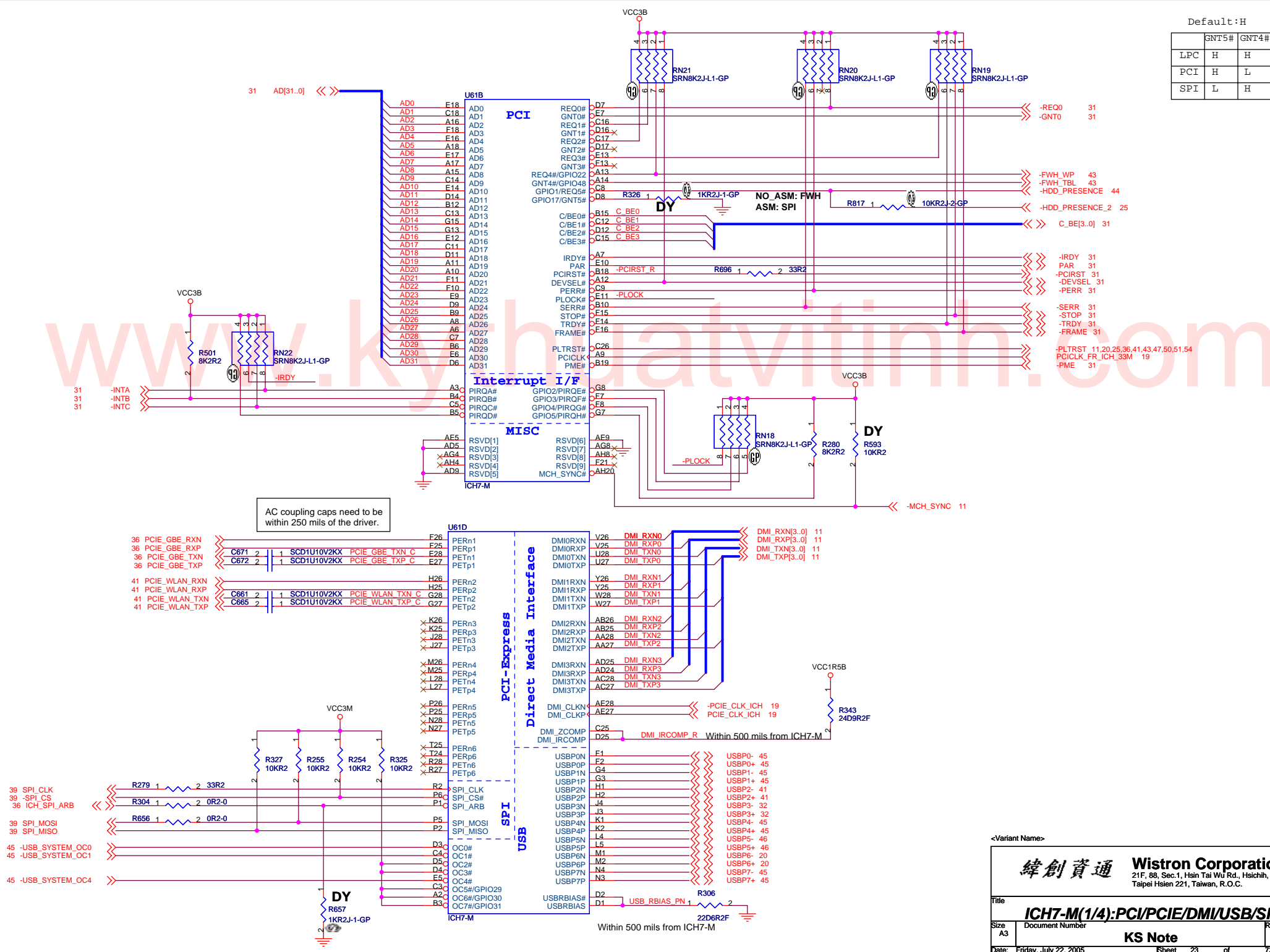
| Title | | | |
|-----------------------------|-----------------|---------|-----------|
| CRT SELECTOR | | | |
| Size A3 | Document Number | | Rev SB |
| Date: Friday, July 22, 2005 | | KS Note | |
| Sheet 21 | | of 73 | |

GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)
SPACING=8MIL



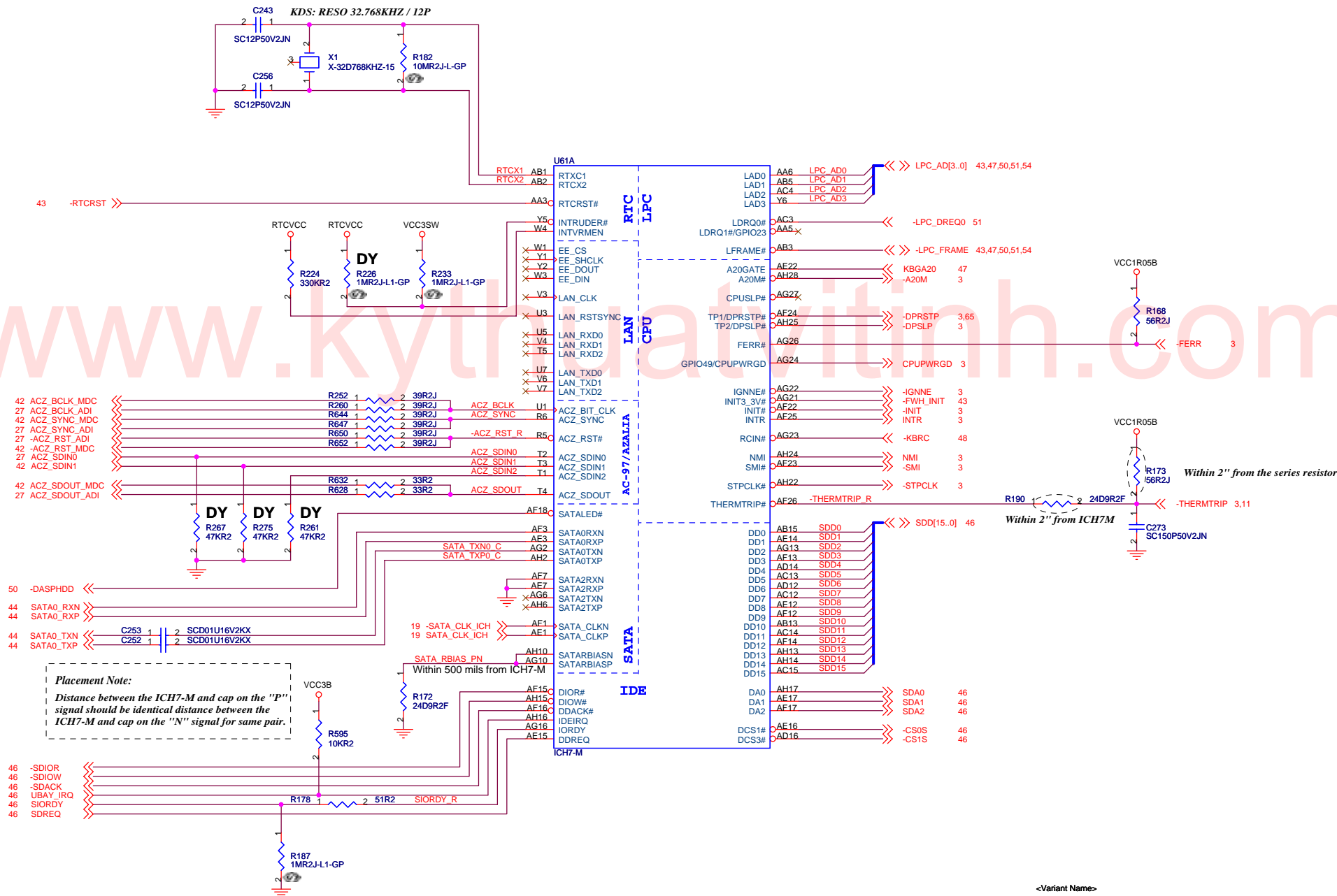
Default:H

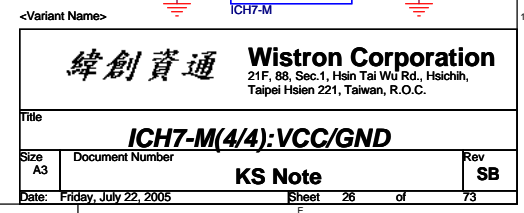
| | GNT5# | GNT4# |
|-----|-------|-------|
| LPC | H | H |
| PCI | H | L |
| SPI | L | H |



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Taipei Hsien 221, Taiwan, R.O.C.

| | | |
|-------|----------------------------------|----------------|
| Title | ICH7-M(1/4):PCI/PCIE/DMI/USB/SPI | |
| Size | Document Number | Rev |
| A3 | KS Note | SB |
| Date: | Friday, July 22, 2005 | Sheet 23 of 73 |

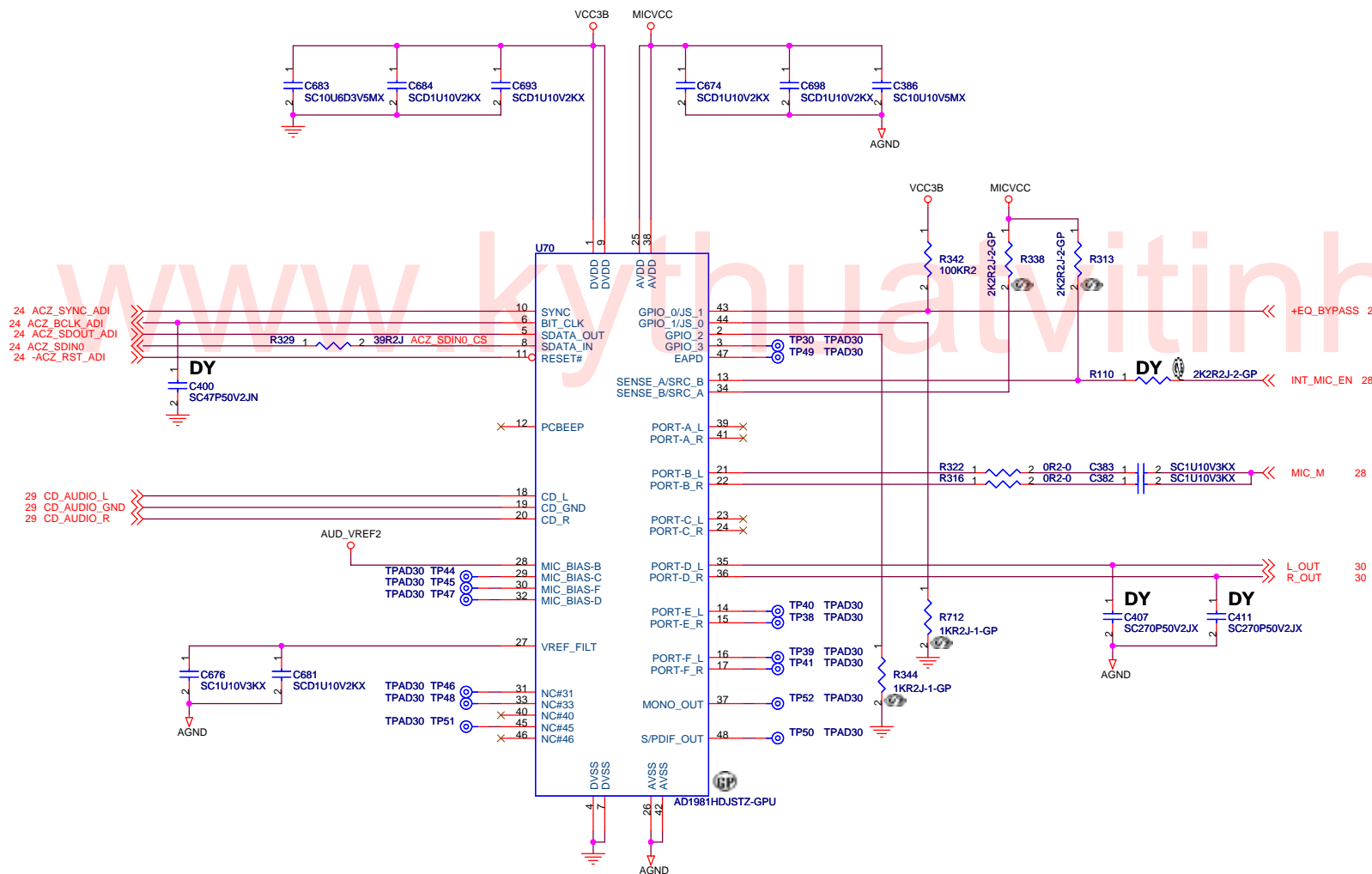




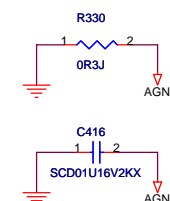
EACH NC PIN MUST HAVE TEST POINT PAD.

NC PIN LIST: PIN 3, 14, 15, 29, 31, 32, 33, 35,
PIN 36, 37, 43, 45, 47, 48

PIN 35 AND 36 ARE NOT NC PIN



PLACE UNDER AD1981HD

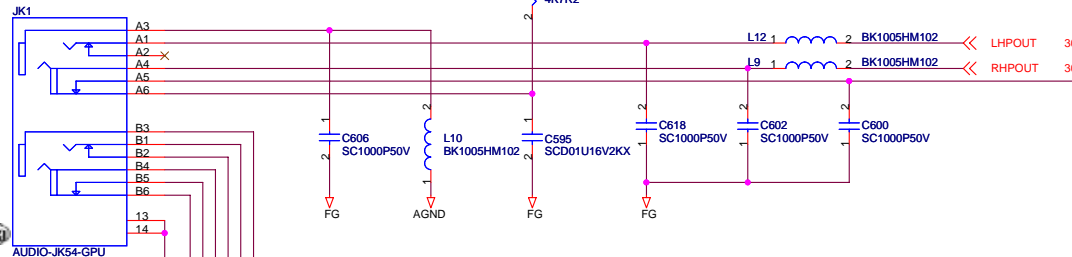


<Variant Name>

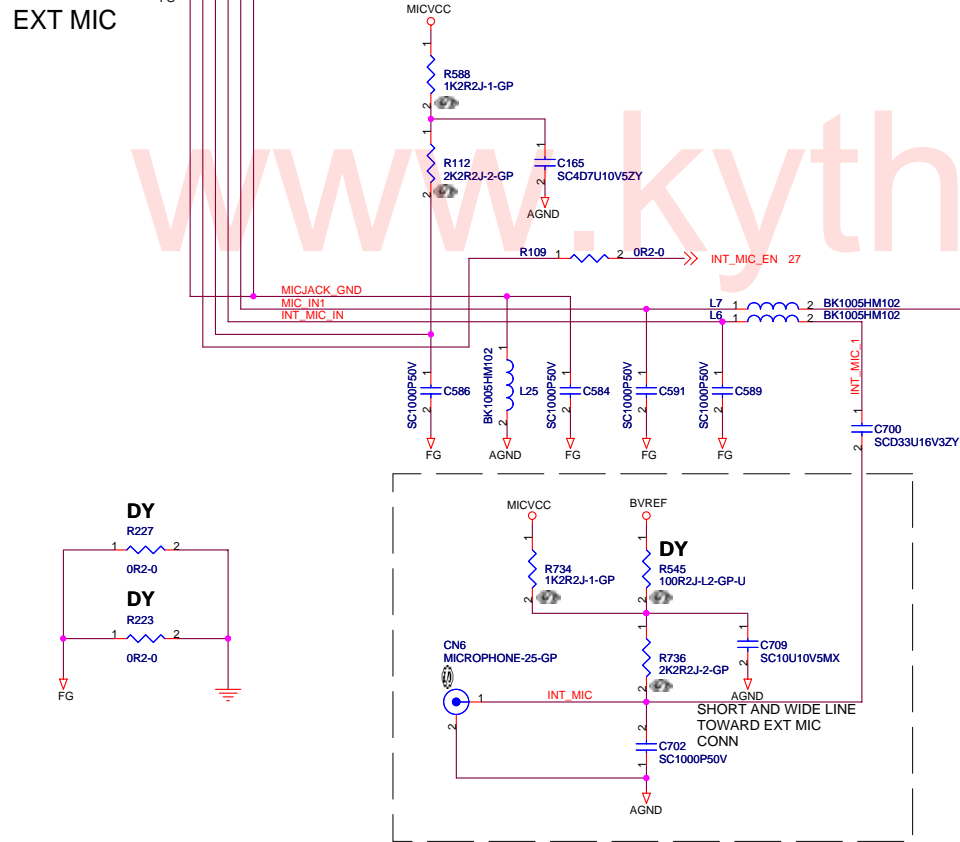
緯創資通 Wistron Corporation
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| | | | | |
|-------|-----------------------|---------|--------------------|-------|
| Title | | | AUDIO AD1981HDJSTZ | |
| Size | Document Number | KS Note | | Rev |
| A3 | | | | SB |
| Date: | Friday, July 22, 2005 | Sheet | 27 | of 73 |

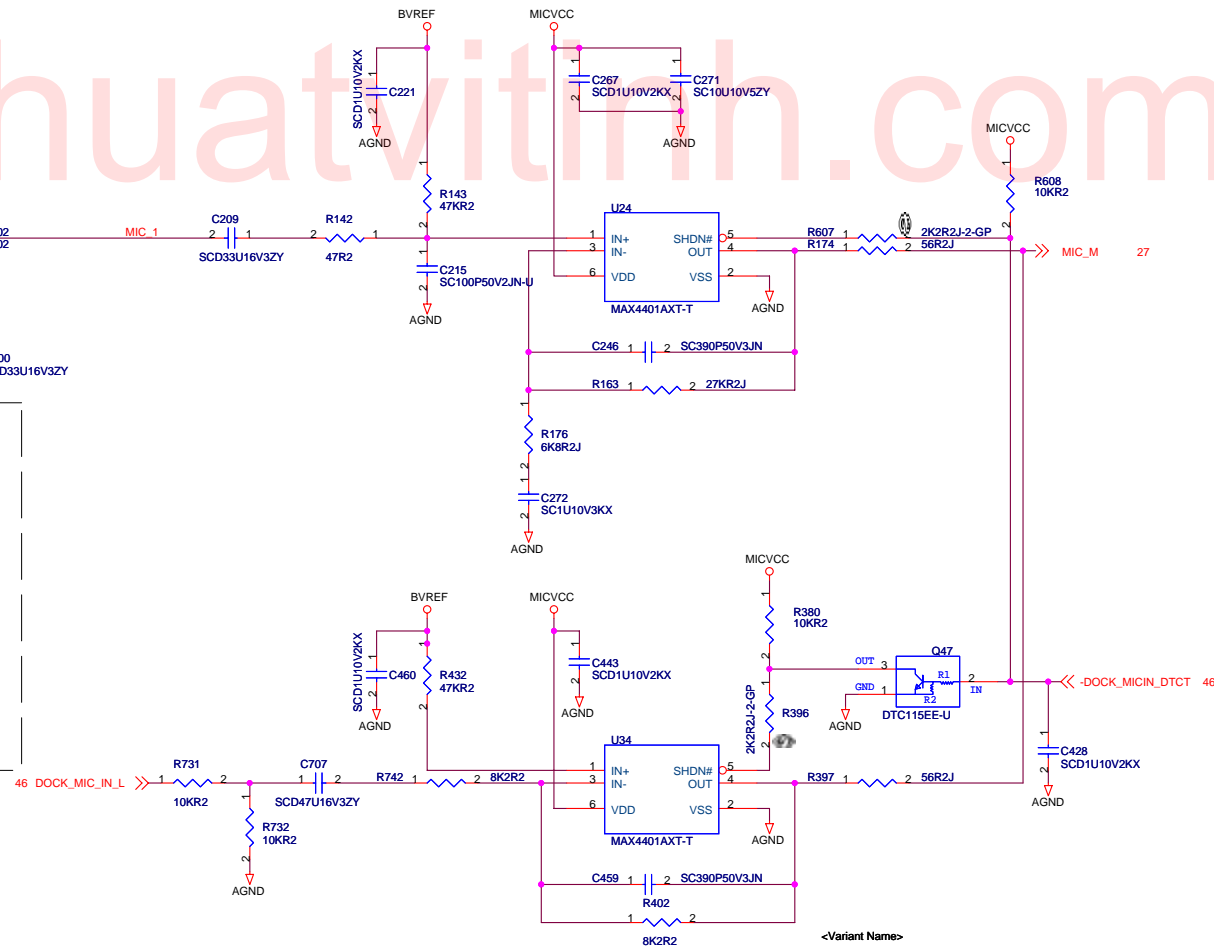
HEADPHONE



EXT MIC



ALL PARTS MUST BE PLACED NEAR TO INTERNAL MICROPHONE

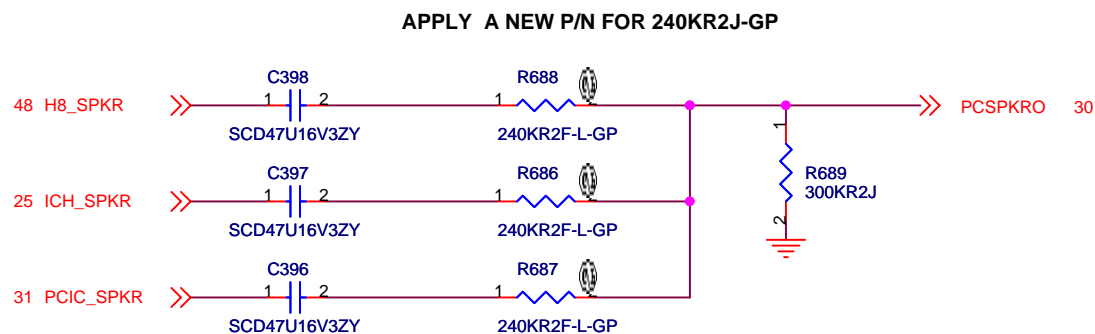
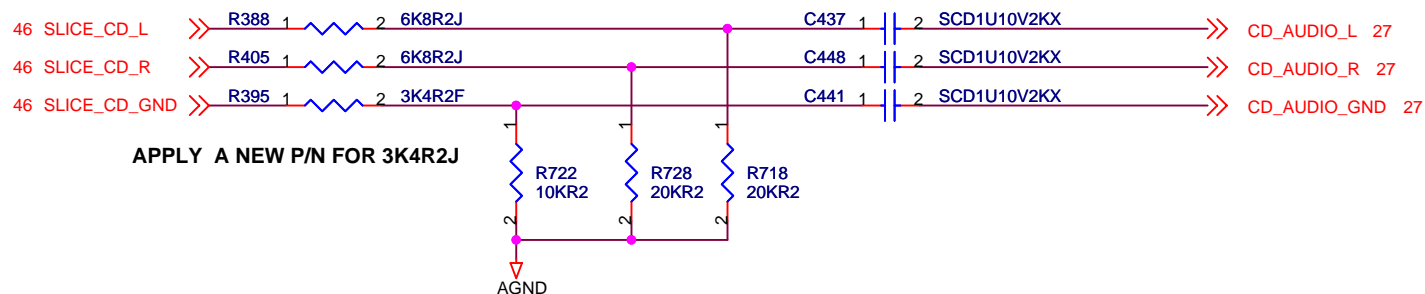
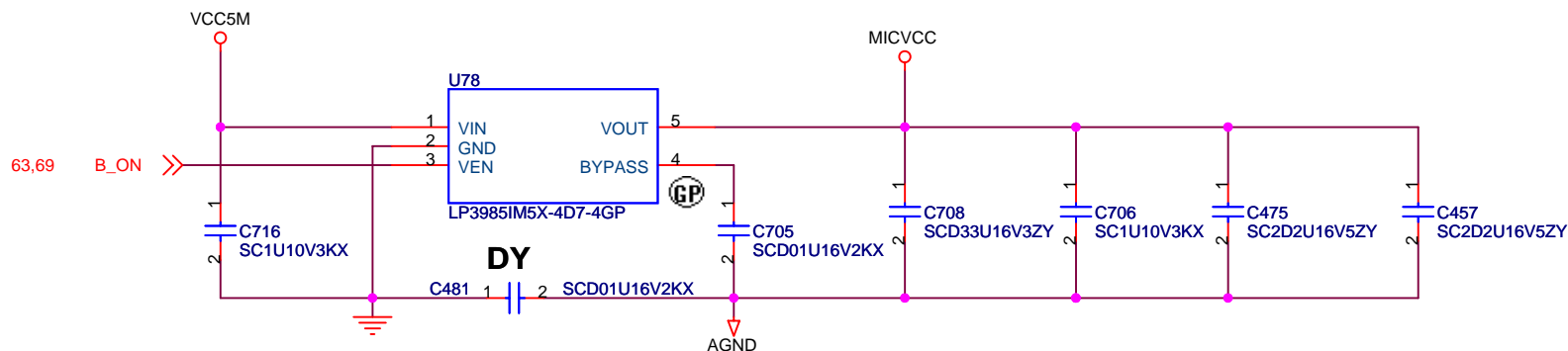


<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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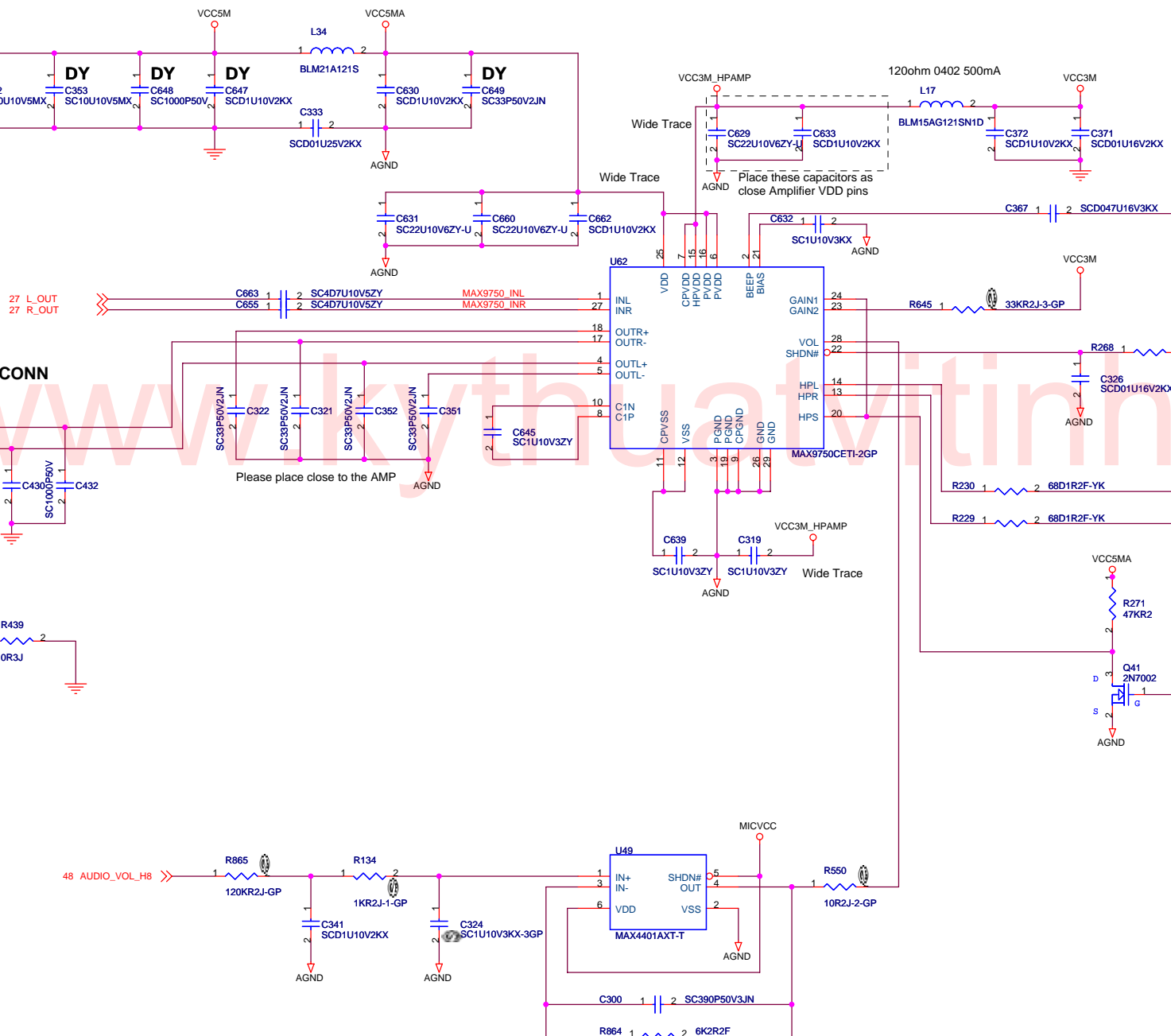
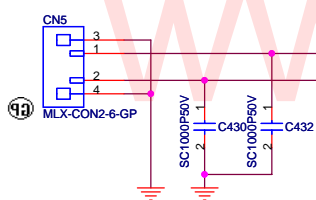
| Title | | | |
|-----------------------------|-----------------|----------|-------|
| AUDIO MIC & CONN | | | |
| Size | Document Number | Rev | |
| Custom | | SB | |
| Date: Friday, July 22, 2005 | | Sheet 28 | of 73 |

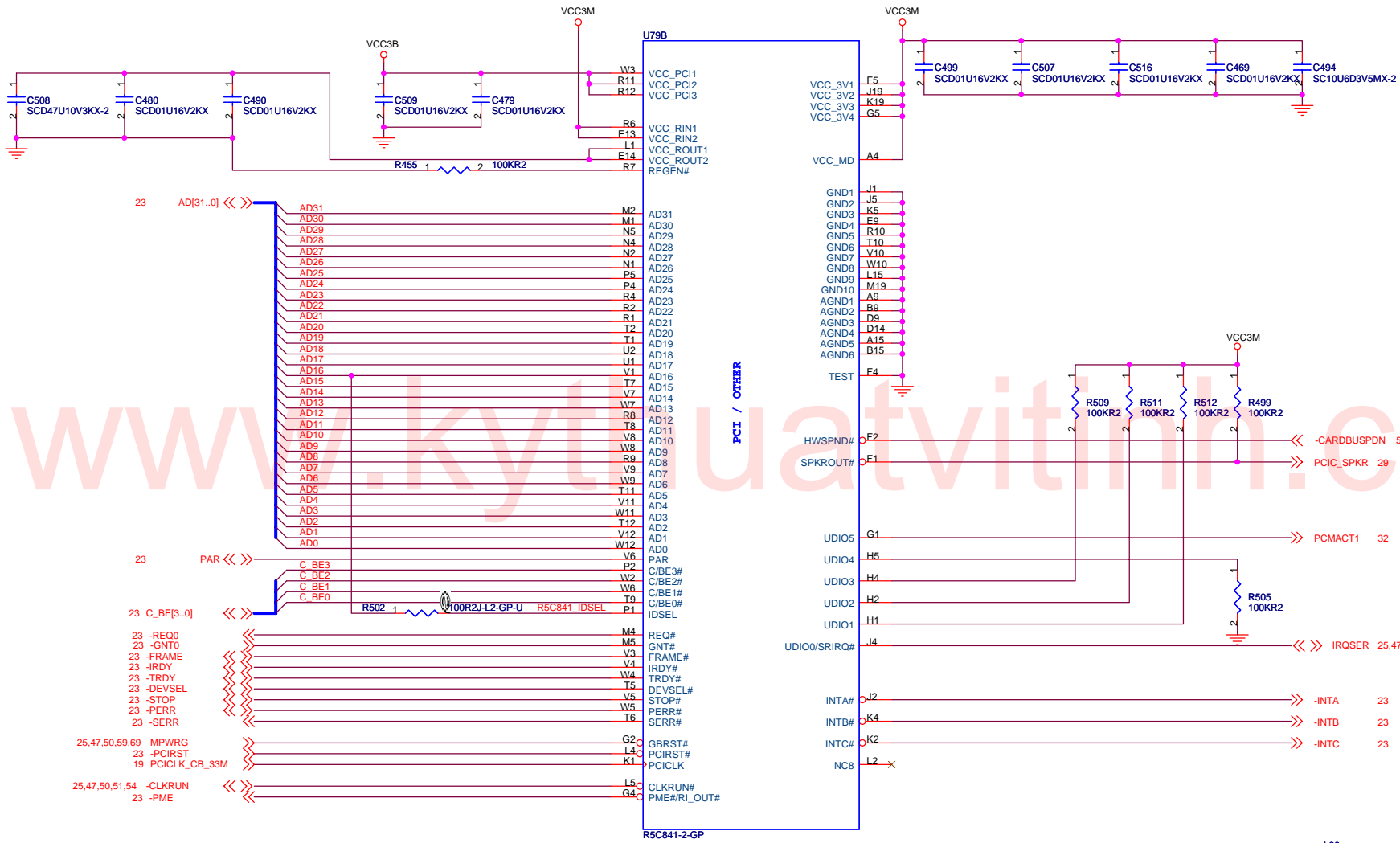


<Variant Name>

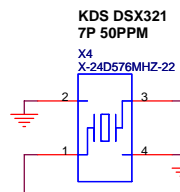
| | |
|--|--------------------------------|
| <div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div> | |
| Title | |
| AUDIO MISC | |
| Size A4 | Document Number KS Note |
| Date: Friday, July 22, 2005 | Rev SB |
| Date: Friday, July 22, 2005 | Sheet 29 of 73 |

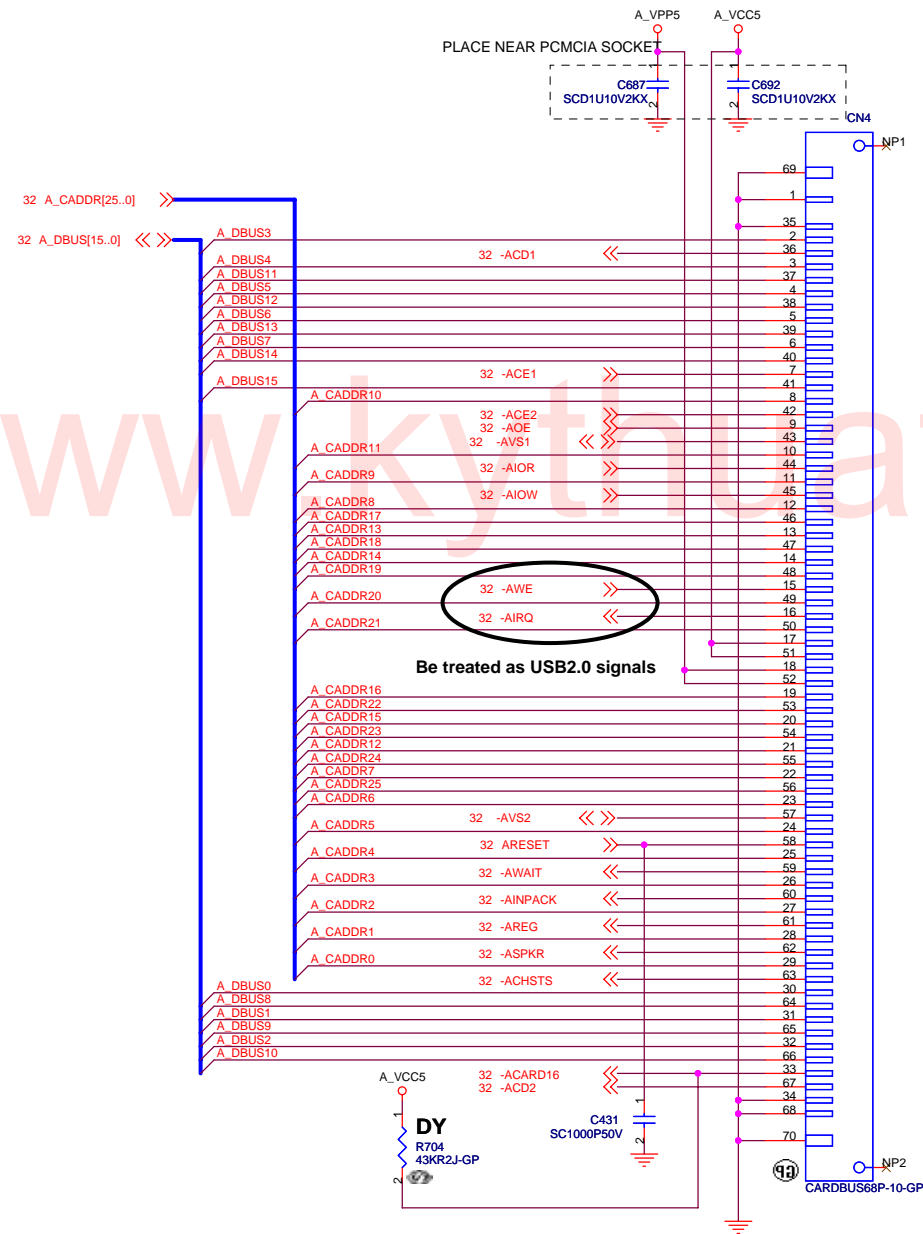
System Speaker CONN

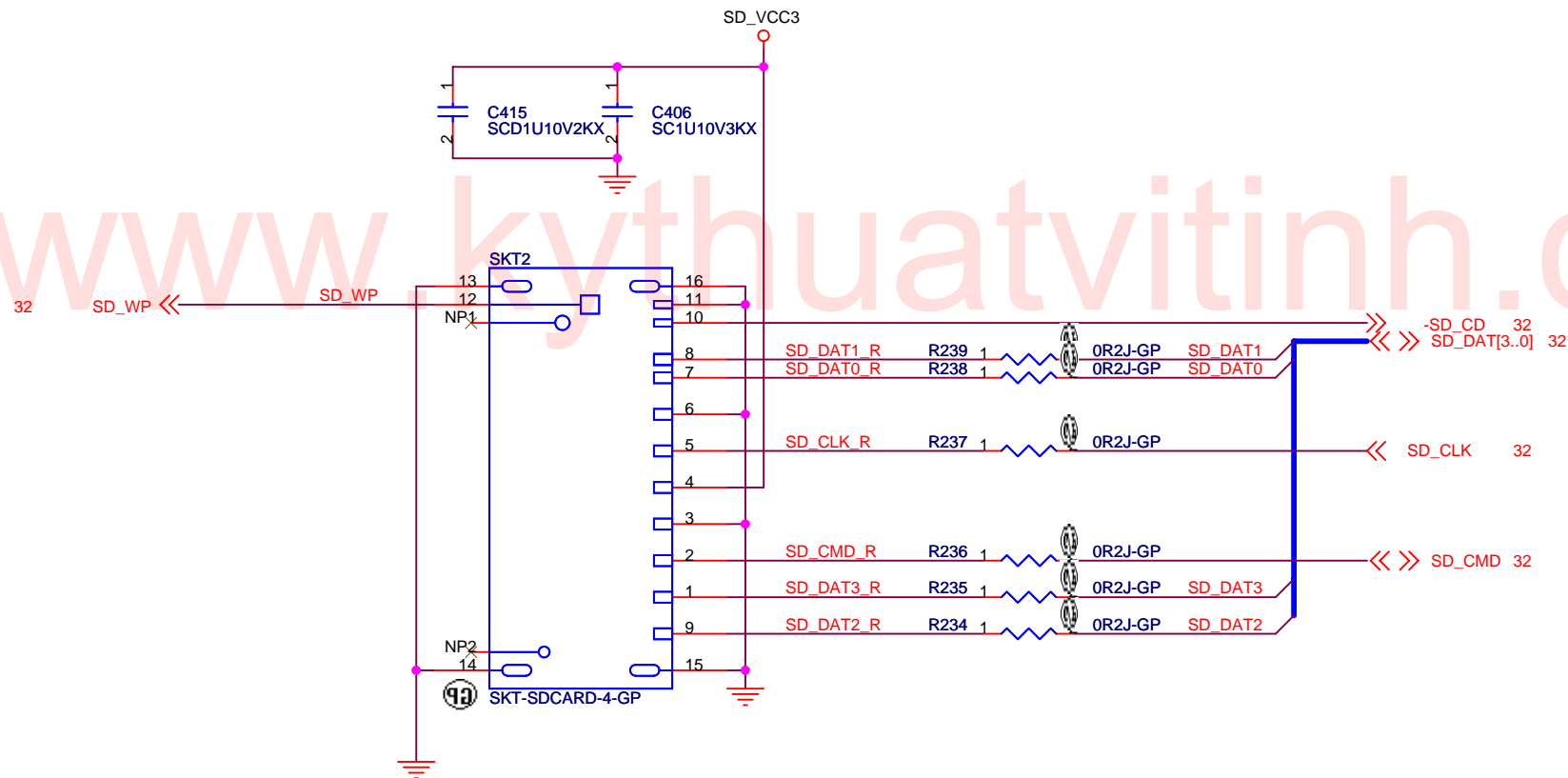




Recommended Conditions
Crystal Oscillator
Normal Frequency : 24.576MHz
Frequency Tolerance : $\pm 50\text{ppm}$ (at 25°C)
Temperature stability : $\pm 50\text{ppm}$ (reference to 25°C)
Operating Temperature Range : -20~70°C
Load Capacitance : 10pF
Driver Level : 0.1mW
Equivalent Series Resistance : 50ohm Max
Insulation resistance : 500M ohm Min (at DC100V \pm 15V)
Shunt Capacitance : 7.0pF Max

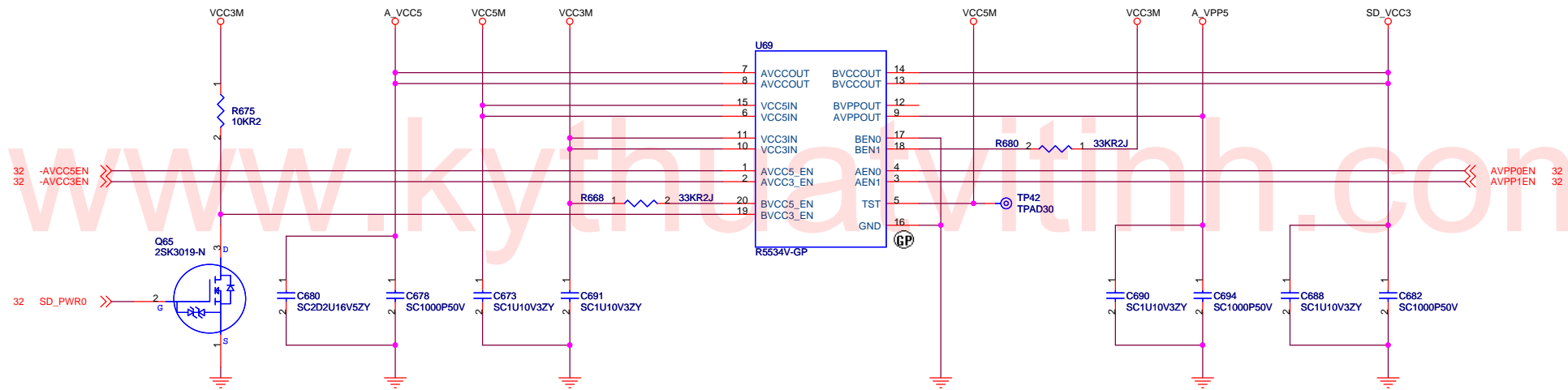






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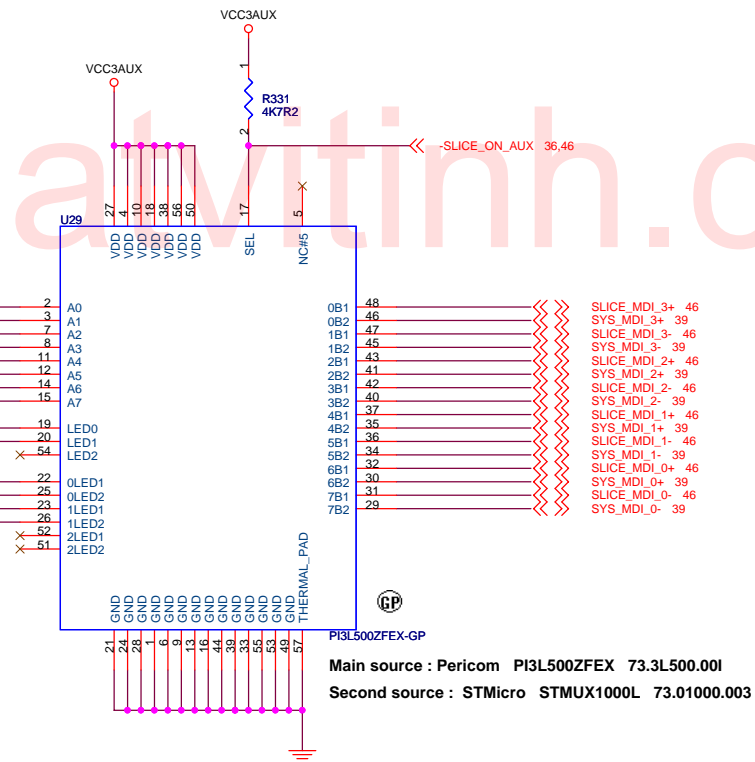
36 MDI_3+
36 MDI_3-
36 MDI_2+
36 MDI_2-
36 MDI_1+
36 MDI_1-
36 MDI_0+
36 MDI_0-

36 -RJ45_ACTIVITY
36 -RJ45_LINKUP

46 -RJ45_ACTIVITY_SLICE
40 -RJ45_ACTIVITY_SYS
46 -RJ45_LINKUP_SLICE
40 -RJ45_LINKUP_SYS

INSTALL 1% 49 OHM RESISTORS WITH
INTEL TEKOA 82573M

THESE R AND C SHOULD BE PLACED NEAR PHY CHIP(82573M)



<Variant Name>

緯創資通 **Wistron Corporation**
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Title

GBE LAN SW

Size

Document Number

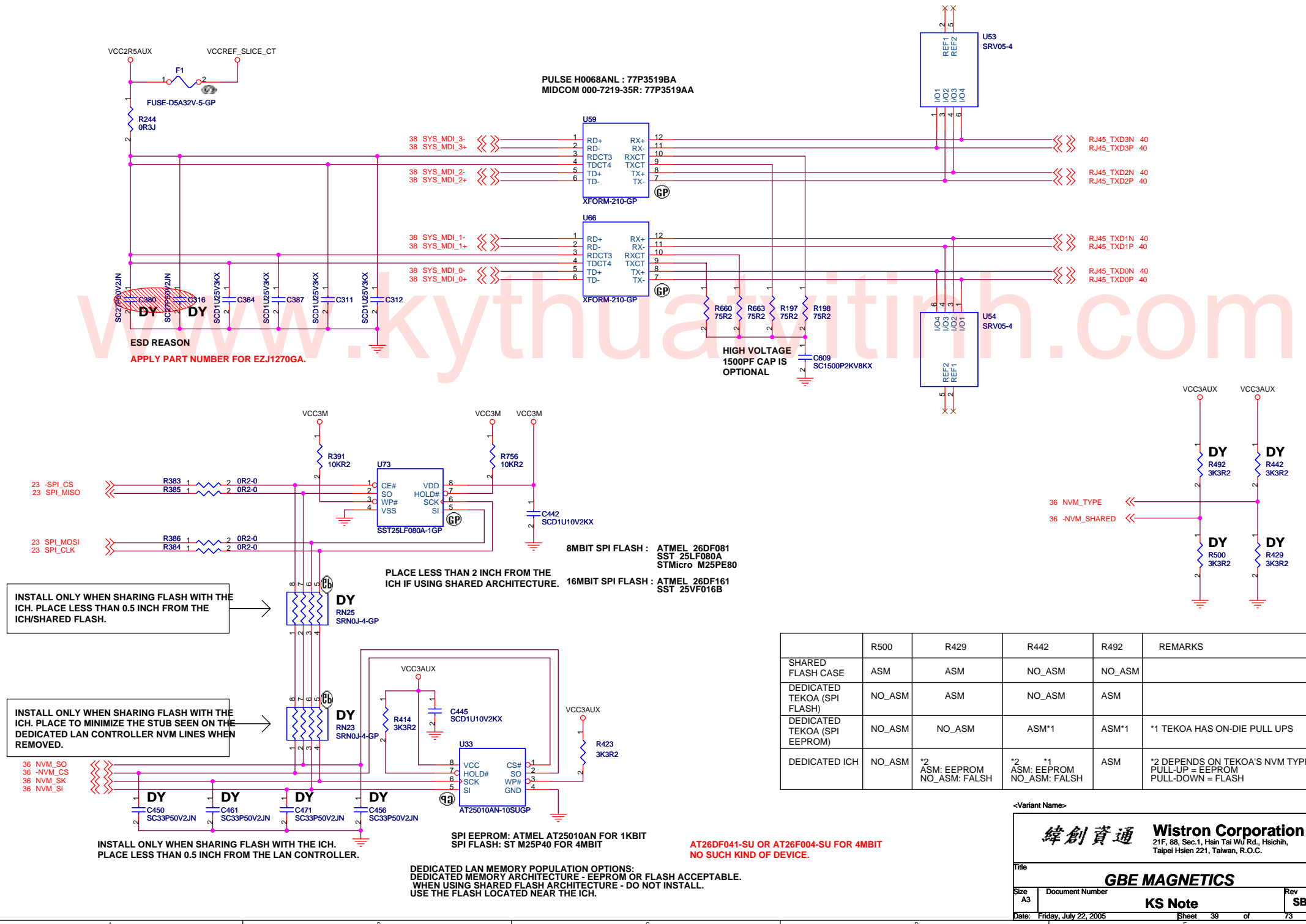
KS Note

Rev

SB

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INSTALL ONLY WHEN SHARING FLASH WITH THE ICH. PLACE LESS THAN 0.5 INCH FROM THE ICH/SHARED FLASH.

INSTALL ONLY WHEN SHARING FLASH WITH THE ICH. PLACE TO MINIMIZE THE STUB SEEN ON THE DEDICATED LAN CONTROLLER NVM LINES WHEN REMOVED.

36 NVM_SO
36 -NVM_CS
36 NVM_SK
36 NVM_SI

INSTALL ONLY WHEN SHARING FLASH WITH THE ICH. PLACE LESS THAN 0.5 INCH FROM THE LAN CONTROLLER.

SPI EEPROM: ATME AT25010AN FOR 1KBIT
SPI FLASH: ST M25P40 FOR 4MBIT
AT26DF041-SU OR AT26F004-SU FOR 4MBIT
NO SUCH KIND OF DEVICE.

DEDICATED LAN MEMORY POPULATION OPTIONS:
DEDICATED MEMORY ARCHITECTURE - EEPROM OR FLASH ACCEPTABLE.
WHEN USING SHARED FLASH ARCHITECTURE - DO NOT INSTALL.
USE THE FLASH LOCATED NEAR THE ICH.

| | R500 | R429 | R442 | R492 | REMARKS |
|------------------------------|--------|---------------------------------|---------------------------------|--------|---|
| SHARED FLASH CASE | ASM | ASM | NO_ASM | NO_ASM | |
| DEDICATED TEKOA (SPI FLASH) | NO_ASM | ASM | NO_ASM | ASM | |
| DEDICATED TEKOA (SPI EEPROM) | NO_ASM | NO_ASM | ASM*1 | ASM*1 | *1 TEKOA HAS ON-DIE PULL UPS |
| DEDICATED ICH | NO_ASM | *2 ASM: EEPROM NO_ASM: FALSH | *2 ASM: EEPROM *1 ASM: FALSH | ASM | *2 DEPENDS ON TEKOA'S NVM TYPE : PULL-UP = EEPROM PULL-DOWN = FLASH |

Variant Name:

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Title

GBE MAGNETICS

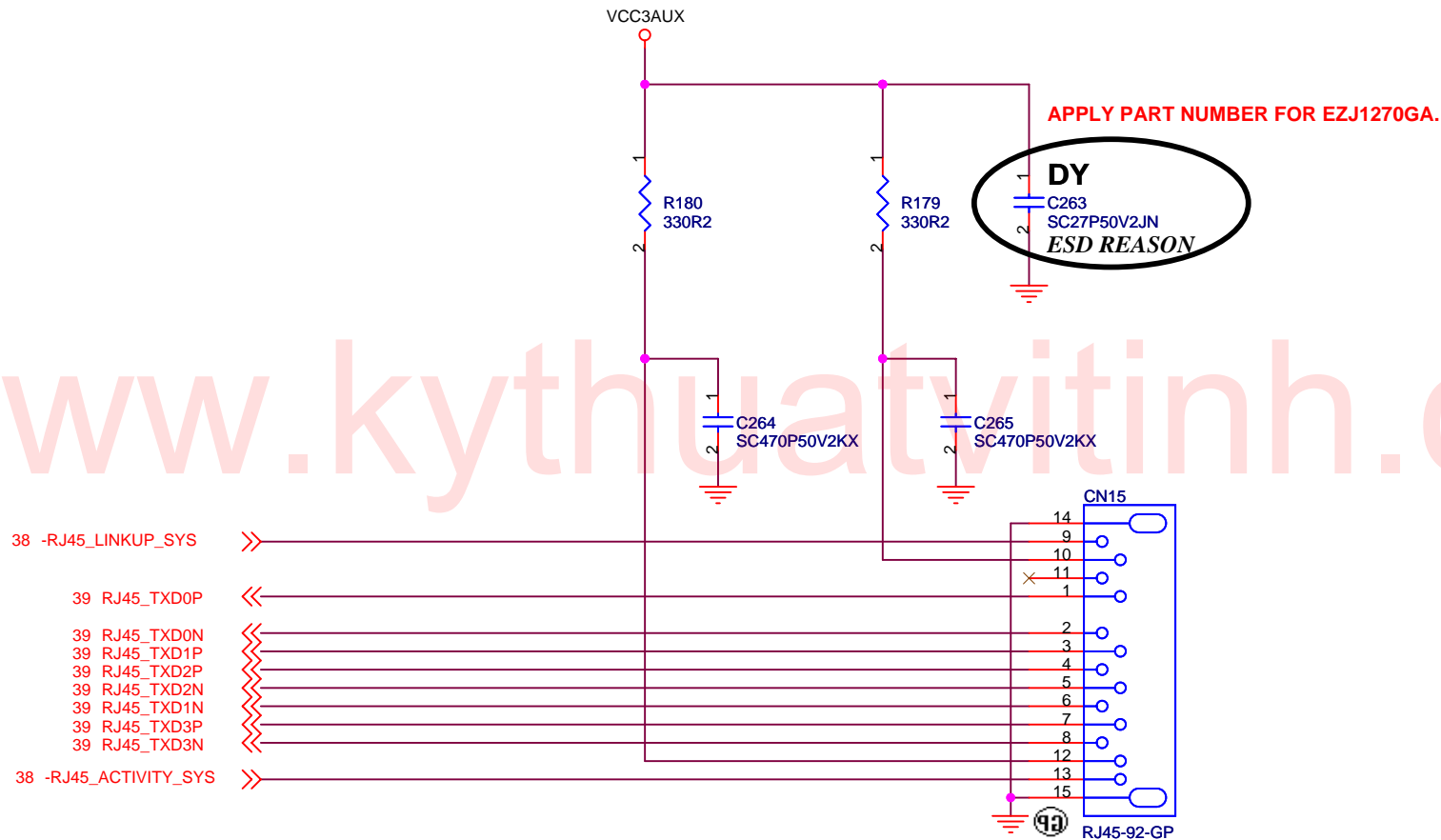
Size A3

Document Number

Rev SB

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<Variant Name>

緯創資通

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Title

RJ11/RJ45 CONN

Size
A4

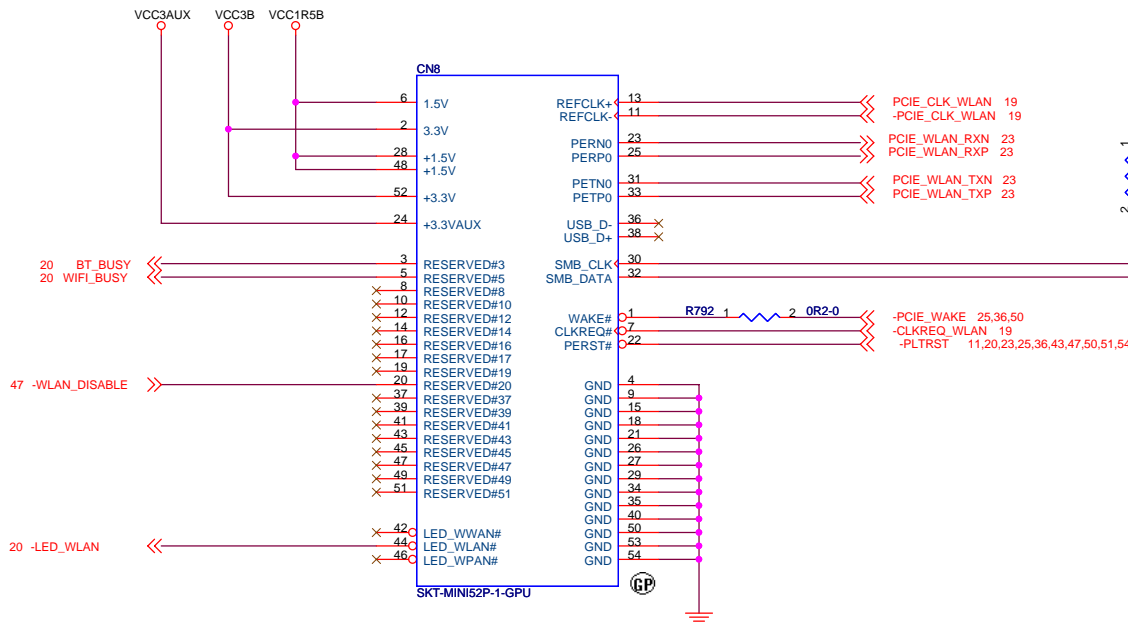
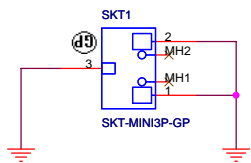
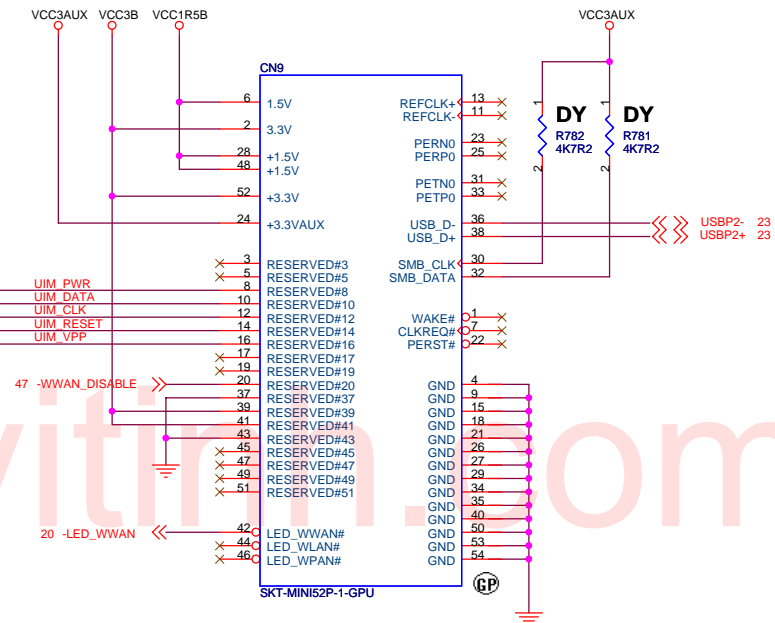
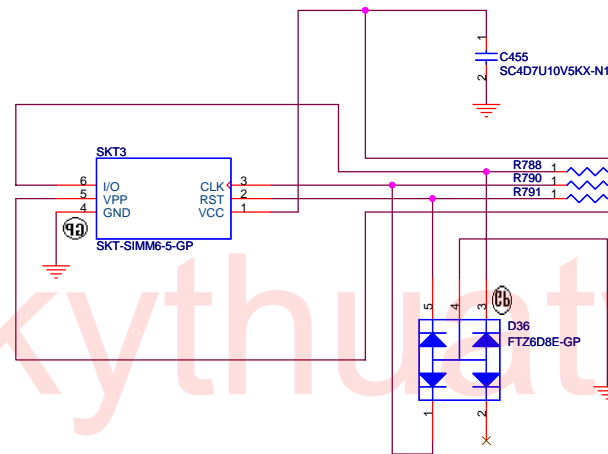
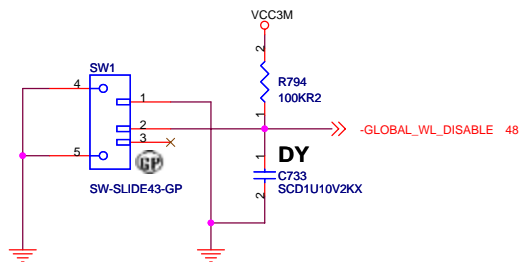
Document Number

KS Note

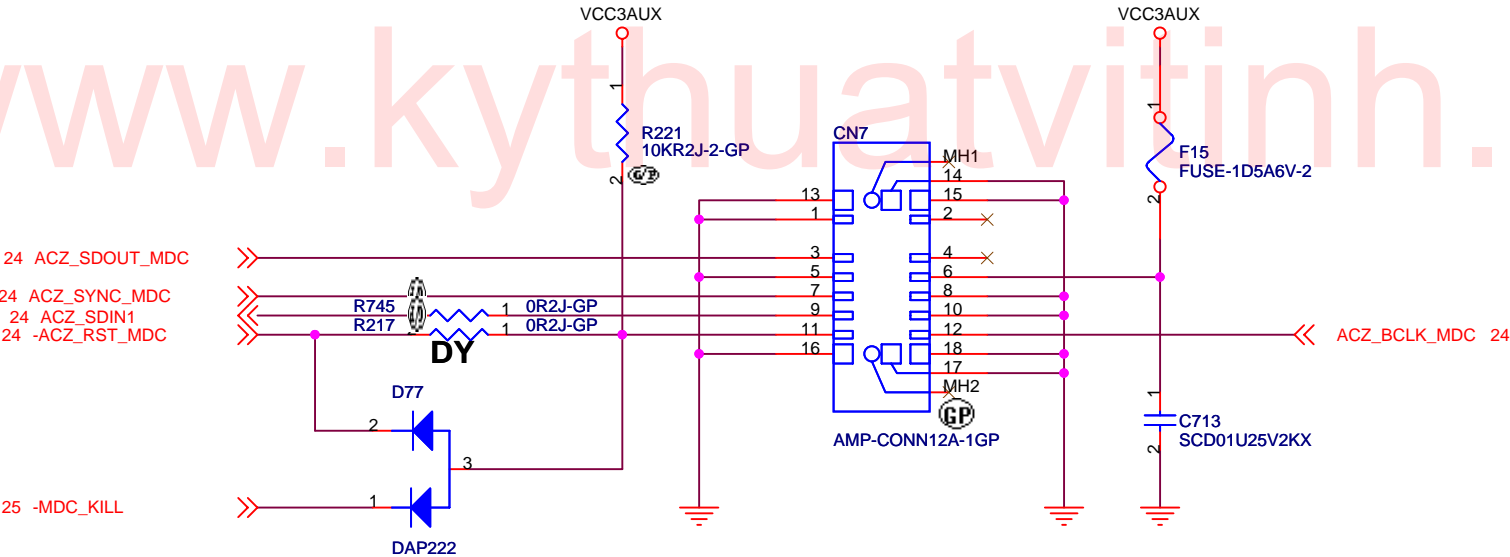
Rev
SB

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MDC CONN



<Variant Name>

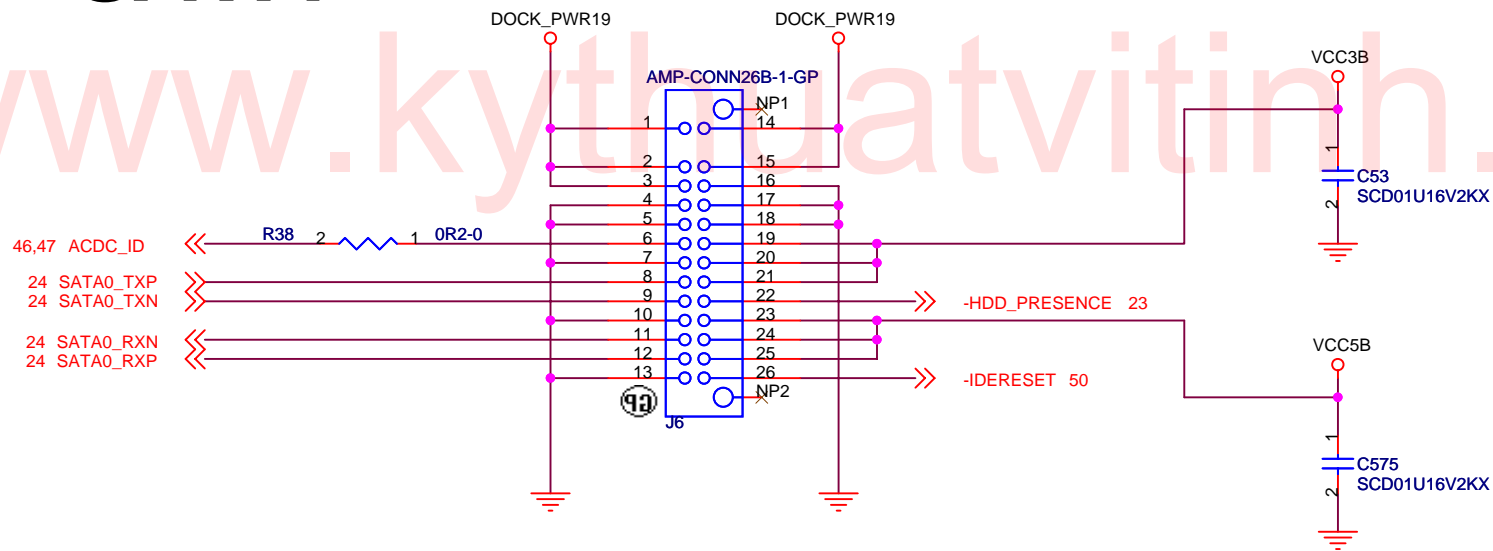
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
MDC 1.5 CONNECTOR

Size A4 Document Number KS Note Rev SB

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SATA



<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title

Board To Board CONN

Size
A4

Document Number

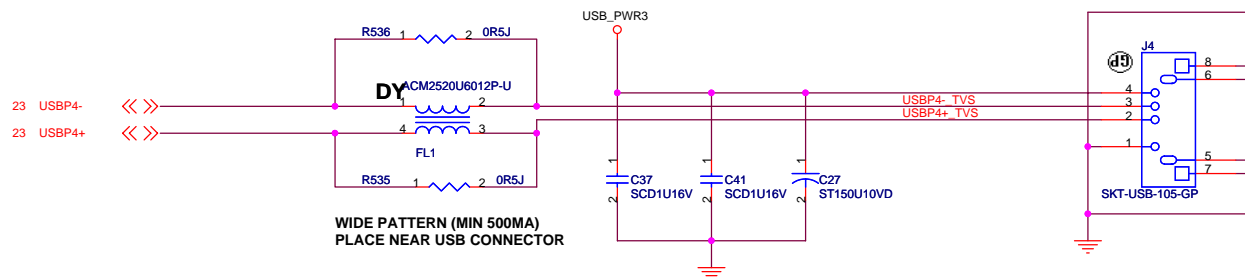
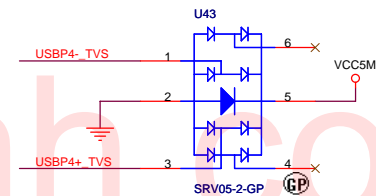
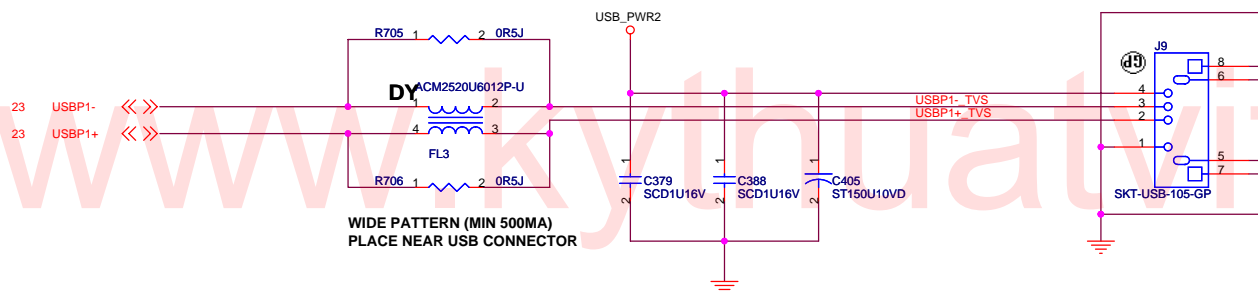
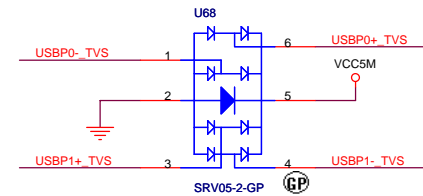
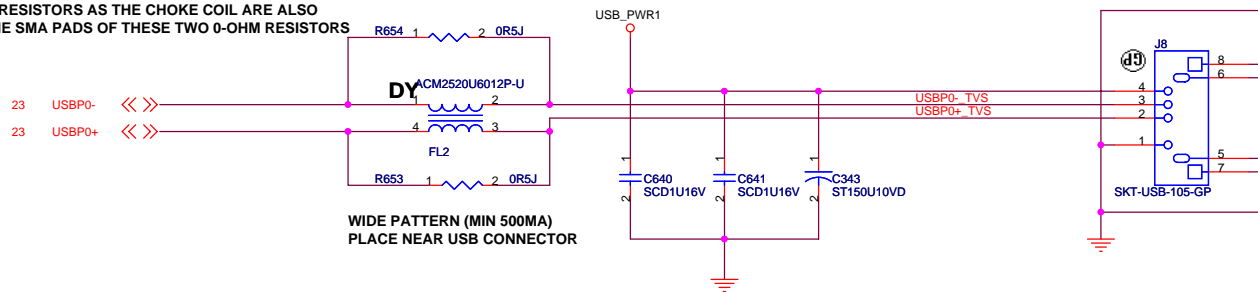
KS Note

Rev
SB

Date: Friday, July 22, 2005

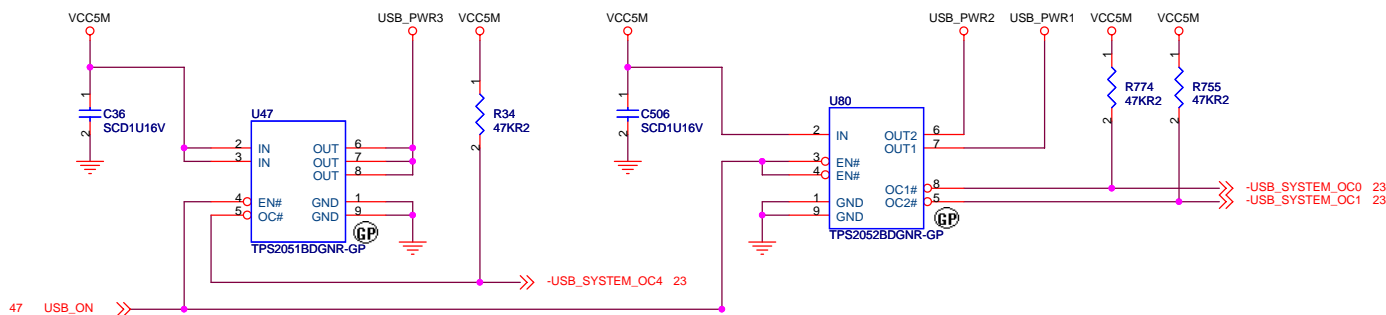
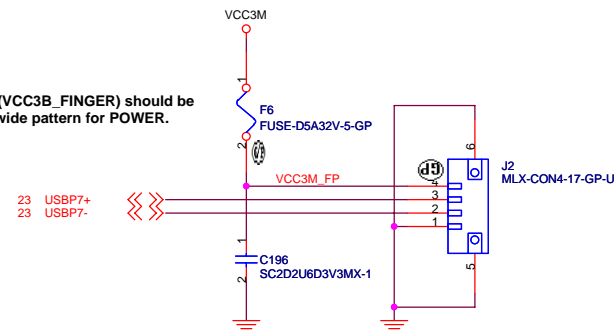
Sheet 44 of 73

COMMON MODE CHOKE COIL: TDK ACM2520U
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO
PLACED ON THE SMA PADS OF THESE TWO 0-OHM RESISTORS



Fingerprint

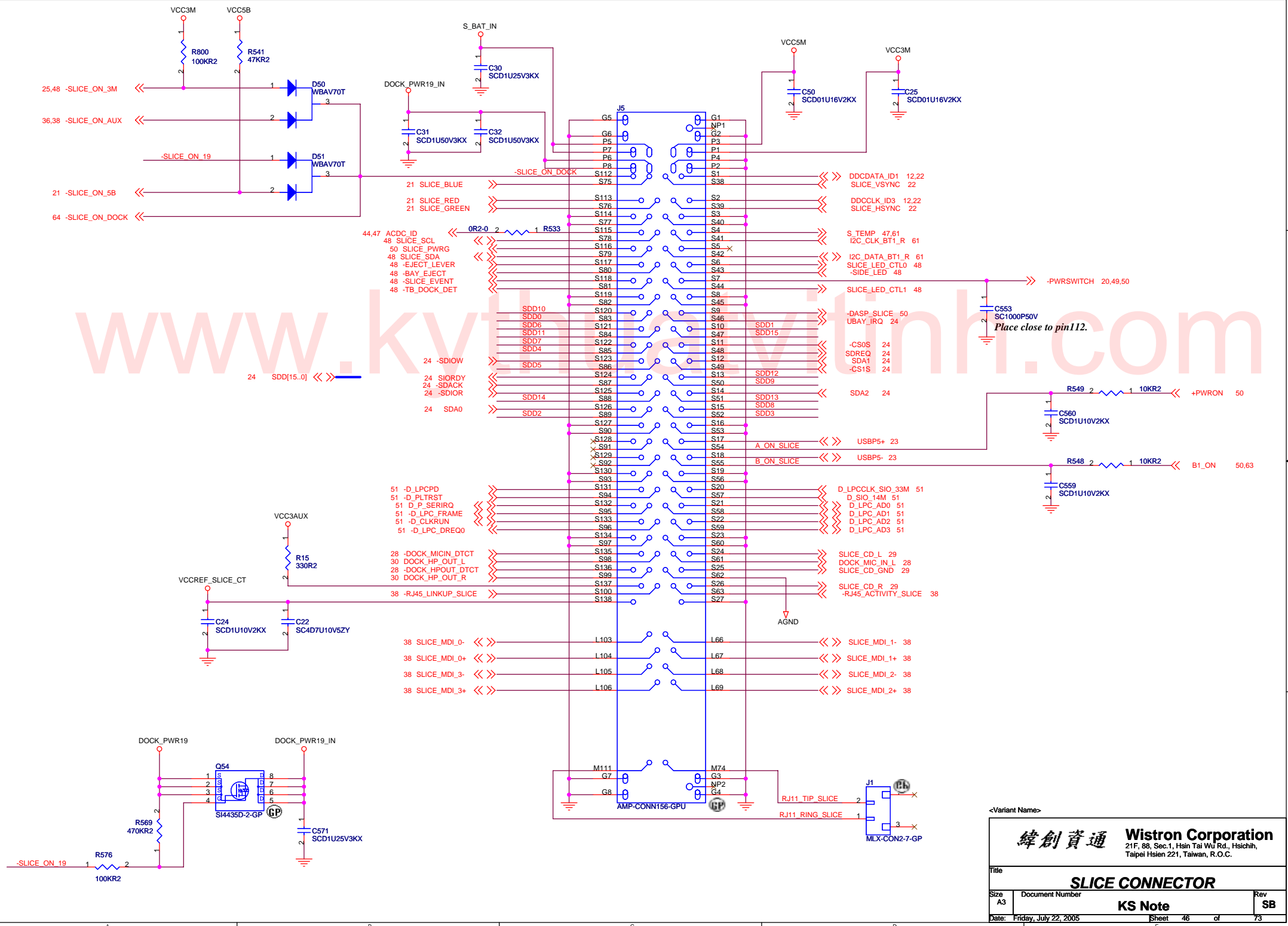
This trace (VCC3B_FINGER) should be
routed by wide pattern for POWER.

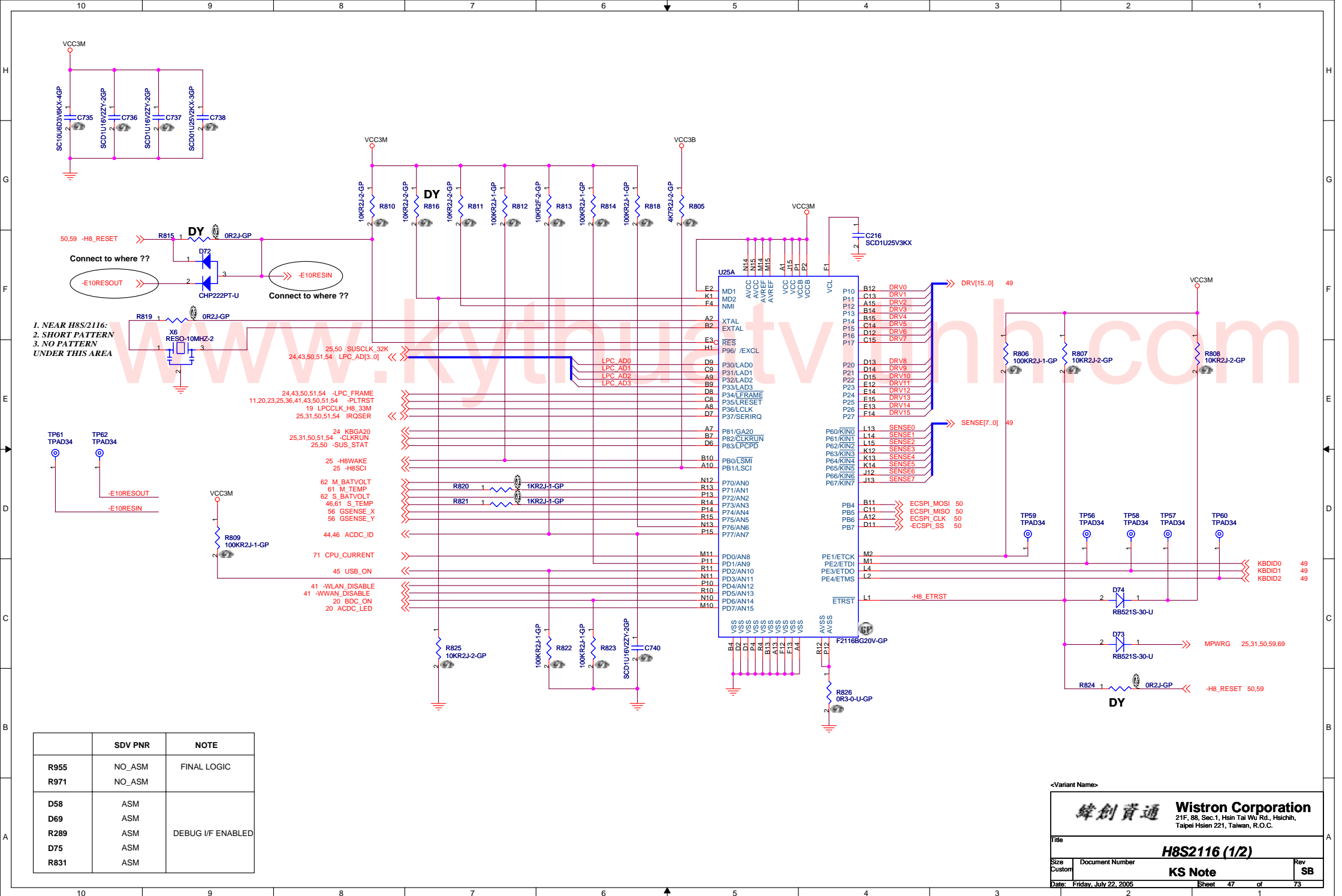


緯創資通

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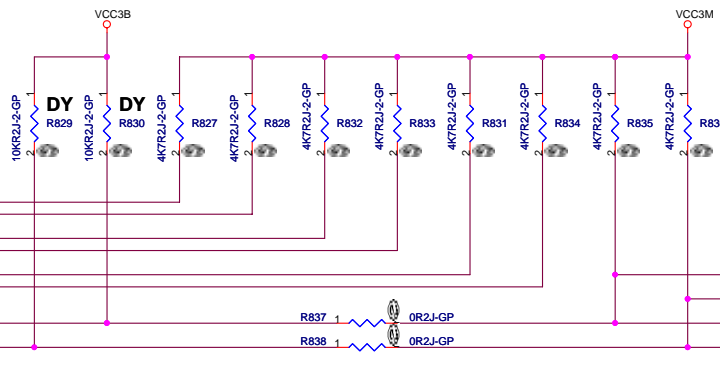
| USB POWER / CONNECTOR | | | |
|-----------------------------|-----------------|---------|-----|
| Title | Document Number | KS Note | Rev |
| Size A3 | | | SB |
| Date: Friday, July 22, 2005 | Sheet 45 | of 73 | |



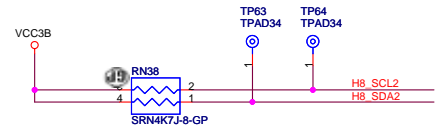


| | SDV PNR | NOTE |
|-----------------------------------|---------------------------------|-------------------|
| R955 R971 | NO_ASM NO_ASM | FINAL LOGIC |
| D58 D69 R289 D75 R831 | ASM ASM ASM ASM ASM | DEBUG I/F ENABLED |

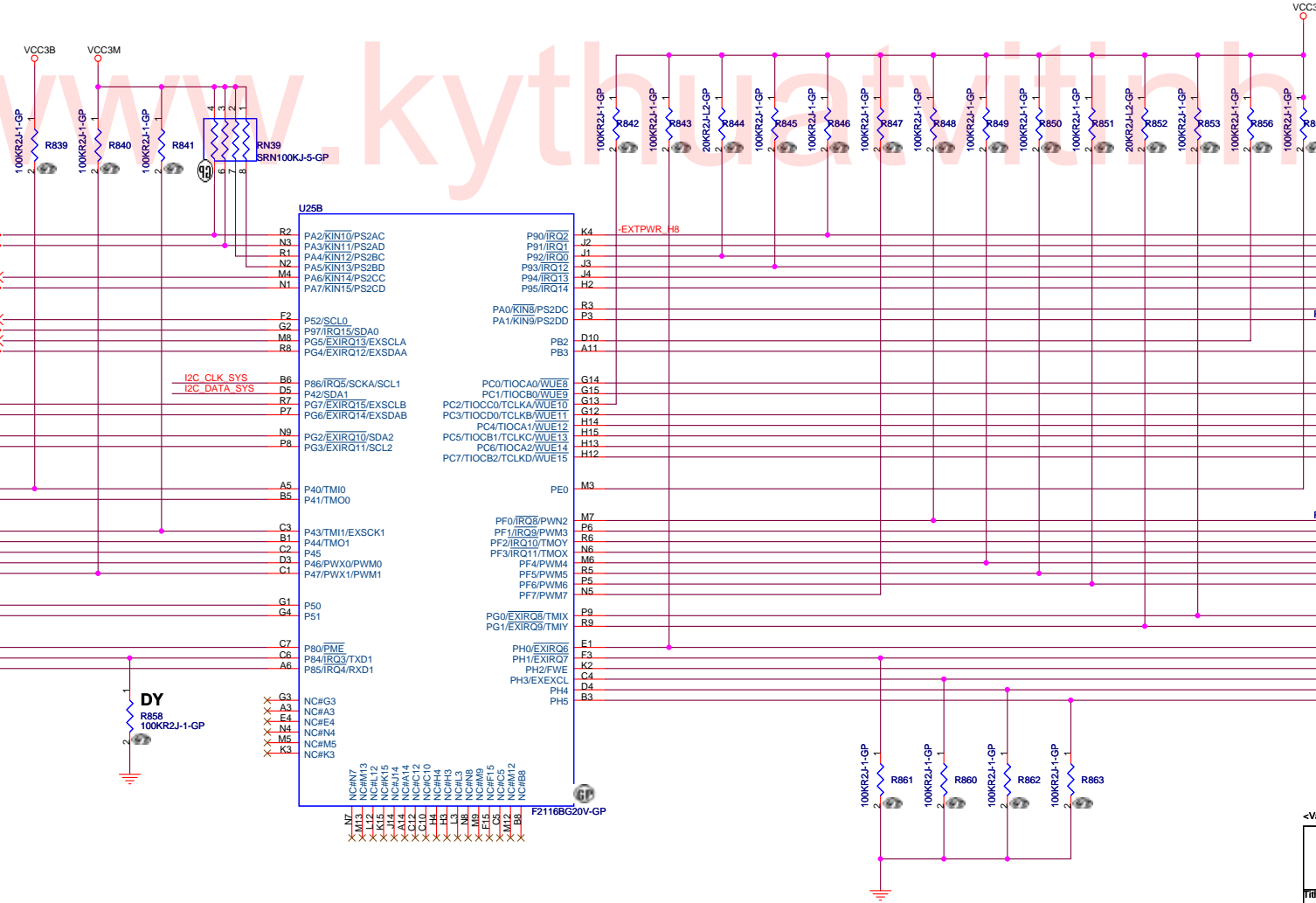
46 SLICE_SCL
46 SLICE_SDA
61 I2C_CLK_BT0
61 I2C_DATA_BT0
61 I2C_CLK_BT1
61 I2C_DATA_BT1
71 I2C_CLK_PS
71 I2C_DATA_PS



H8 I2C BUS2
TEST PAD X2
BOTTOM SIDE
DO NOT MOVE AFTER FIX



46 -TB_DOCK_DET
46 -EJECT_LEVER
49 IPDCLK
49 IPDDATA
61 I2C_CLK_BT0
61 I2C_DATA_BT0
61 I2C_CLK_BT1
61 I2C_DATA_BT1
46 SLICE_SCL
46 SLICE_SDA
53 H8_SDA2
53 H8_SCL2
24 -KBRC
29 H8_SPKR
25 -BATLOW
55 FAN_ON
62 BATMON_EN
30 AUDIO_VOL_H8
59 CHARGE_CURRENT_1DBA
46 SLICE_LED_CTL0
46 SLICE_LED_CTL1
25 46 -SLICE_ON_3M
12.69 PANEL_POWER_ON
11.25,50.53,54.69 BPWRG



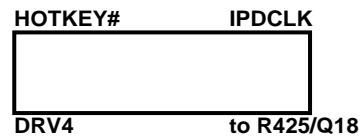
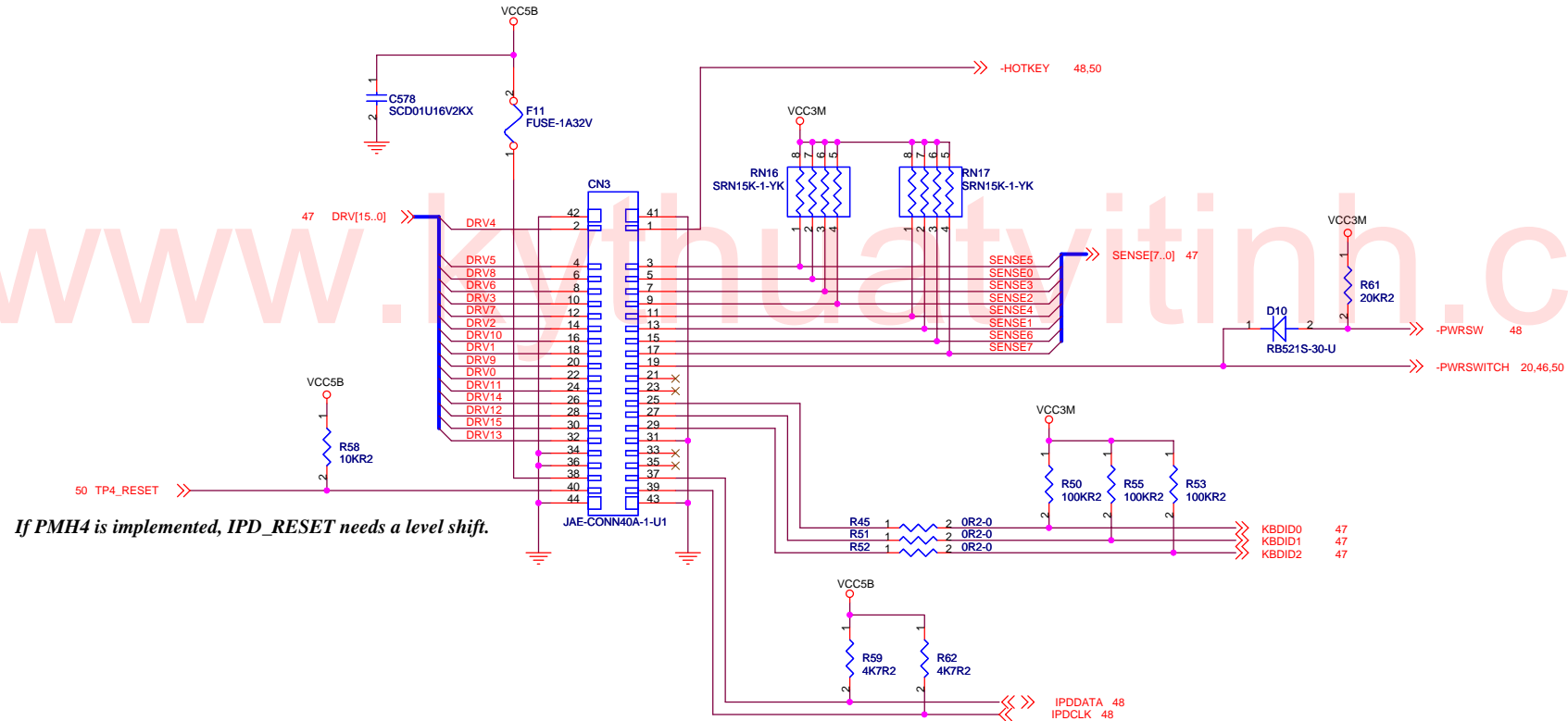
D75 2 1 RB521S-30-U
D76 2 1 RB521S-30-U
R854 1 560R2J-N4-GP
R855 1 560R2J-N4-GP
-EXTPWWR_H8
-PWRSW_H8 25
FAN_FRQ 55
-MISCMI 50
-LPMODE 72
LEDSECURITY 20
LEDNUMLOCK 20
LEDCPSLOCK 20
-SIDE_LED 46
LID_SWITCH 20
KBD_LIGHT_ON 20
-GSENSE_ON 56
GSENSE_TST 56
CHARGE_VOLT_402V 59
CHARGE_CELL0 59
CHARGE_CELL1 59
CHARGE_VOLT_4035V 59
-SLICE_EVENT 46
-BAY_EJECT 46
GPIO_BT 20
-MAINOFF 50
-SPK_MUTE 30
-MUTE 30
DISCHARGE 58.69

<Variant Name>

緯創資通 Wistron Corporation
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| | | | |
|-----------------------------|--|----------------|--|
| Title | | H8S2116(2/2) | |
| Size | | KS Note | |
| Custom | | Rev SB | |
| Date: Friday, July 22, 2005 | | Sheet 48 of 73 | |

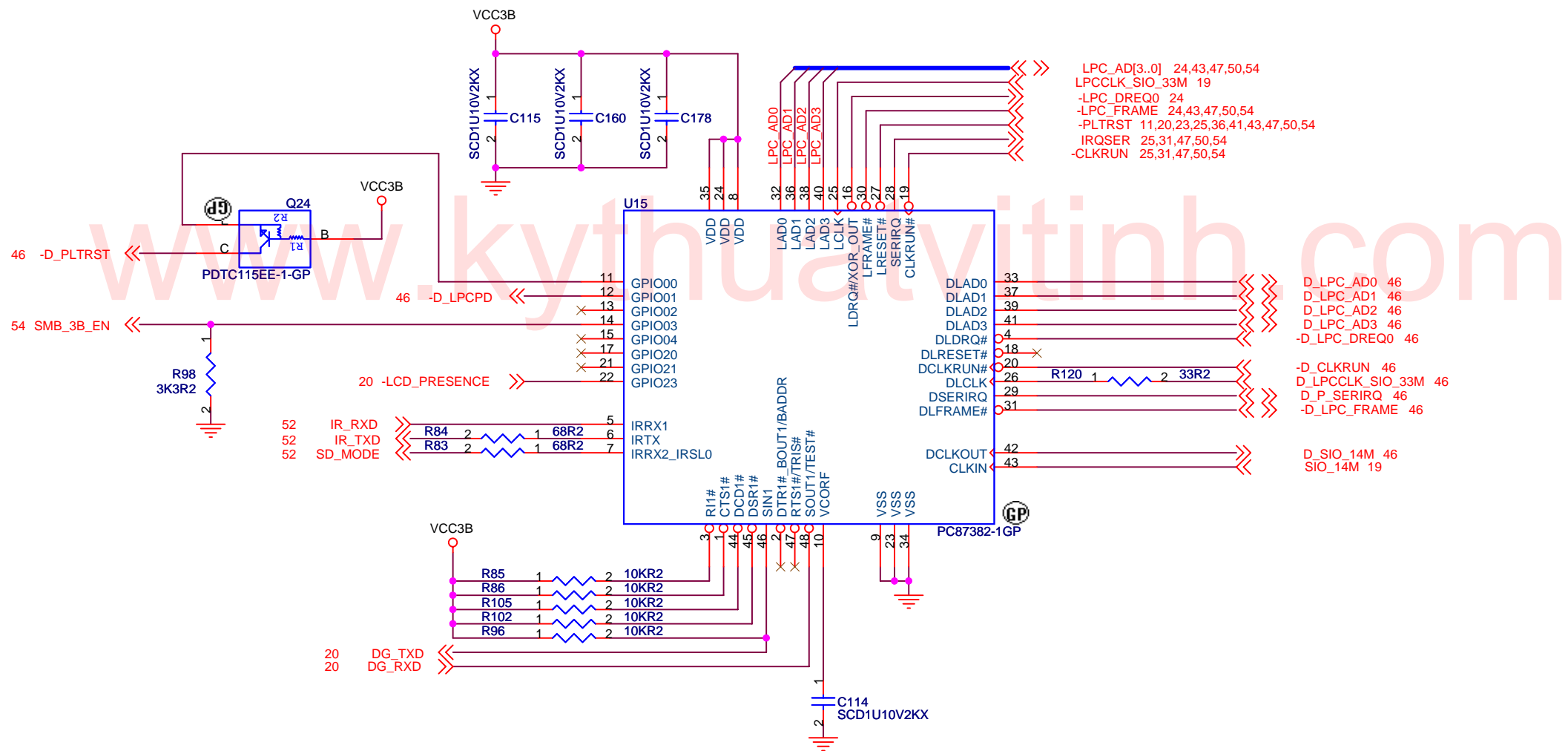
Keyboard Connector



Keyboard Connector Top View

<Variant Name>

| | | | |
|-----------------------------|-----------------|---|------------------|
| 緯創資通 | | Wistron Corporation | |
| | | 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C. | |
| Title | | | |
| KEYBOARD CONN | | | |
| Size A3 | Document Number | KS Note | Rev SB |
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<Variant Name>

緯創資通

Wistron Corporation

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Taipei Hsien 221, Taiwan, R.O.C.

Title

SIO NS87382

Size
A4

Document Number

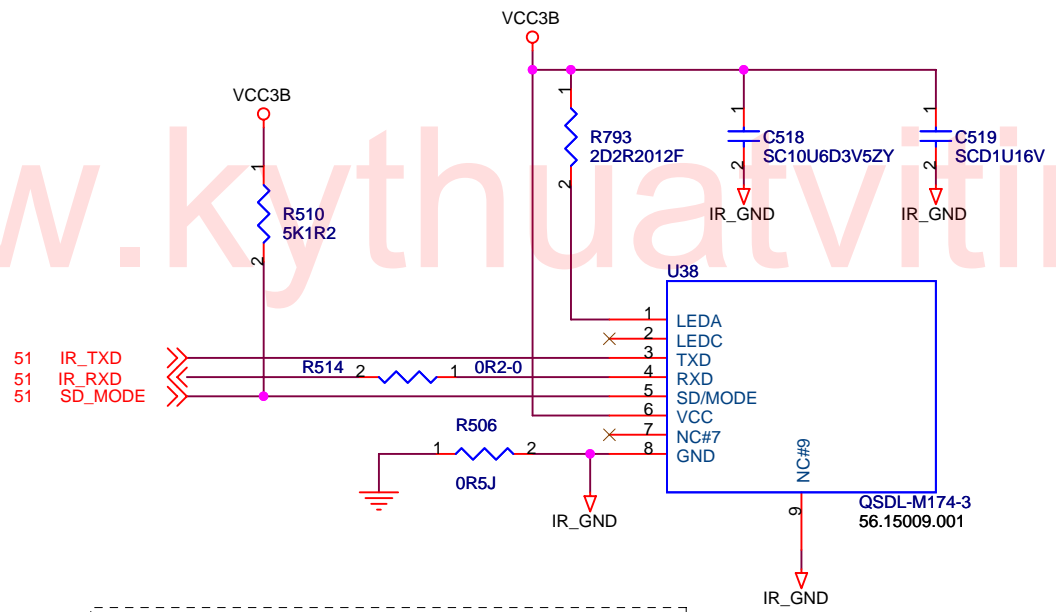
KS Note

Rev
SB

Date: Friday, July 22, 2005

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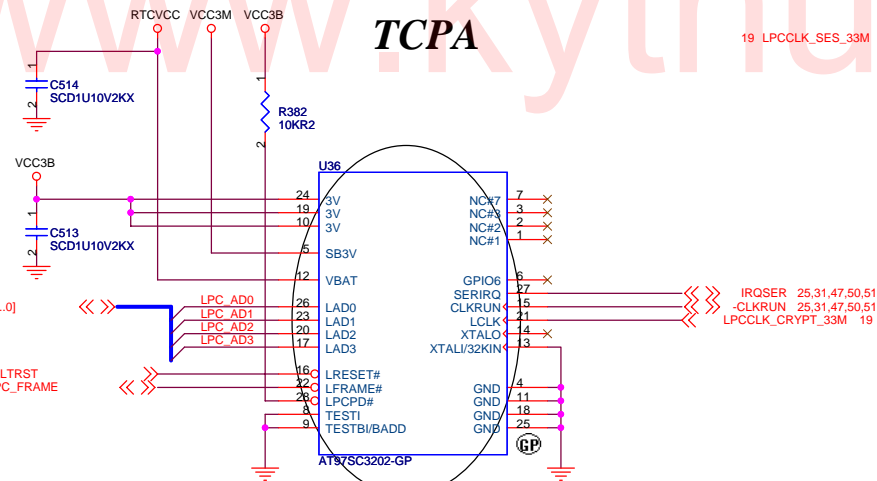
SHOULD QUALIFY BELOW PARTS
PRIMATY ALIGENT QSDL-M174-1 : 56.15009.001
SECOND VISHAY TFDU6102 : 56.15001.051

<Variant Name>

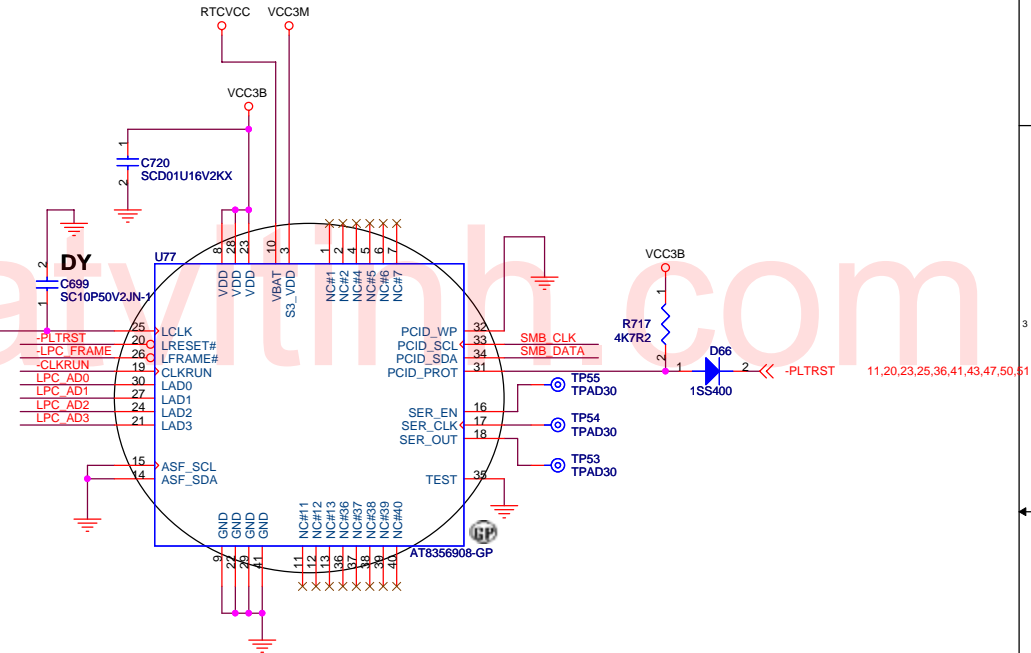
| | | | |
|-----------------------------|-----------------|--|-----------|
| 緯創資通 | | Wistron Corporation | |
| | | 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C. | |
| Title | | | |
| IR MODULE | | | |
| Size | Document Number | | Rev |
| A4 | | | SB |
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TATER

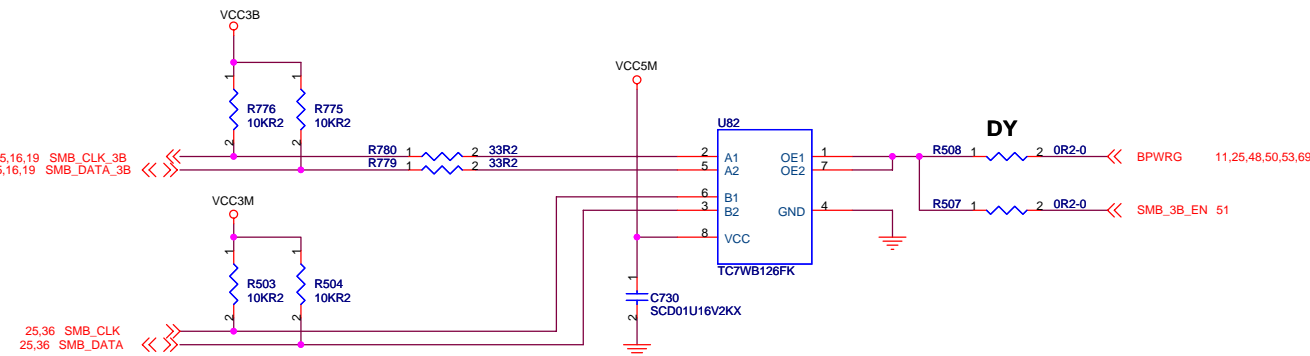
TCPA



Change to ATD97SC3203-XA2T10



Change to AT8356908-DML6X



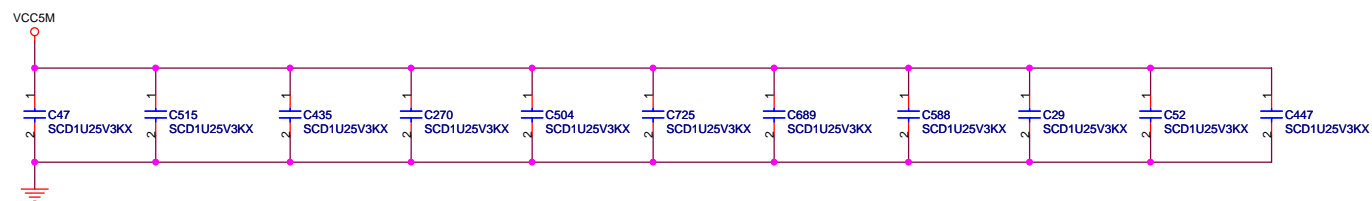
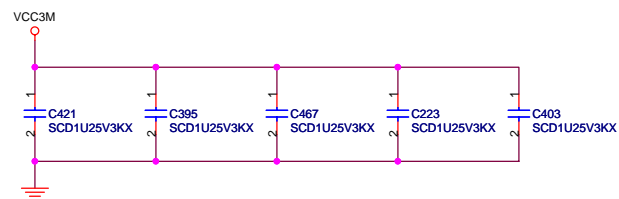
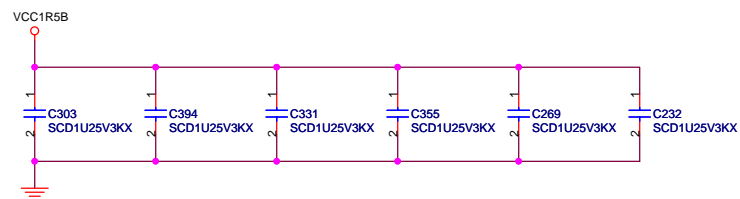
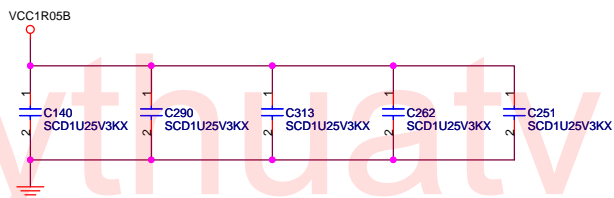
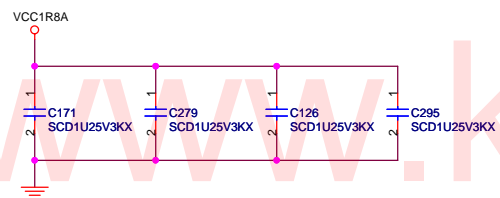
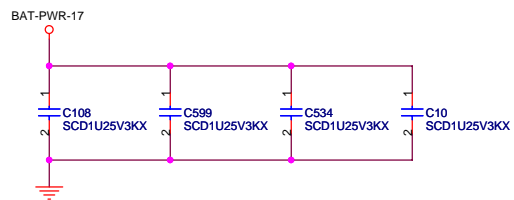
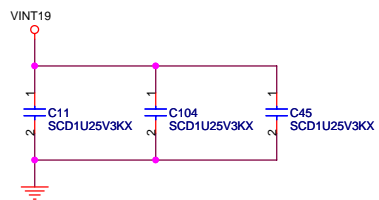
<Variant Name>

緯創資通 Wistron Corporation
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| | | | |
|-------|-----------------------|--------------|----------|
| Title | | I2CBUS/SMBUS | |
| Size | Document Number | KS Note | |
| A3 | | SB | |
| Date: | Friday, July 22, 2005 | Sheet | 54 of 73 |



| KS-NOTE | ADI | STMicro | No Accel. |
|---------|--------|---------|-----------|
| R671 | ASM | NO_ASM | NO_ASM |
| R677 | 56K | 56K | NO_ASM |
| C679 | ASM | ASM | NO_ASM |
| R672 | 56K | 56K | NO_ASM |
| C675 | ASM | ASM | NO_ASM |
| R266 | NO_ASM | NO_ASM | NO_ASM |
| C335 | NO_ASM | NO_ASM | NO_ASM |
| R318 | NO_ASM | NO_ASM | NO_ASM |
| R678 | NO_ASM | NO_ASM | NO_ASM |
| R670 | NO_ASM | NO_ASM | NO_ASM |
| R683 | NO_ASM | ASM | NO_ASM |
| R669 | NO_ASM | ASM | NO_ASM |



<Variant Name>

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Title

EMC DECOUPLING

Size
A3

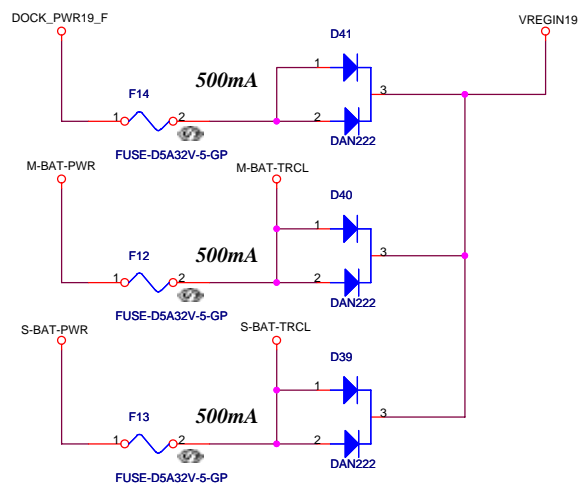
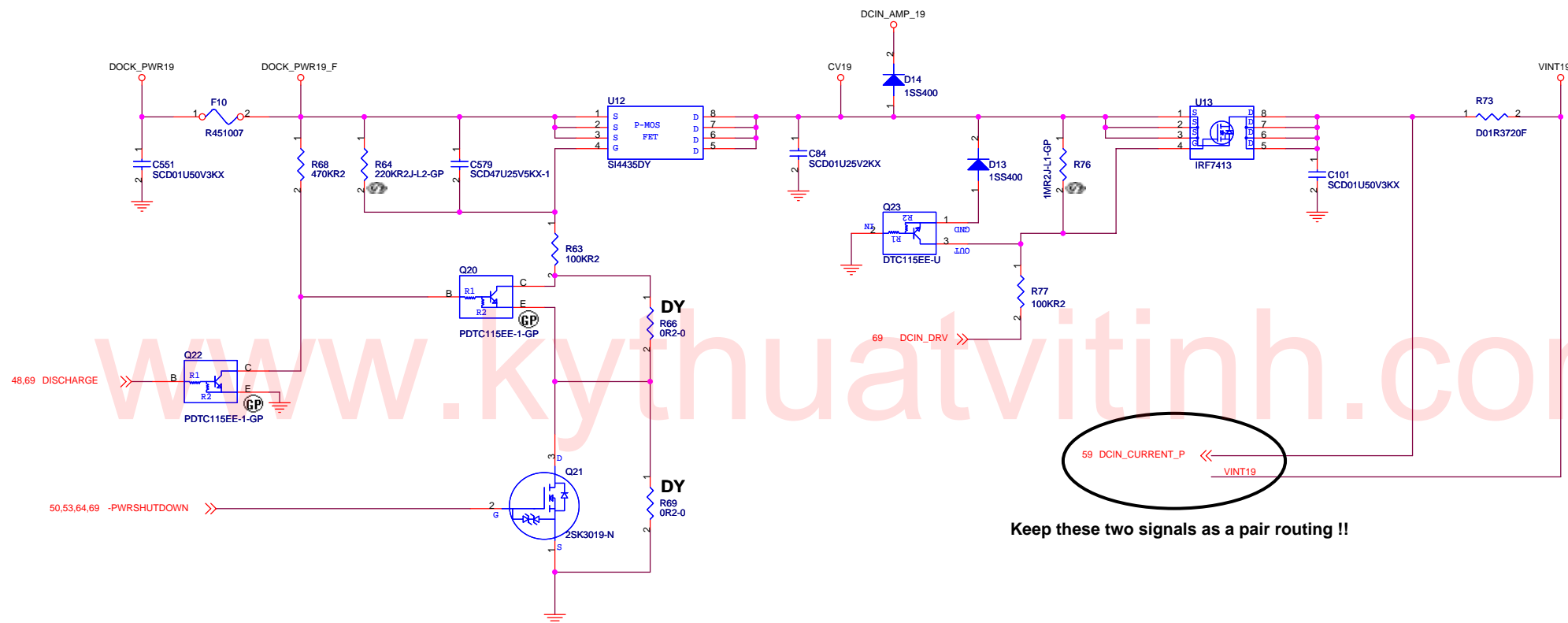
Document Number

KS Note

Rev
SB

Date: Friday, July 22, 2005

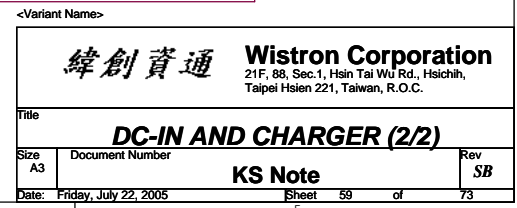
Sheet 57 of 73

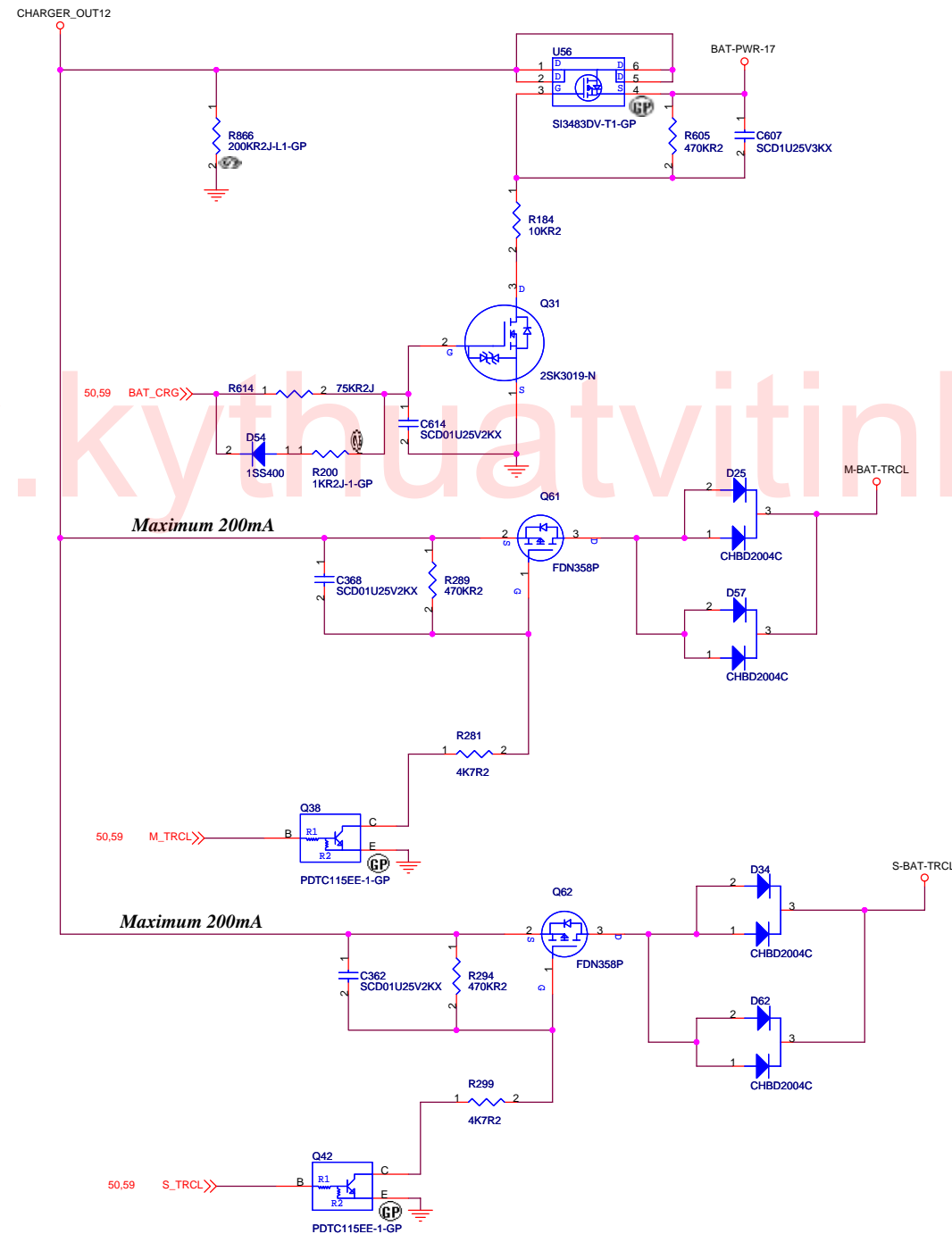


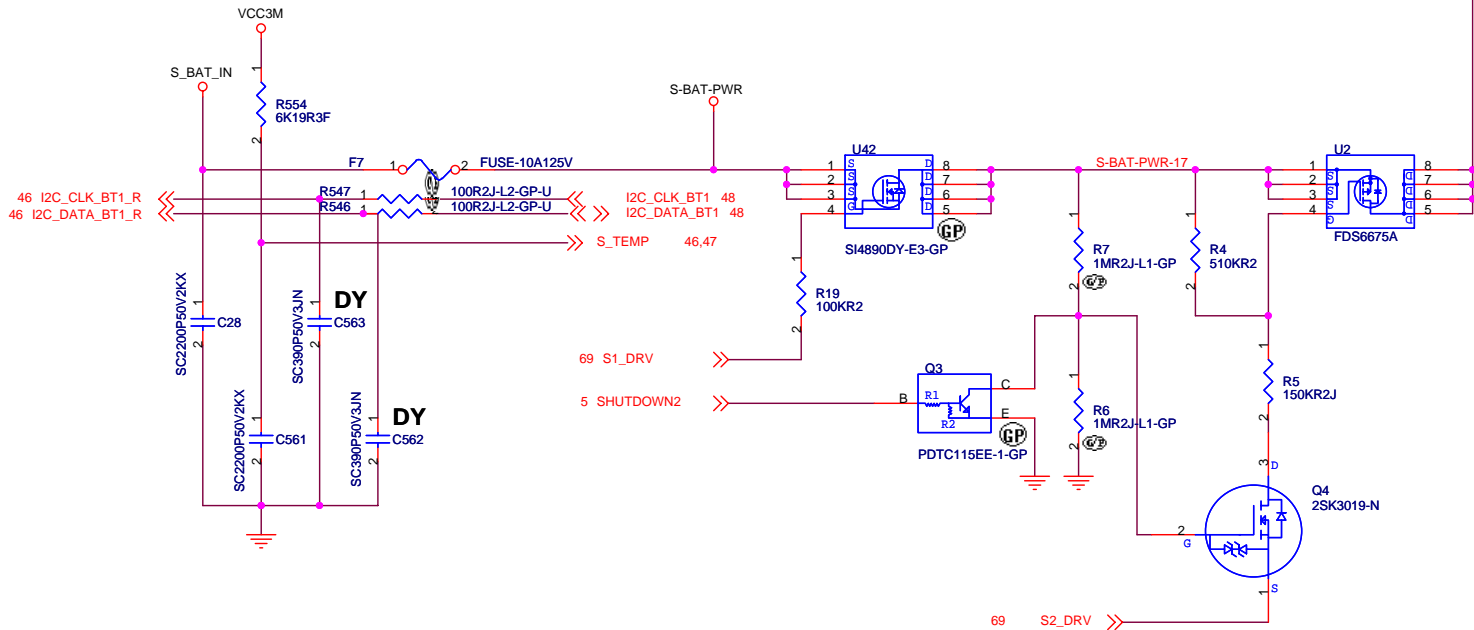
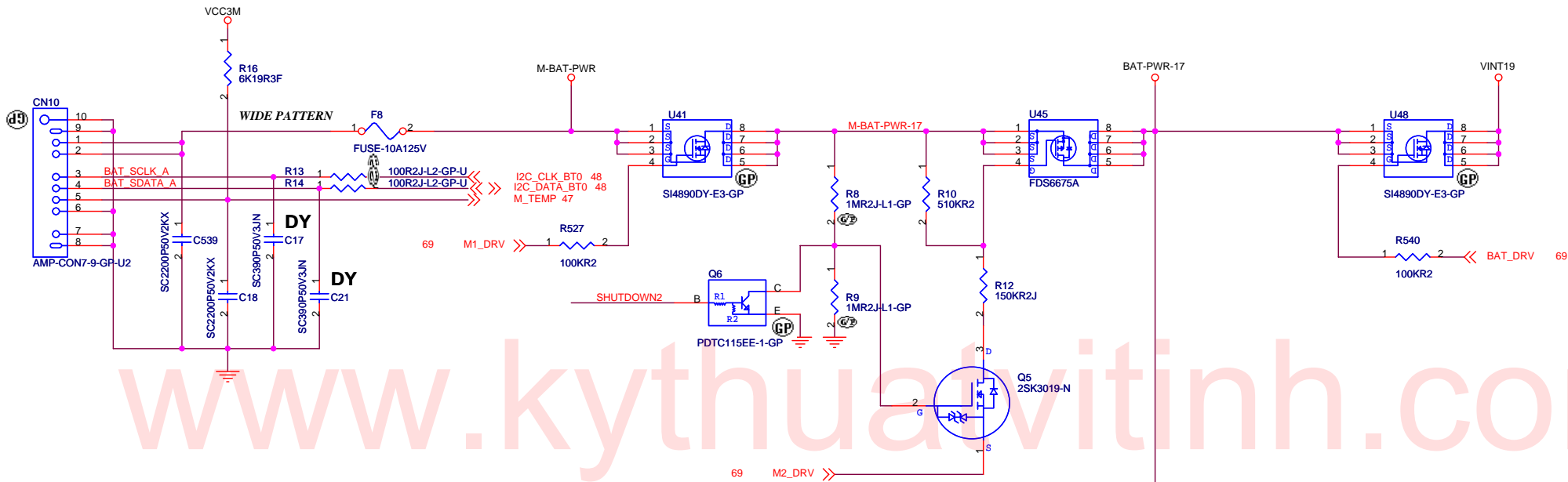
<Variant Name>

緯創資通 **Wistron Corporation**
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| Title | | | |
|--------------------------------|-----------------|---------|-------|
| DC-IN AND CHARGER (1/2) | | | |
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| | | Rev | SB |







<Variant Name>

緯創資通

Wistron Corporation
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Title

BATTERY INPUT

Size
B

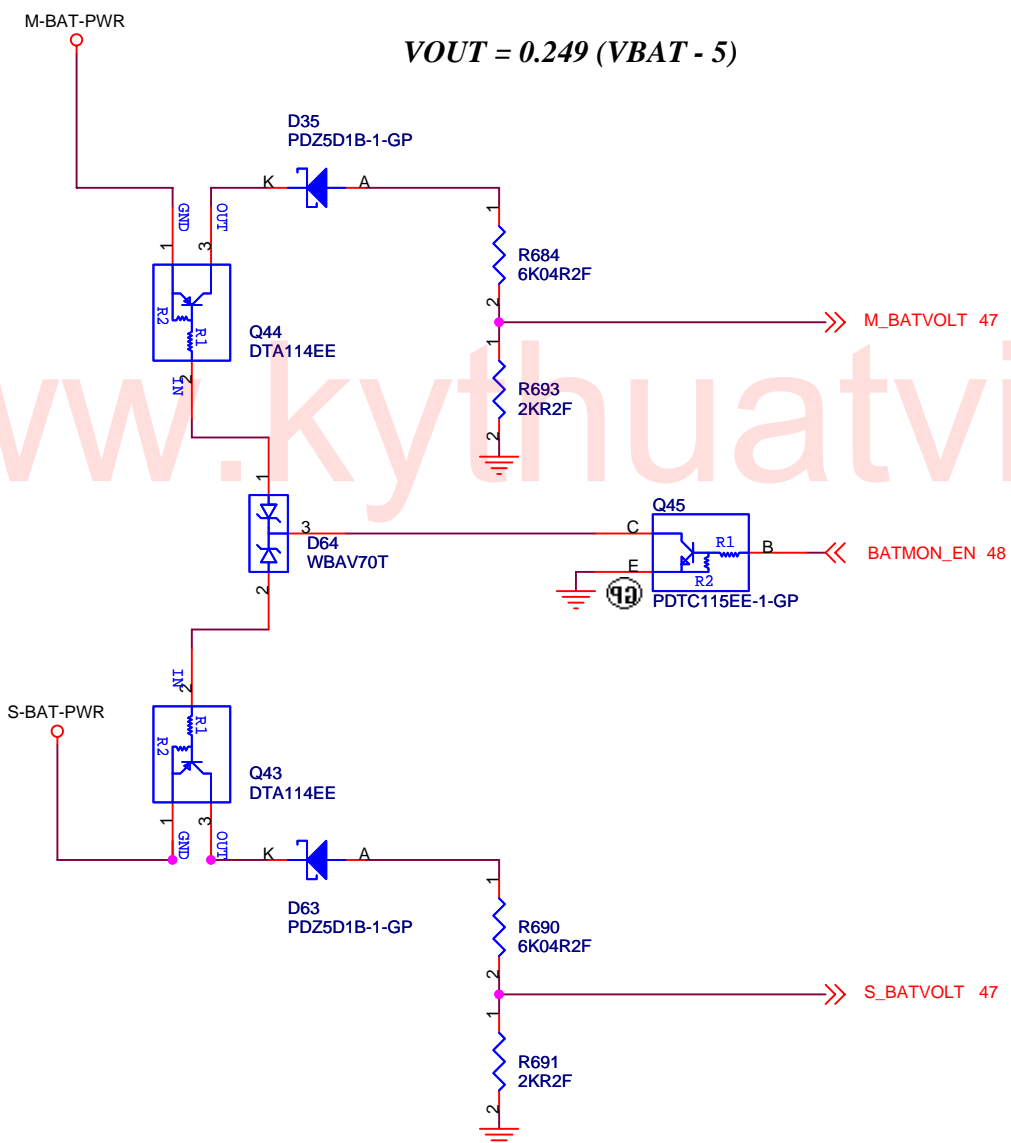
Document Number

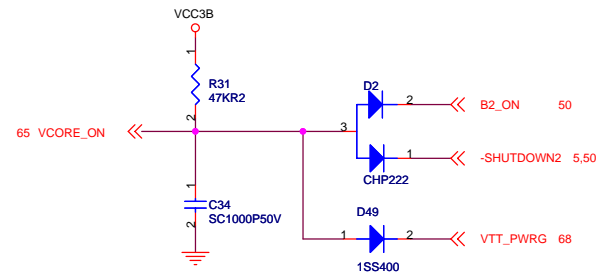
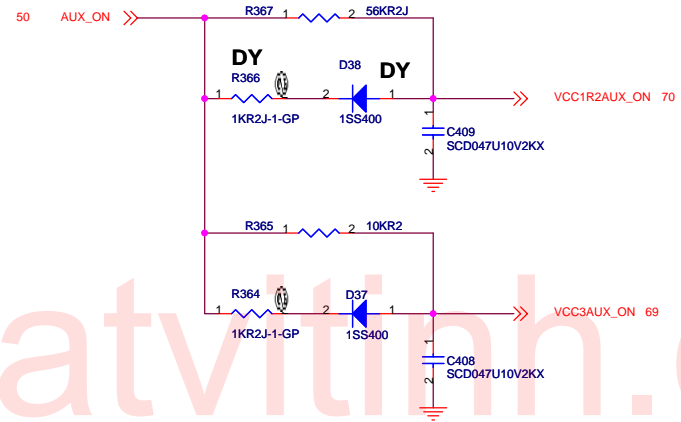
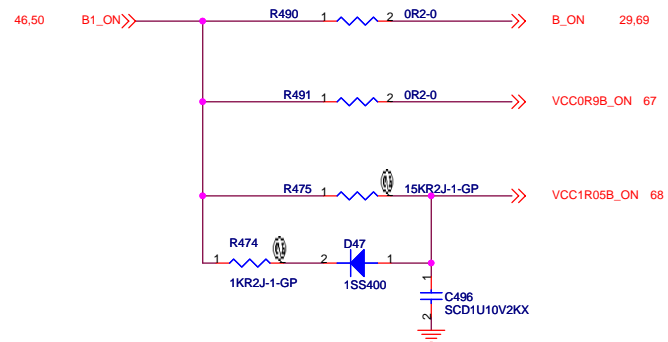
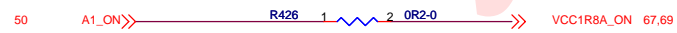
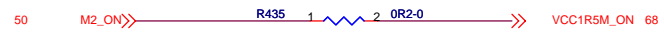
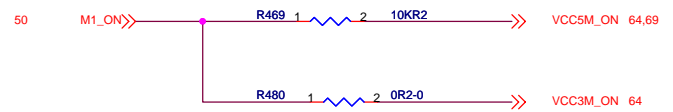
KS Note

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<Variant Name>

緯創資通 Wistron Corporation
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Title

POWER SEQUENCE

Size
A3

Document Number

KS Note

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**6.5A Peak
5.5A Average**

7A

These components should be located near by MAX1977

These components should be located near by MAX1977

$$\begin{aligned} \text{ILIM5: } 5 \times 36.5 / (36.5 + 110) &= 1.245\text{V} \\ 1.245 / 15 &= 8.3\text{A} \\ \text{ILIM3: } 5 \times 22.6 / (22.6 + 110) &= 0.8521\text{V} \\ 0.8521 / 15 &= 5.6\text{A} \end{aligned}$$

100mA MAX. each

Change to 34KR2, 1%

SKIP# = GND : SKIP MODE
SKIP# = REF/FloatING : Ultrasonic MODE
(25KHz min)

<Variant Name>

緯創資通 Wistron Corporation
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VCC3M / VCC5M

| | | | |
|-----------------------------|-----------------|---------|--------|
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<Variant Name>

緯創資通 **Wistron Corporation**
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Title

DC-DC VCCCPUCORE

| | |
|------|-----------------|
| Size | Document Number |
|------|-----------------|

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Date: Friday, July 22, 2005

Sheet 6

| | |
|-----|----|
| Rev | SB |
|-----|----|

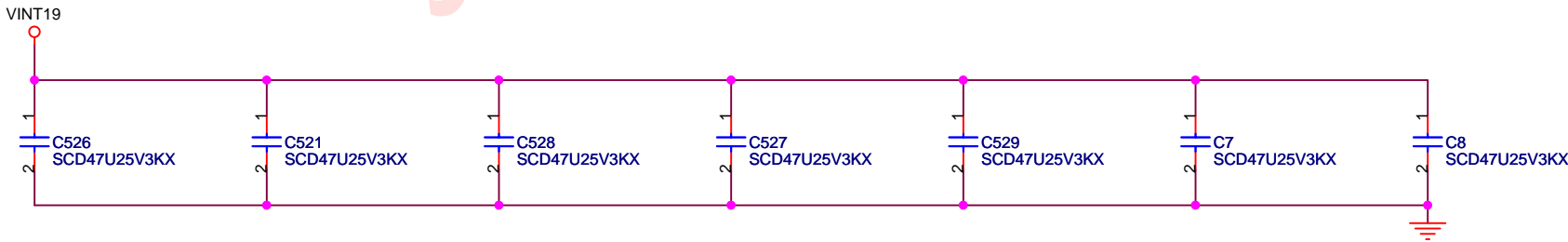
Date: Friday, July 22, 2005

Sheet 6

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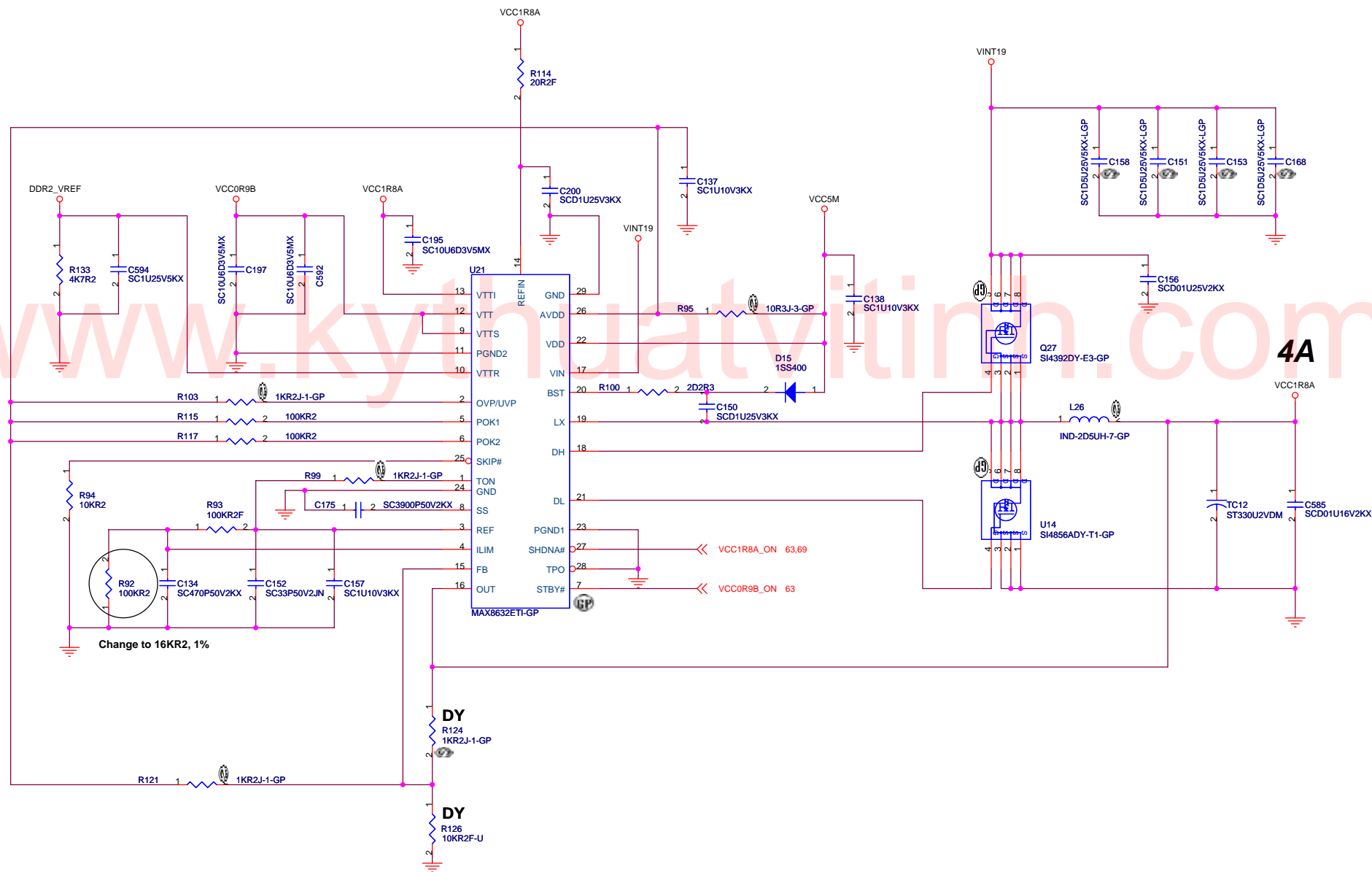
FOLLOWING 7 PCS OF 0.47UF AREA ASM FOR VCCCPUCORE TO REDUCE EMI

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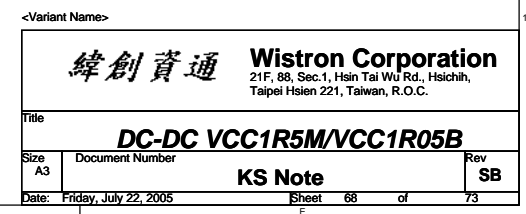
| | |
|--|-------------------|
| <div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div> | |
| Title <div>DC-DC VCORE INPUT CAP</div> | |
| Size <div>A4</div> | Rev <div>SB</div> |
| Date <div>Friday, July 22, 2005</div> | |
| Document Number <div>KS Note</div> | |
| Sheet <div>66</div> of <div>73</div> | |



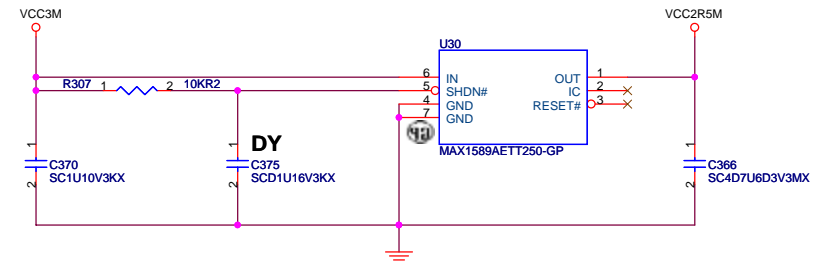
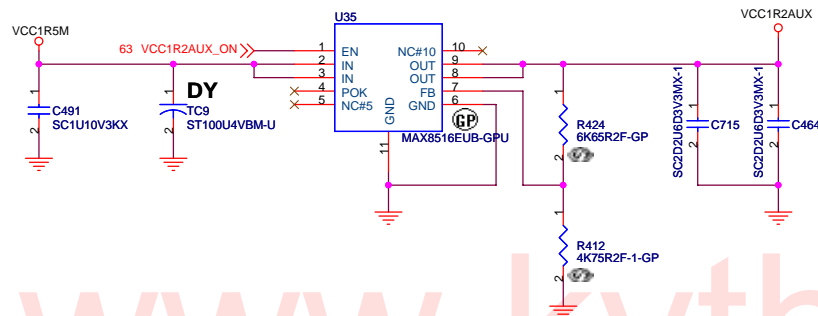
<Variant Name>

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| | | | |
|-------|-----------------------|---------------|----------|
| Title | | DC-DC VCC1R8A | |
| Size | Document Number | KS Note | |
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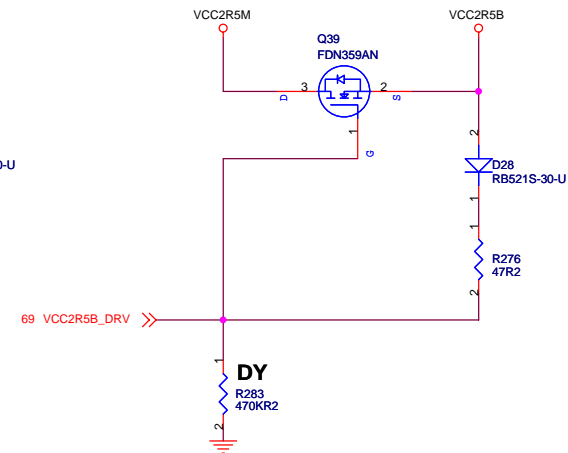
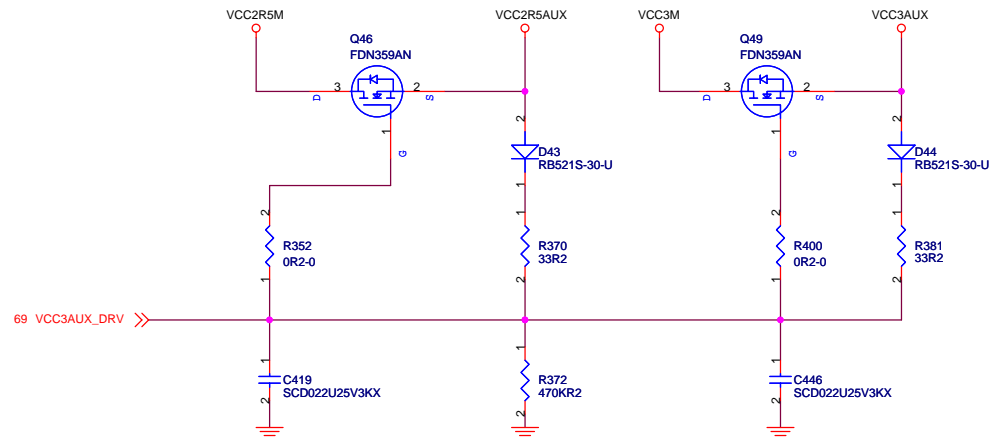
VCC1R2AUX



VCC2R5AUX

VCC3AUX

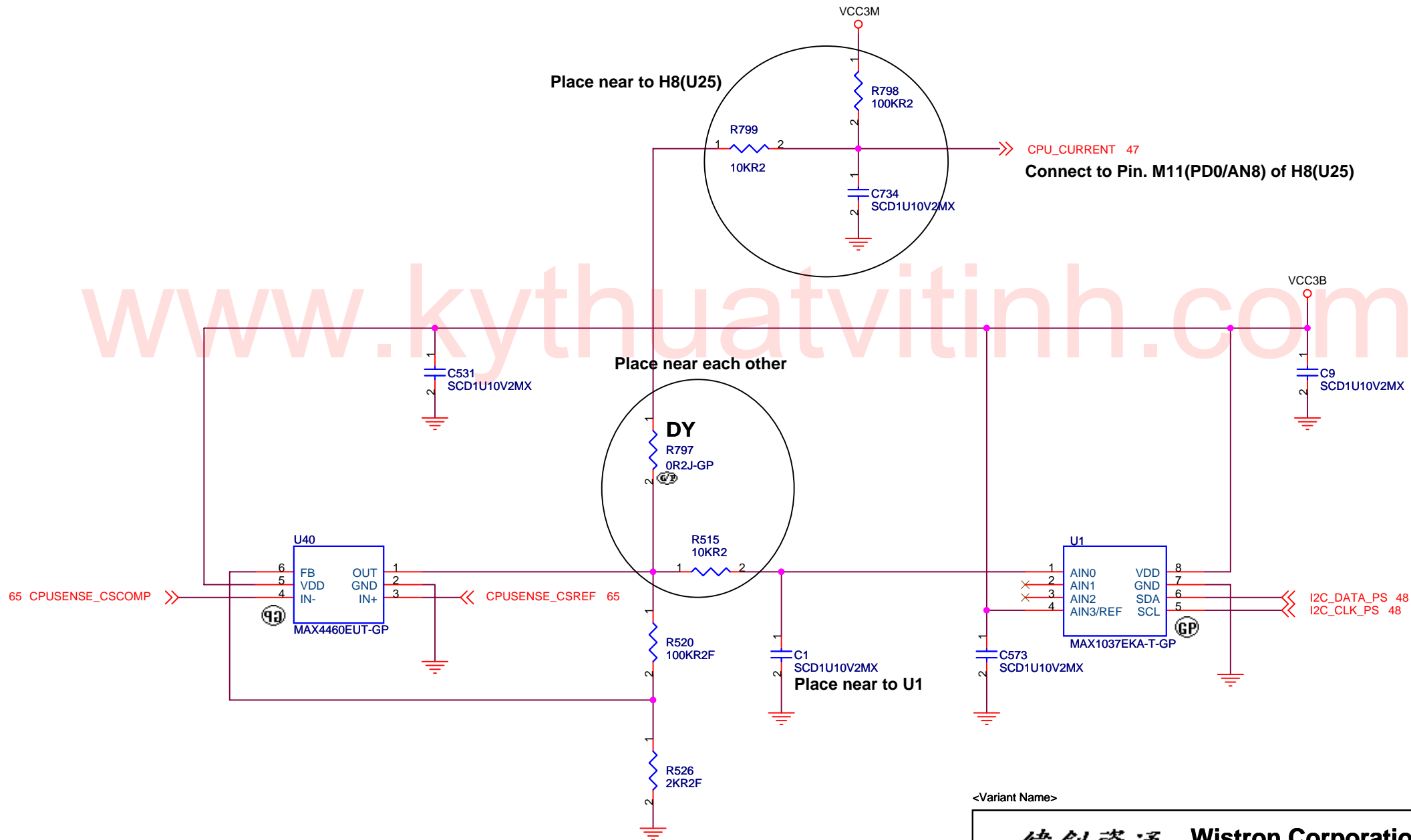
VCC2R5B



<Variant Name>

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|-----------------------------|-----------------|----------|-----|
| Title | | | |
| LOAD SW | | | |
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| KS Note | | SB | |
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<Variant Name>

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Title

CURRENT SENSE

Size
A4

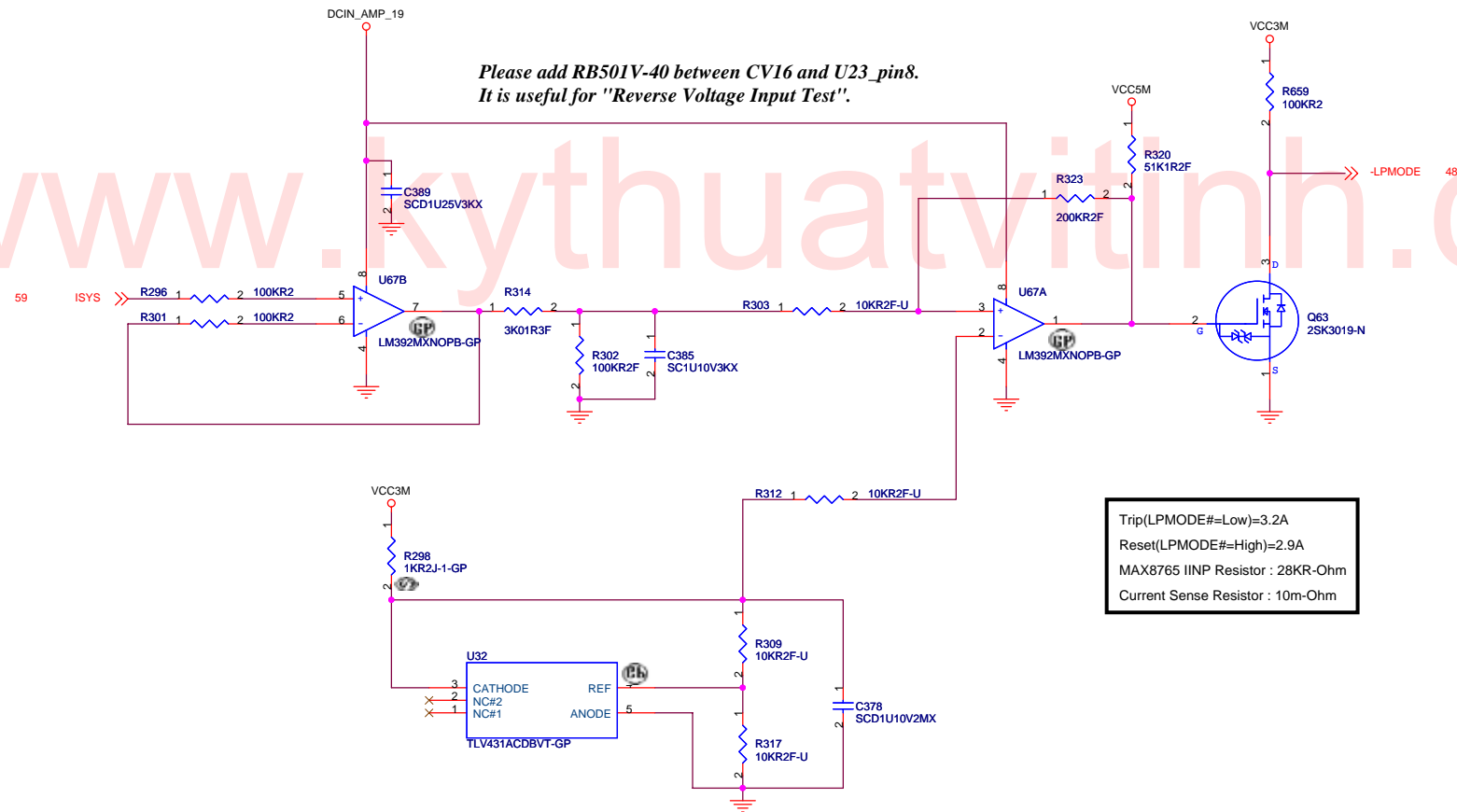
Document Number

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Title

MAX POWER CONTROL

Size
A3

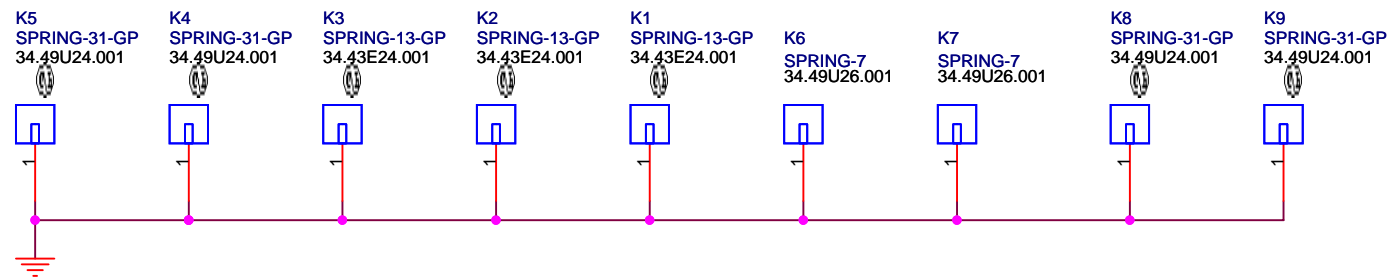
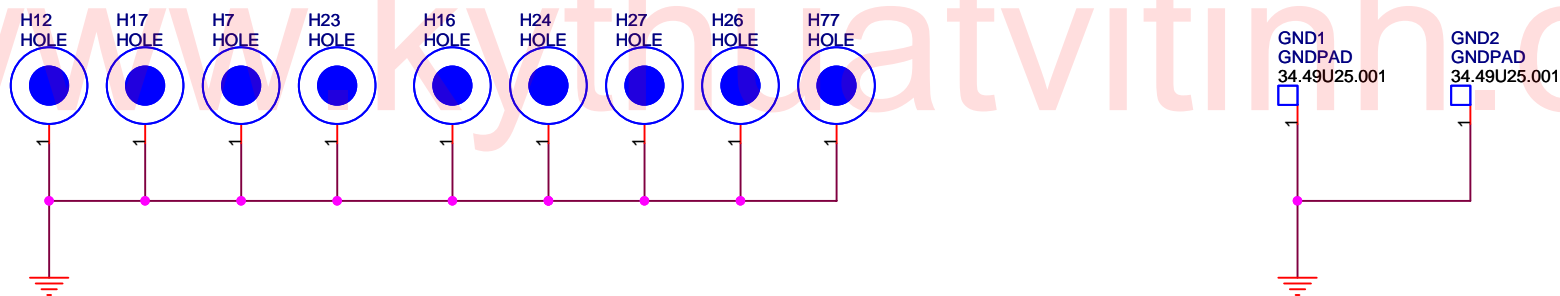
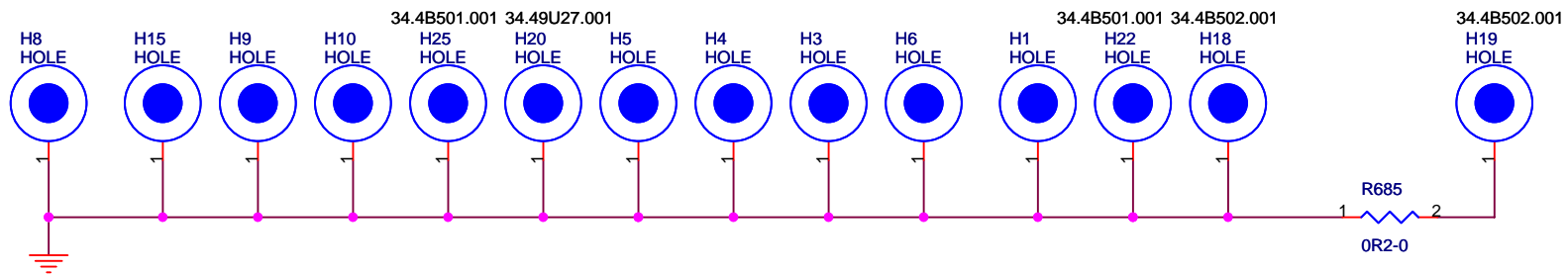
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<Variant Name>

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Title

HOLES/GND/PADS

Size
A4

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