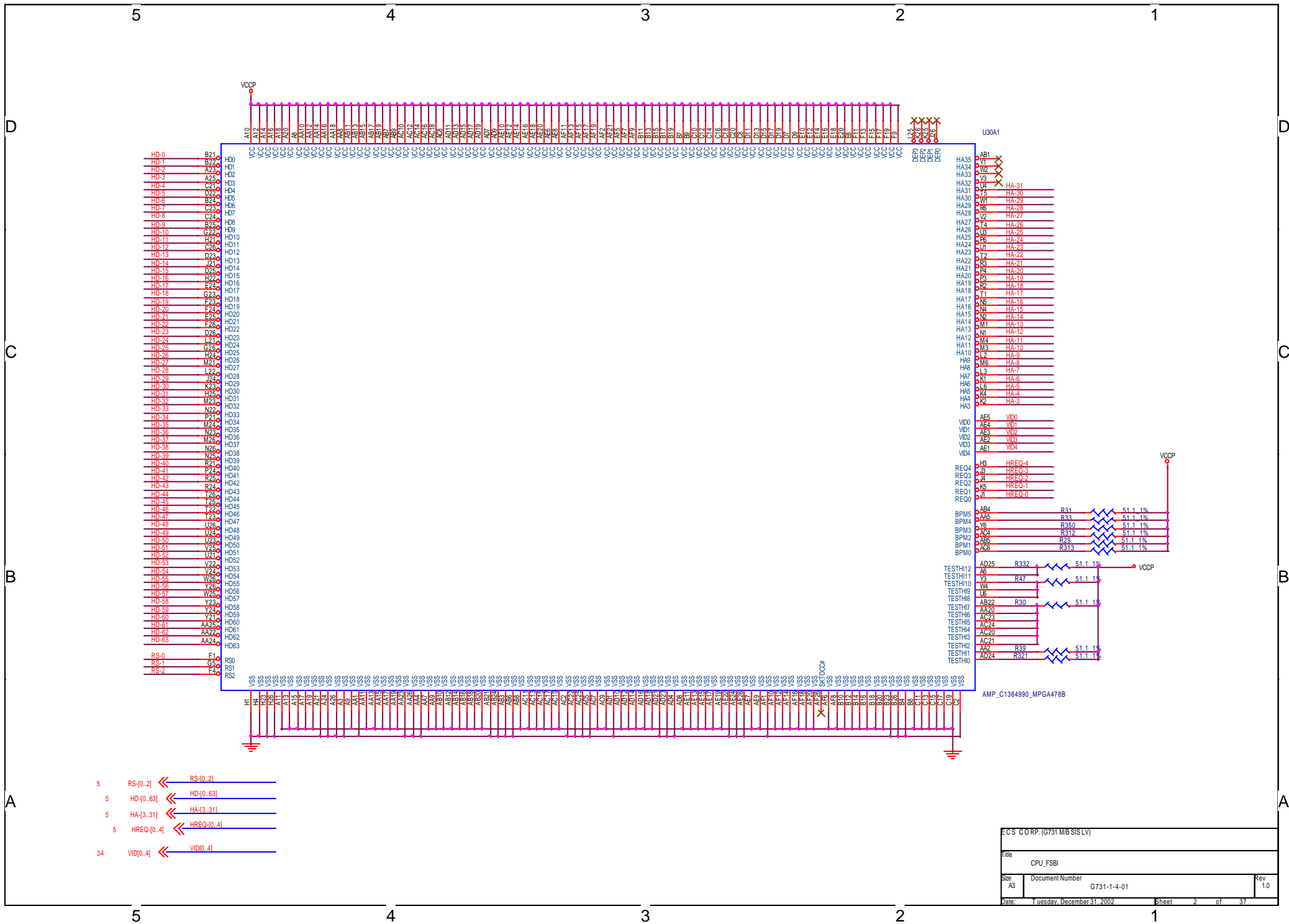


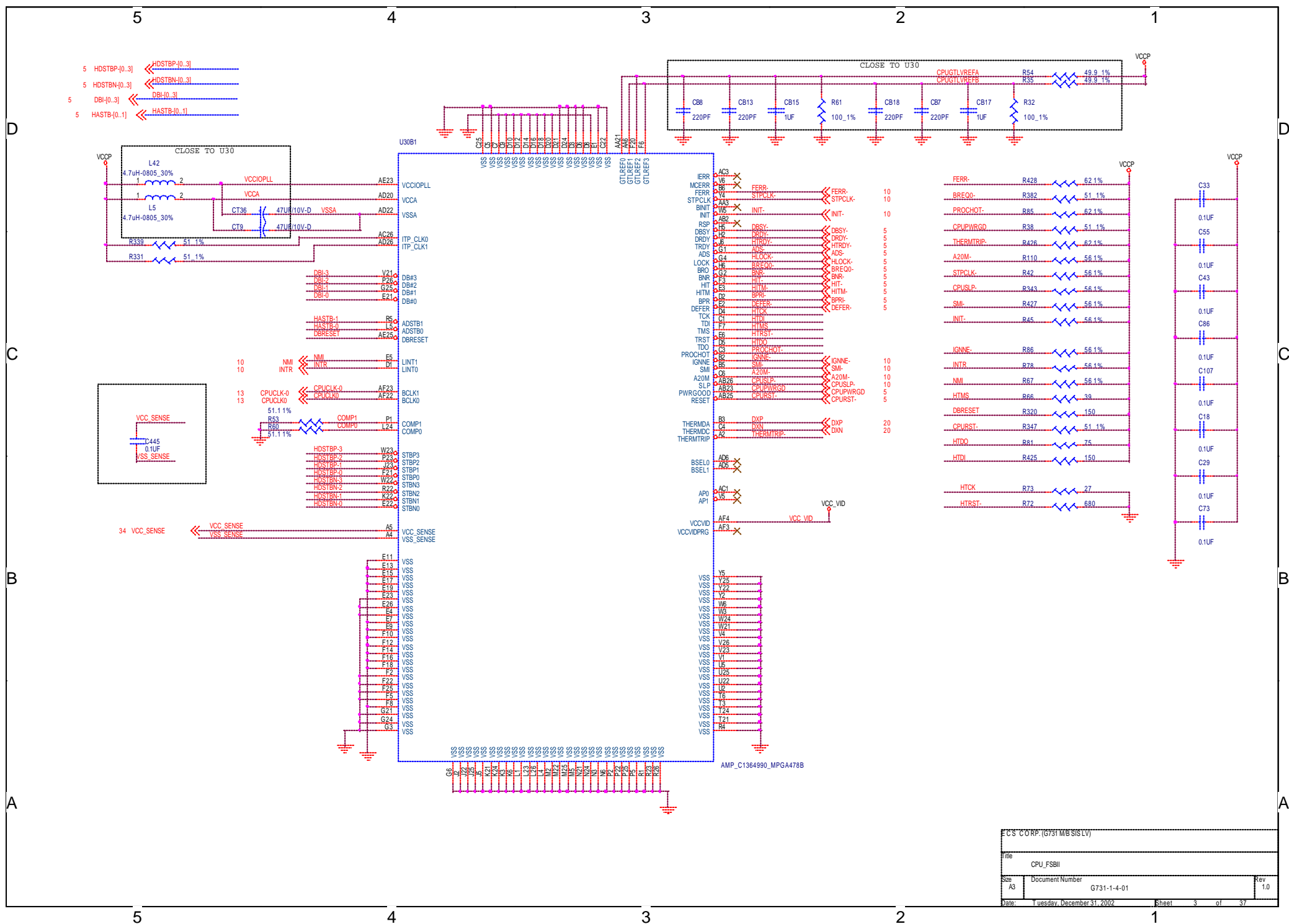
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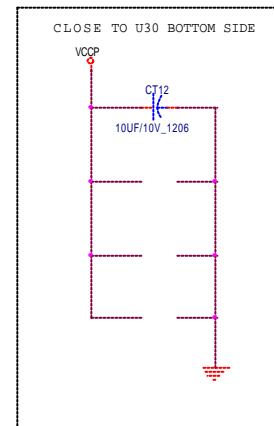
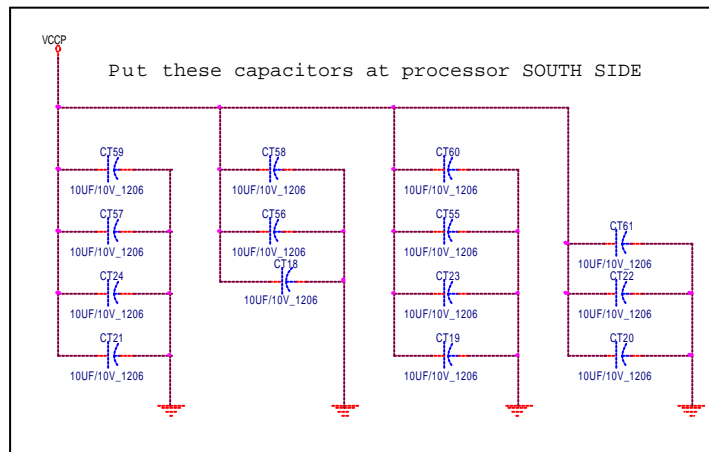
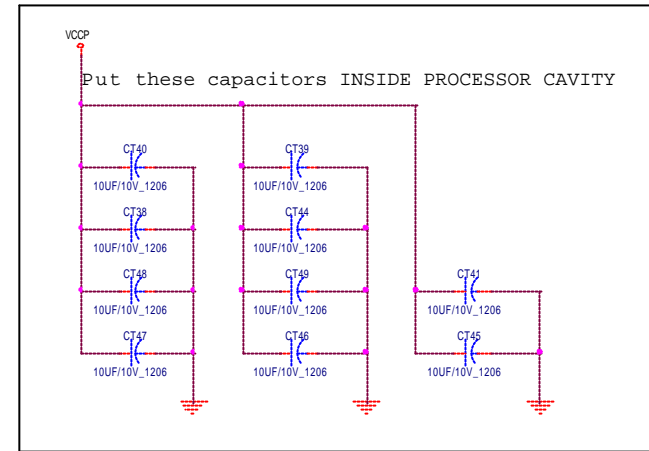
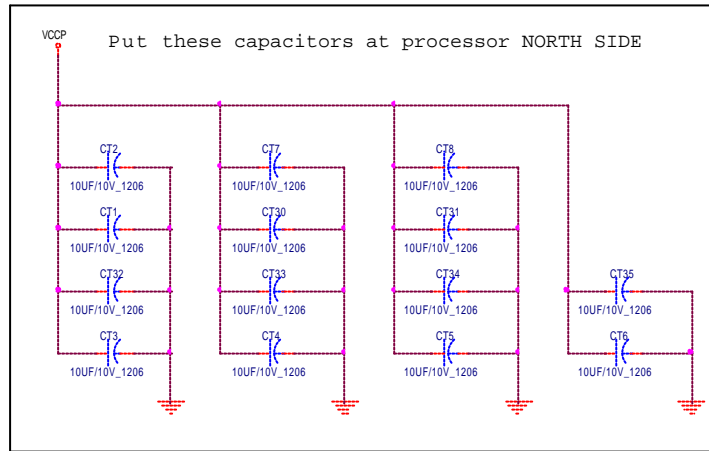


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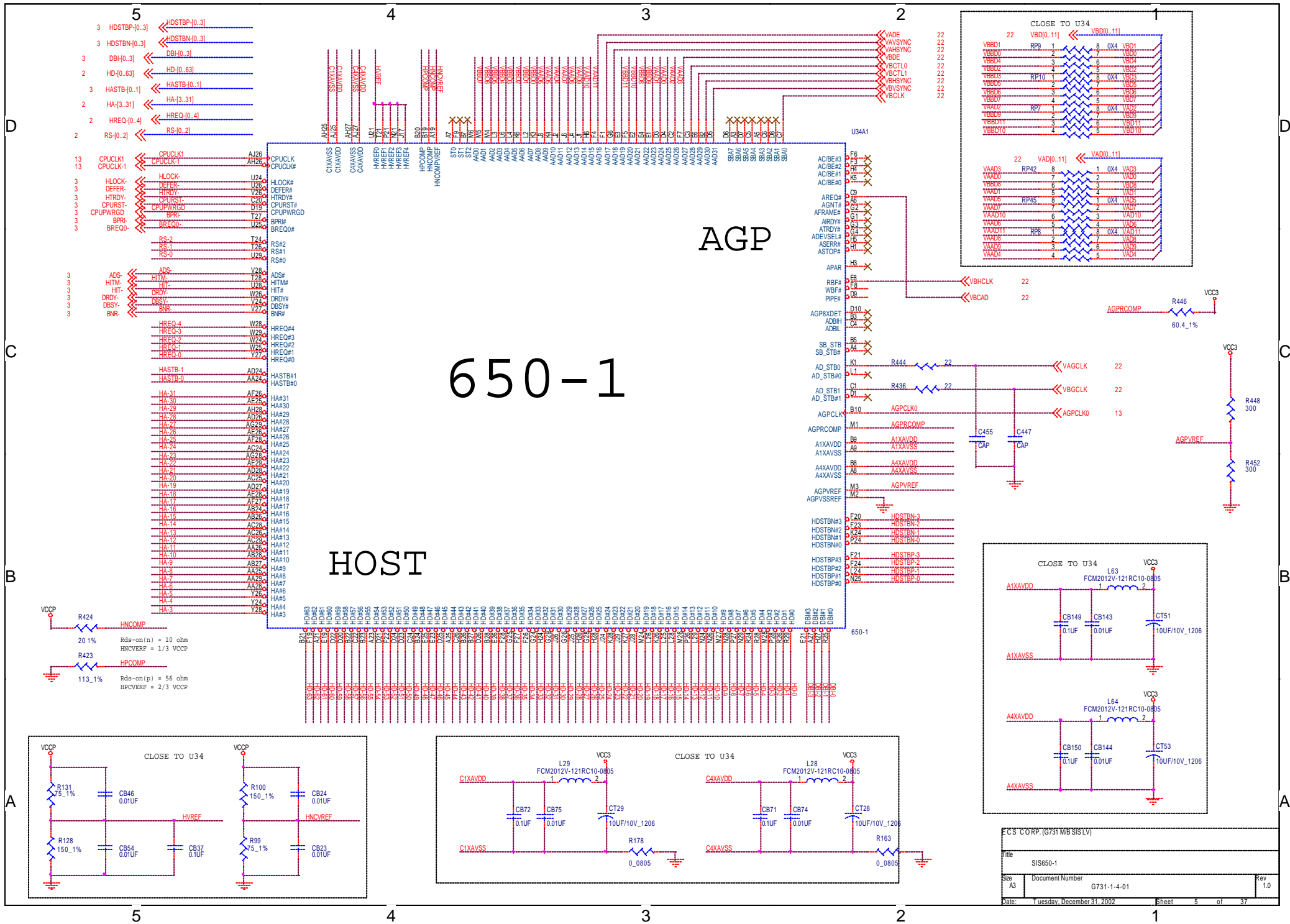
	Approved	by	Checked	by	Designed	by
	ECS CORP. (G731 MB SIS LV)					
P. Leader	Title G 7 3 1 M /B DESCRIPTION PAGE					
	Size A3	Document Number G731-1-4-01				Rev 1.E
	Date	Tuesday, December 31, 2002		Sheet	1	of 37



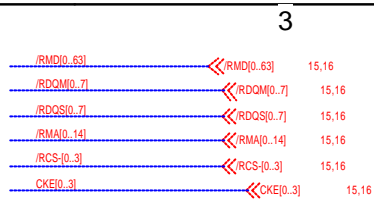




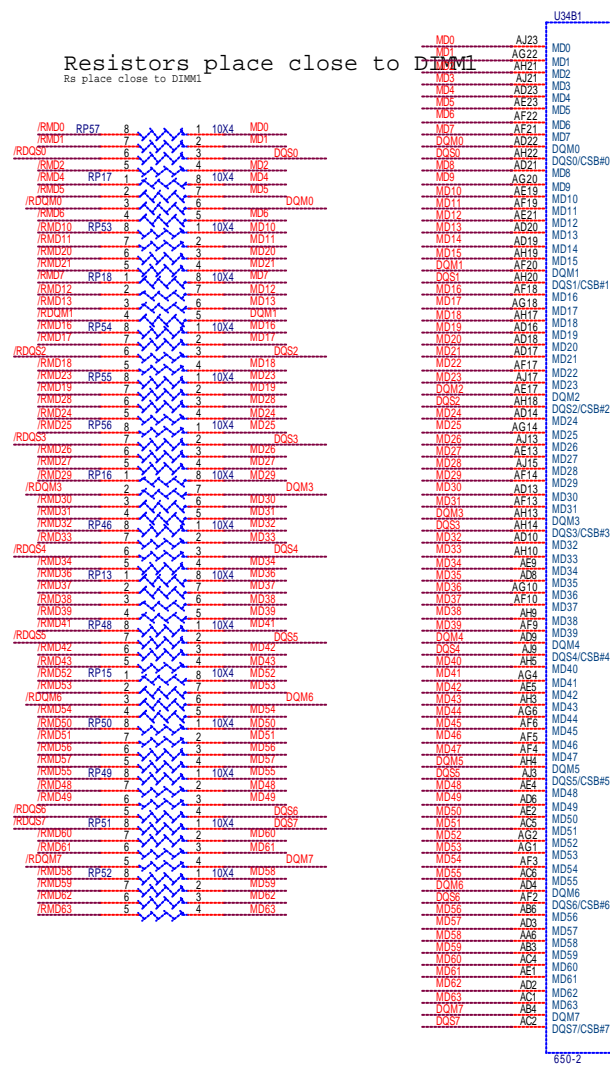
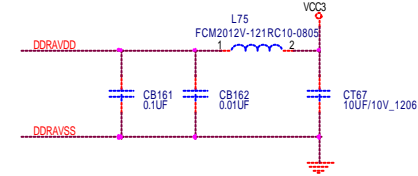
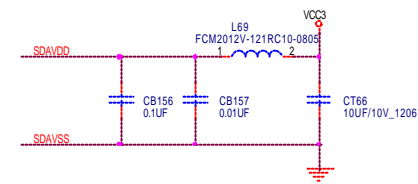
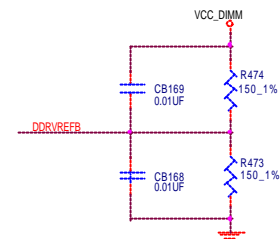
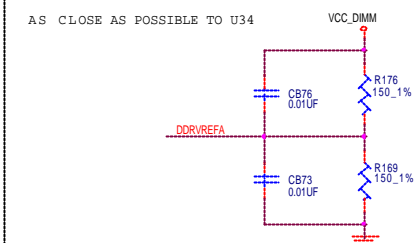
ECS CORP. (G731MB/SISLV)			
Title CPU_POWER			
Size A3	Document Number G731-1-4-01		Rev 1.0
Date: Tuesday, December 31, 2002	Sheet 4 of 37		



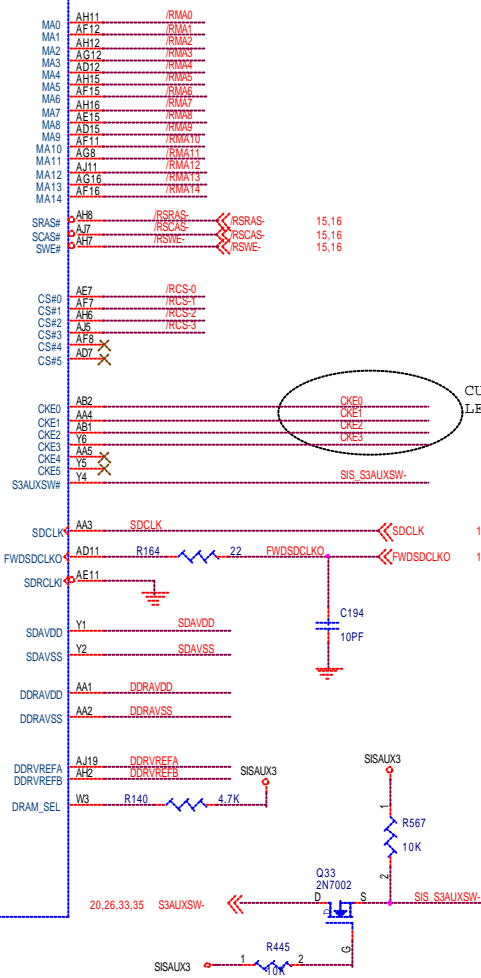
FCS CORP. (G731MBSISLV)			
SIS650-1			
Size	Document Number	Rev	
A3	G731-1-4-01	1.0	
Date:	Tuesday, December 31, 2002	Sheet	5 of 37



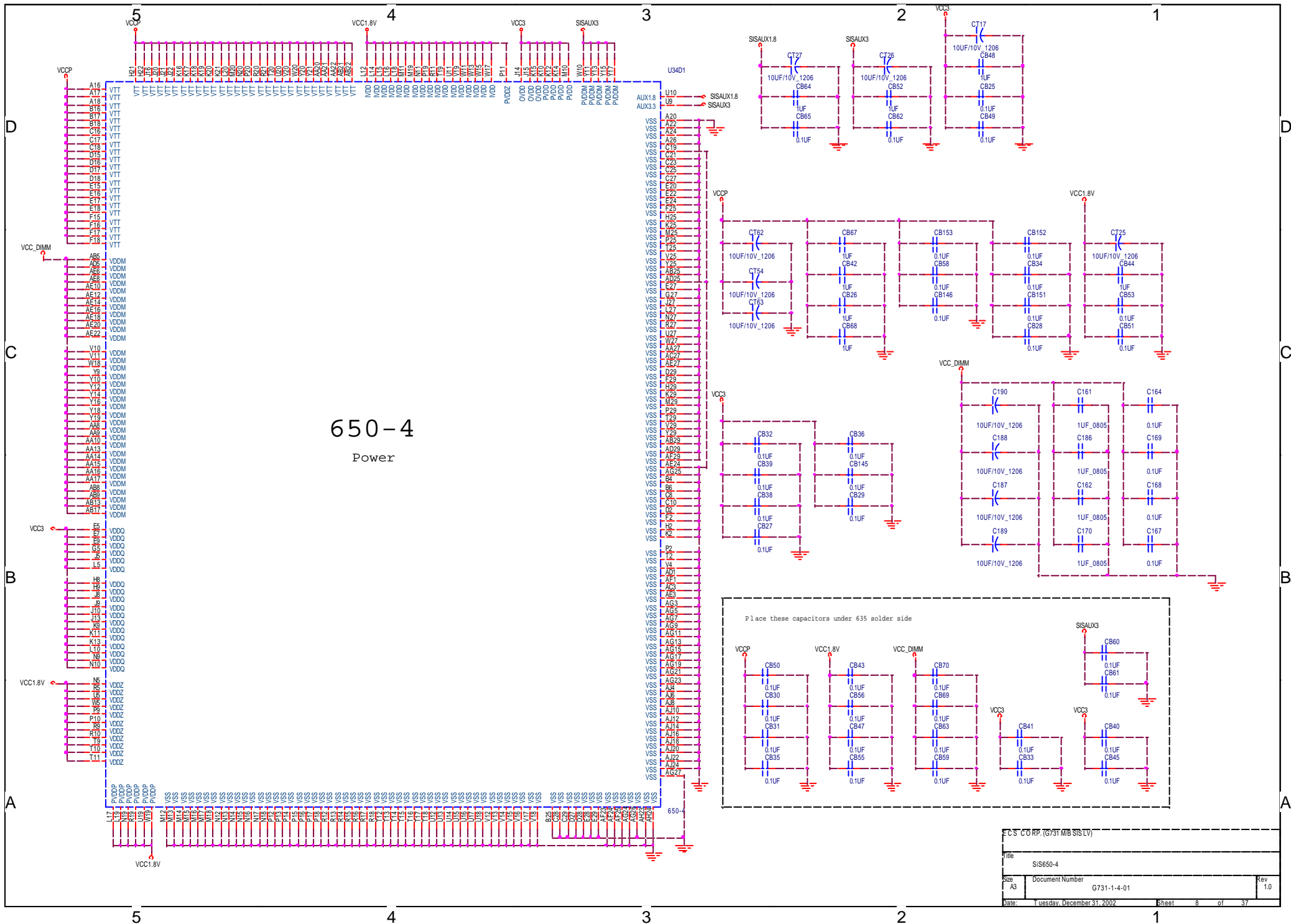
Rs place close to DIMM1

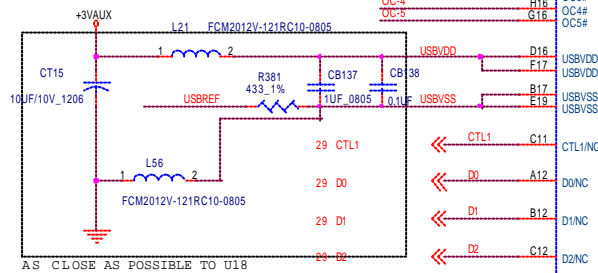
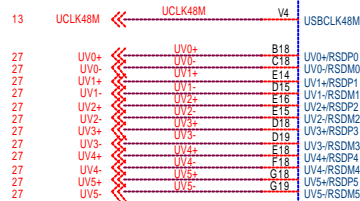


650-2



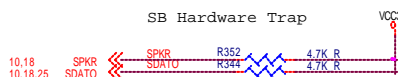
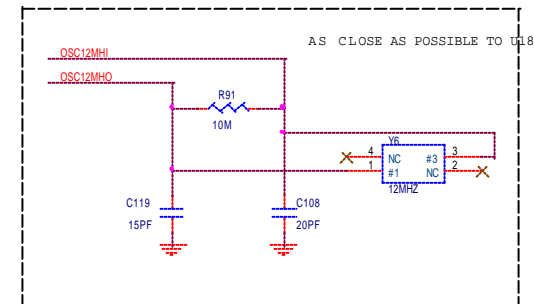
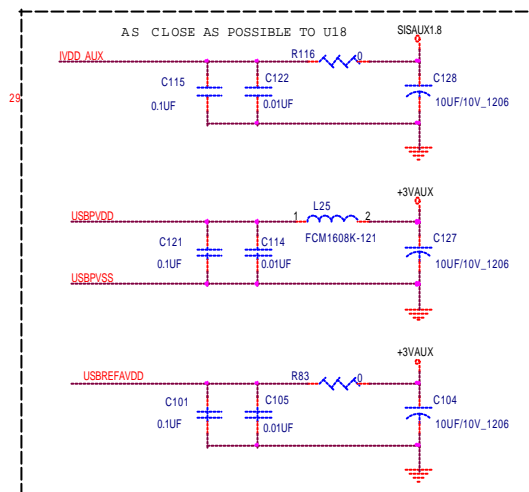
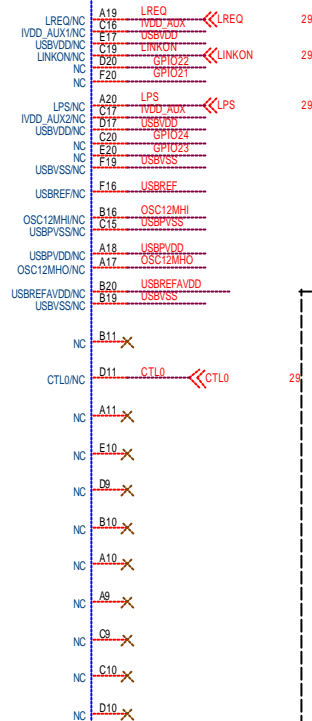
CURRENT
LEAKAGE!!!



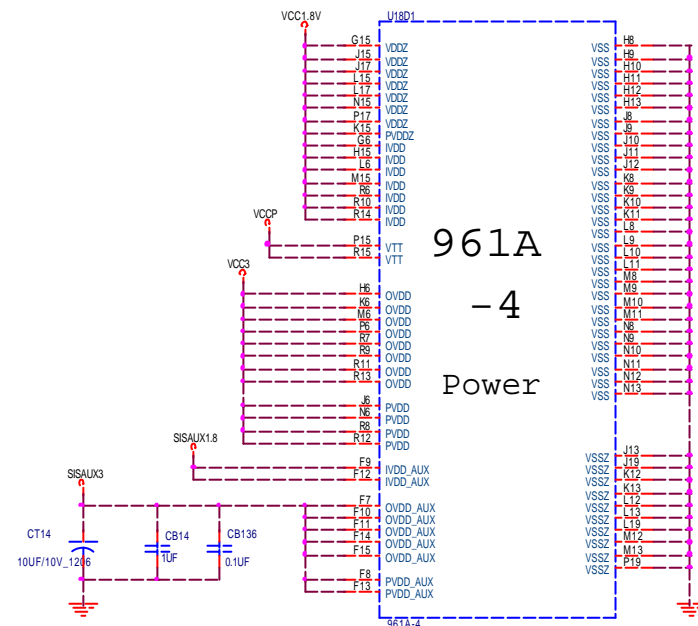
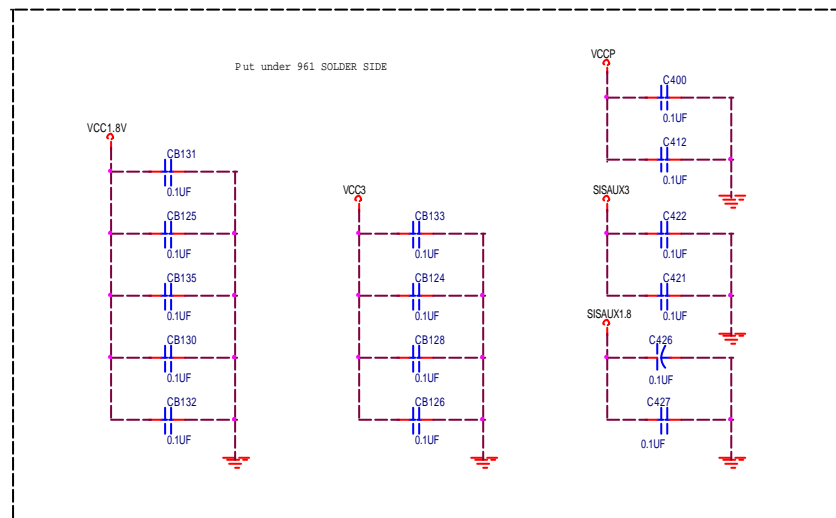
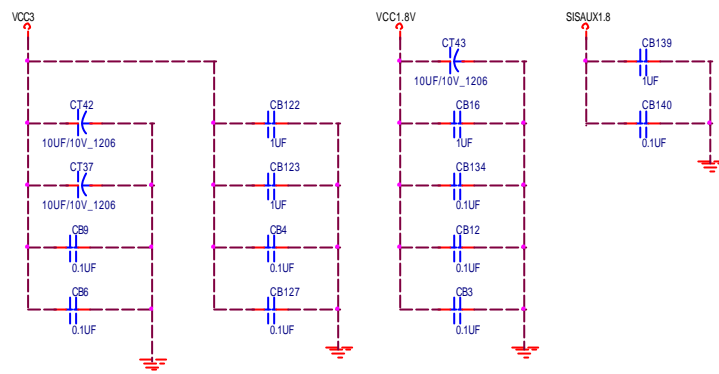


AS CLOSE AS POSSIBLE TO U18

USB
961A-3



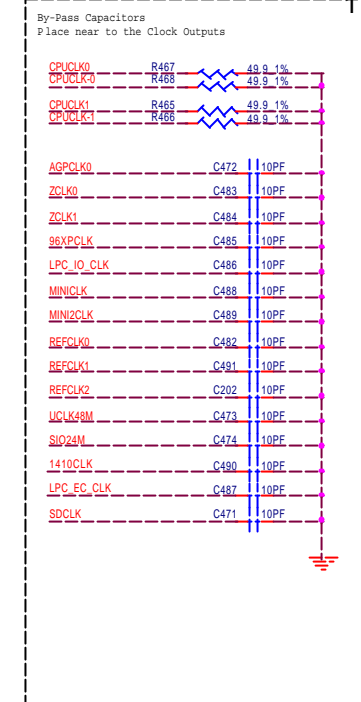
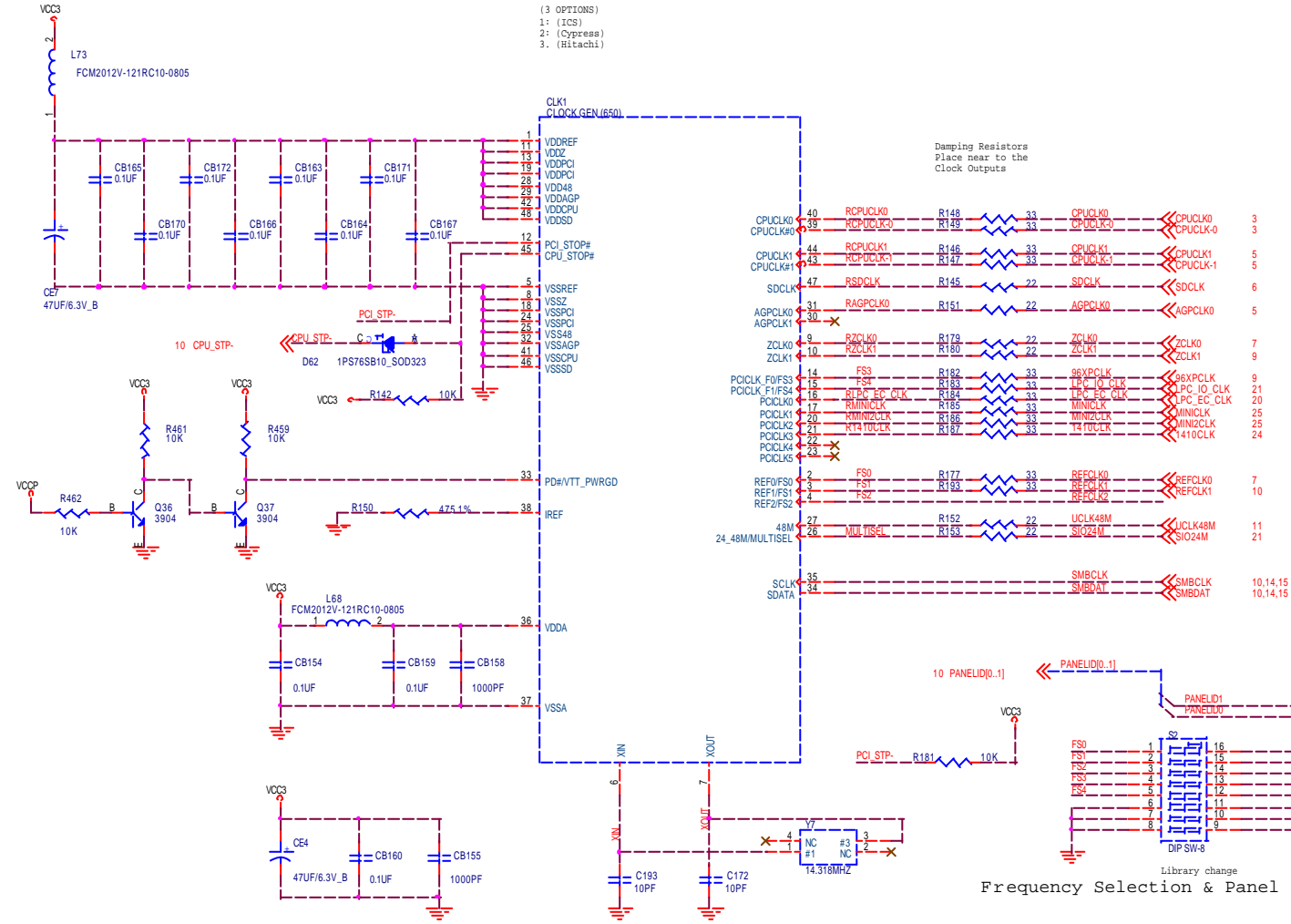
	0	1	Default
SPKR (LPC addr mapping)	disable	enable	0
SDA70 (PCTICK PLL)	enable	disable	0
OC2 (SB debur mode)	enable	disable	1
OC5 (Trap mode)	PCI AD	ROM	1



FCS CORP. (G731 MIB SIS LV)			
Title			
SIS962-4			
Size	Document Number		Rev
A3	G731-1-4-01		1.0
Date:	Tuesday, December 31, 2002	Sheet	12 of 37

Main Clock Generator

(3 OPTIONS)
1: (ICS)
2: (Cypress)
3: (Hitachi)



Frequency Selection & Panel Selection

SIS650 CLOCK							
FS4	FS3	FS2	FS1	FS0	CPU (MHz)	SDRAM (MHz)	ZCLK (MHz)
0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0
0	0	0	1	0	0	0	0
0	0	0	1	1	0	0	0
0	0	1	0	0	0	0	0
0	0	1	0	1	0	0	0
0	0	1	1	0	0	0	0
0	0	1	1	1	0	0	0
0	1	0	0	0	0	0	0
0	1	0	0	1	0	0	0
0	1	0	1	0	0	0	0
0	1	0	1	1	0	0	0
0	1	1	0	0	0	0	0
0	1	1	0	1	0	0	0
0	1	1	1	0	0	0	0
0	1	1	1	1	0	0	0

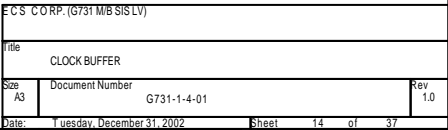
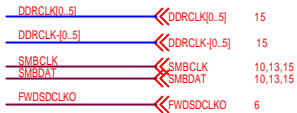
SIS650 CLOCK							
FS4	FS3	FS2	FS1	FS0	CPU (MHz)	SDRAM (MHz)	ZCLK (MHz)
1	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0
1	0	0	1	0	0	0	0
1	0	0	1	1	0	0	0
1	0	1	0	0	0	0	0
1	0	1	0	1	0	0	0
1	0	1	1	0	0	0	0
1	0	1	1	1	0	0	0
1	1	0	0	0	0	0	0
1	1	0	0	1	0	0	0
1	1	0	1	0	0	0	0
1	1	0	1	1	0	0	0
1	1	1	0	0	0	0	0
1	1	1	0	1	0	0	0
1	1	1	1	0	0	0	0
1	1	1	1	1	0	0	0

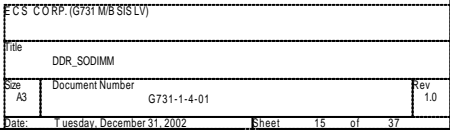
EC'S CORP. (G731 MIBSIS LV)

Title: CLOCK GENERATOR

Size: A3 Document Number: G731-1-4-01 Rev: 1.0

Date: Tuesday, December 31, 2002 Sheet: 13 of 37





5

4

3

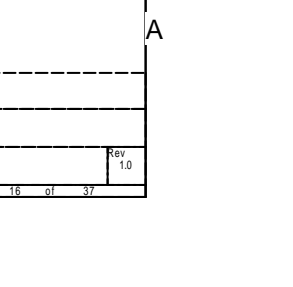
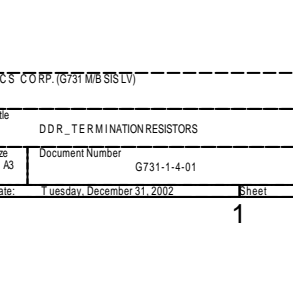
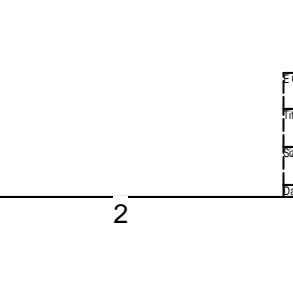
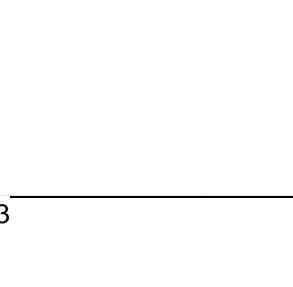
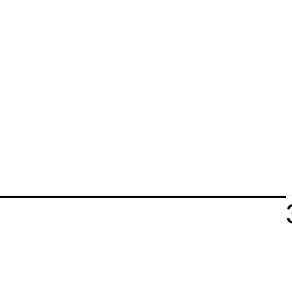
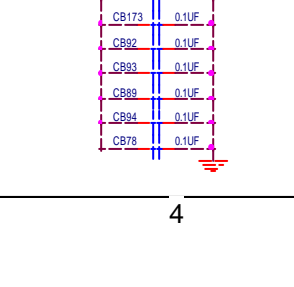
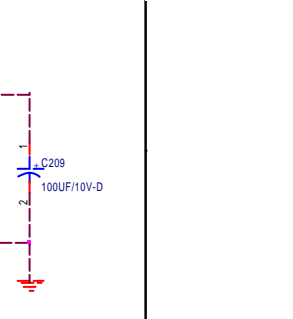
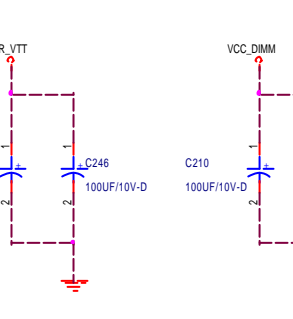
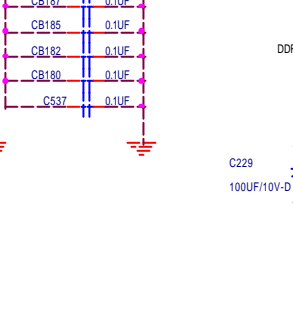
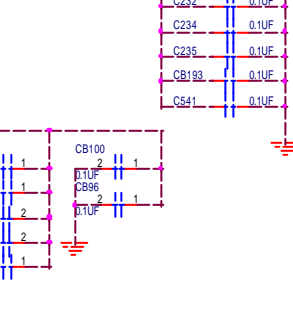
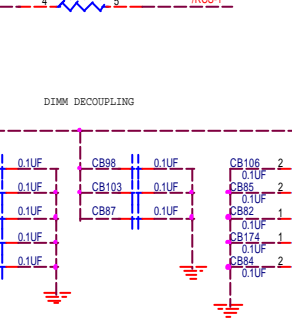
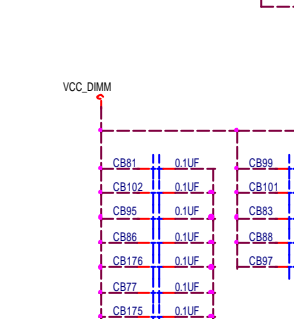
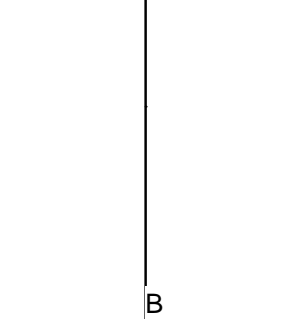
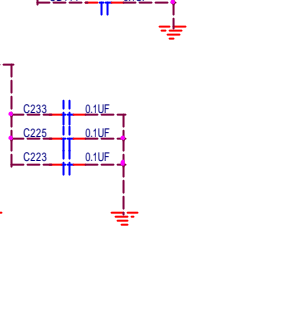
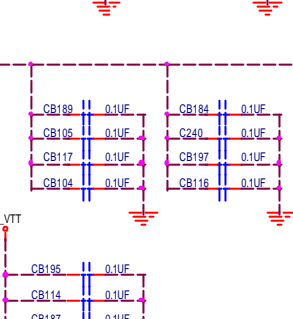
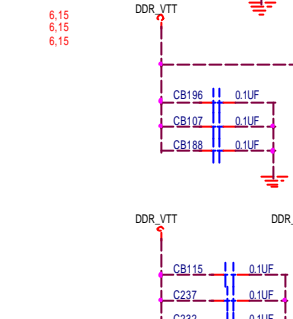
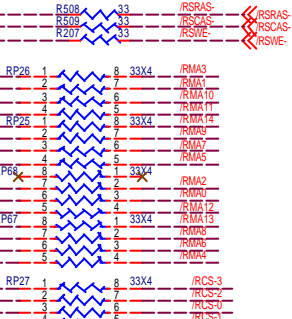
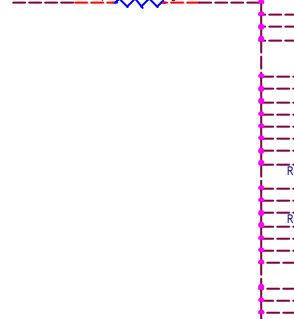
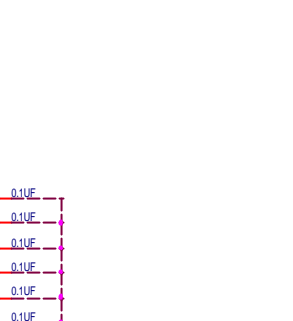
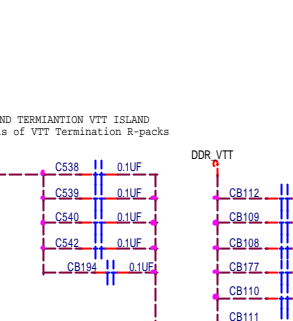
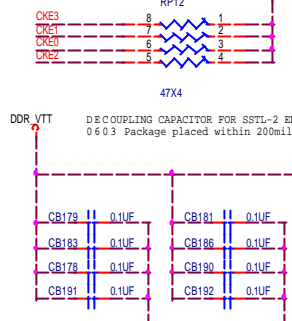
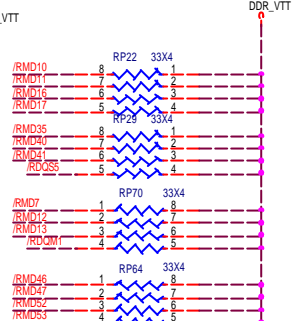
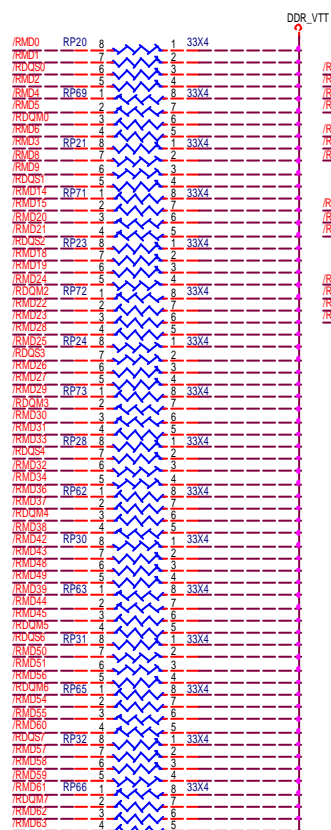
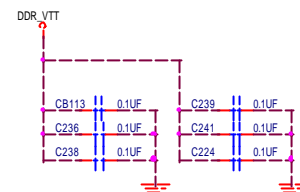
2

1

SSTL-2 Termination Resistors

/RMD[0..63] <<RMD[0..63] 6,15
 /RDOM[0..7] <<RDOM[0..7] 6,15
 /RDQS[0..7] <<RDQS[0..7] 6,15
 /RMA[0..14] <<RMA[0..14] 6,15
 /RCS[0..3] <<RCS[0..3] 6,15
 /CKE[0..3] <<CKE[0..3] 6,15

IOB	IOB	IOB	IOB	IOB	IOB
IOB/DQM/(DQS)	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS
IOB/Control	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS
IOB/CS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS
IOB/CKE	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS	IOB-DQMOS



FCS CORP. (G731 MIBSIS LV)		
Title: DDR_TERMINATIONRESISTORS		
Size: A3	Document Number: G731-1-4-01	Rev: 1.0
Date: Tuesday, December 31, 2002	Sheet: 16	of: 37

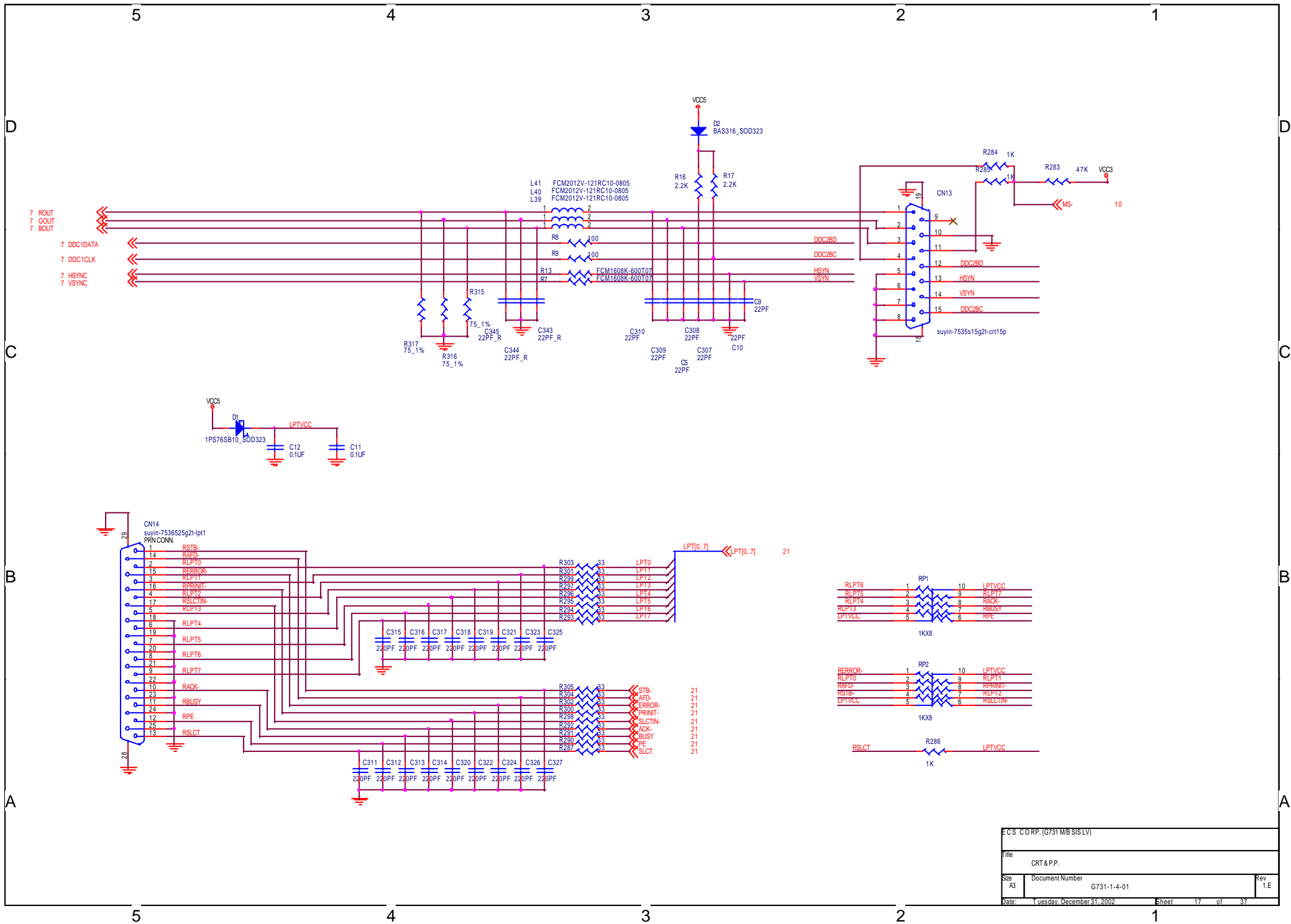
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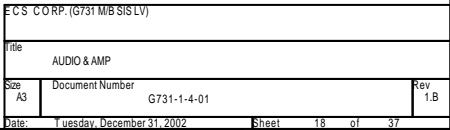
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3

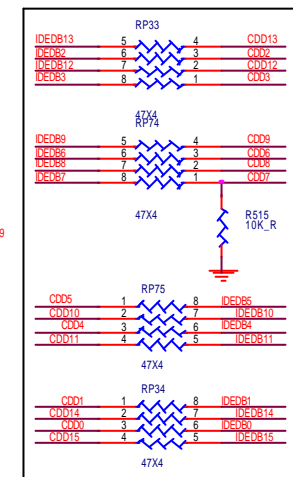
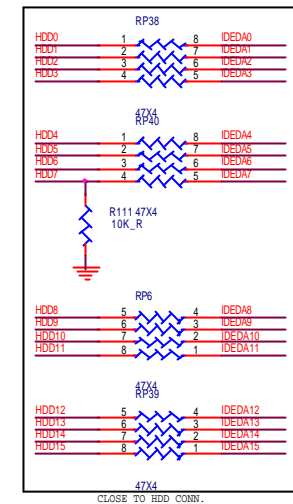
2

1

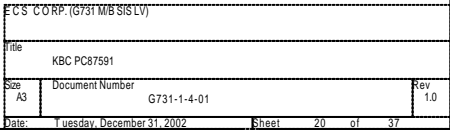


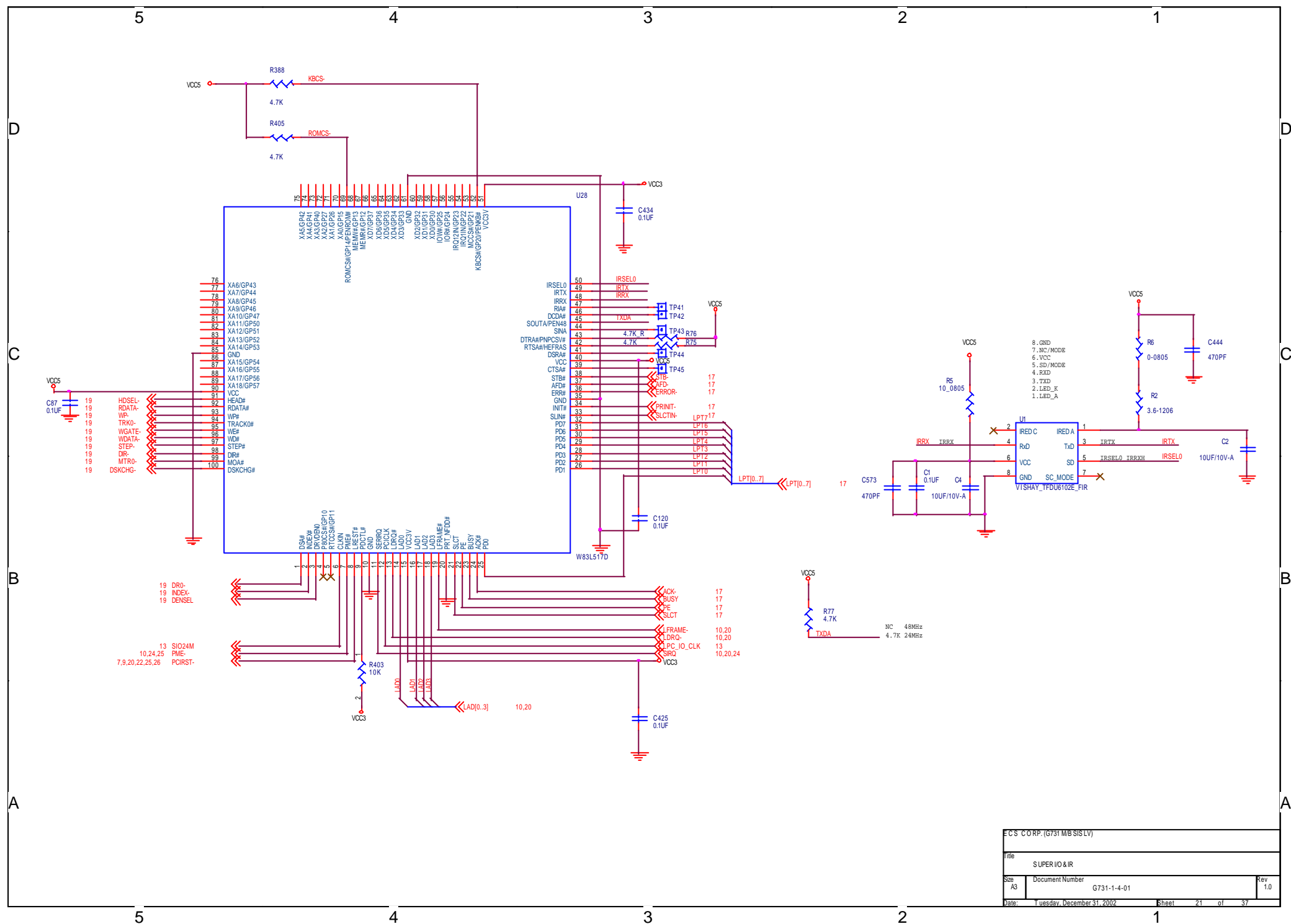


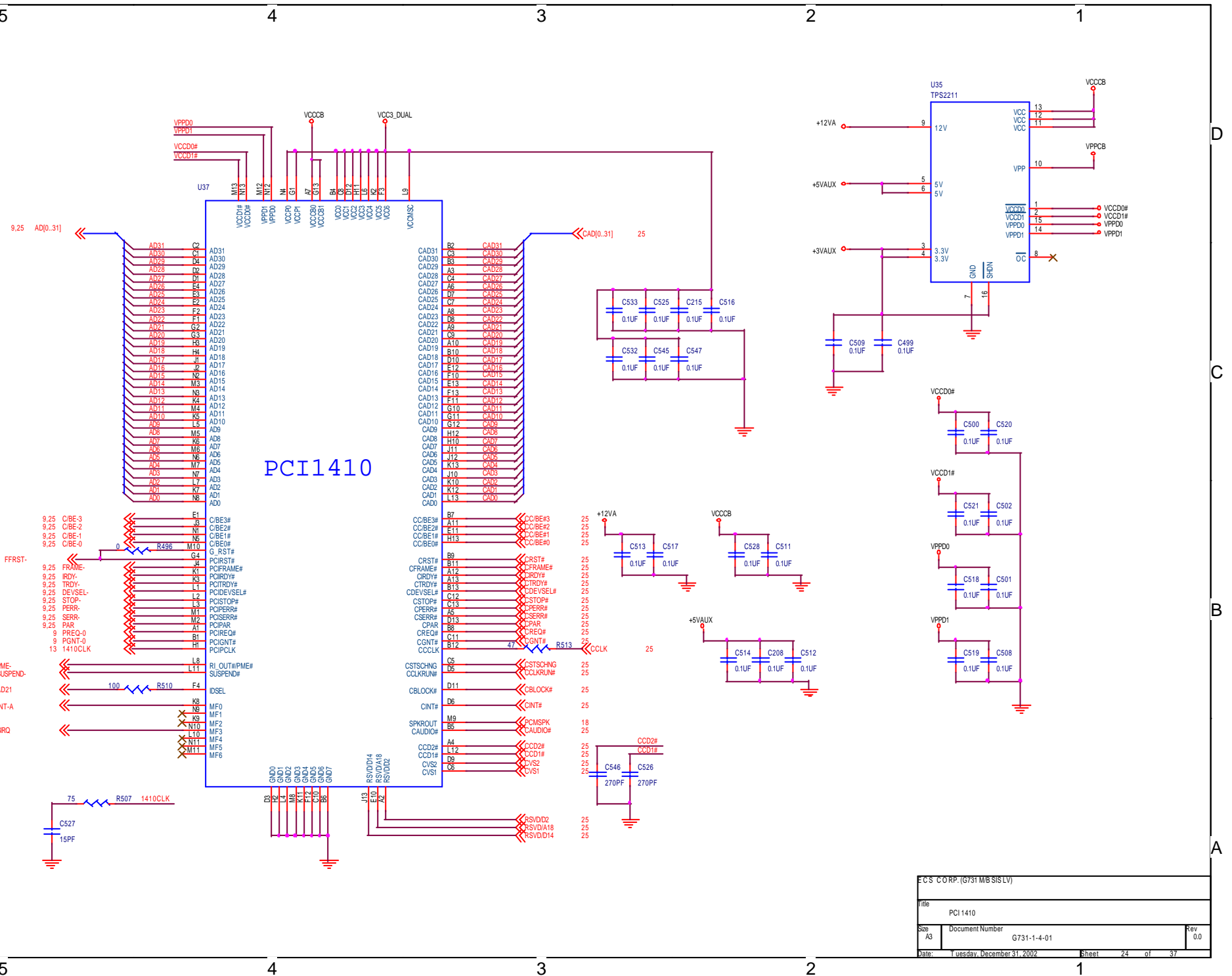
D The length must be less than 1 inch

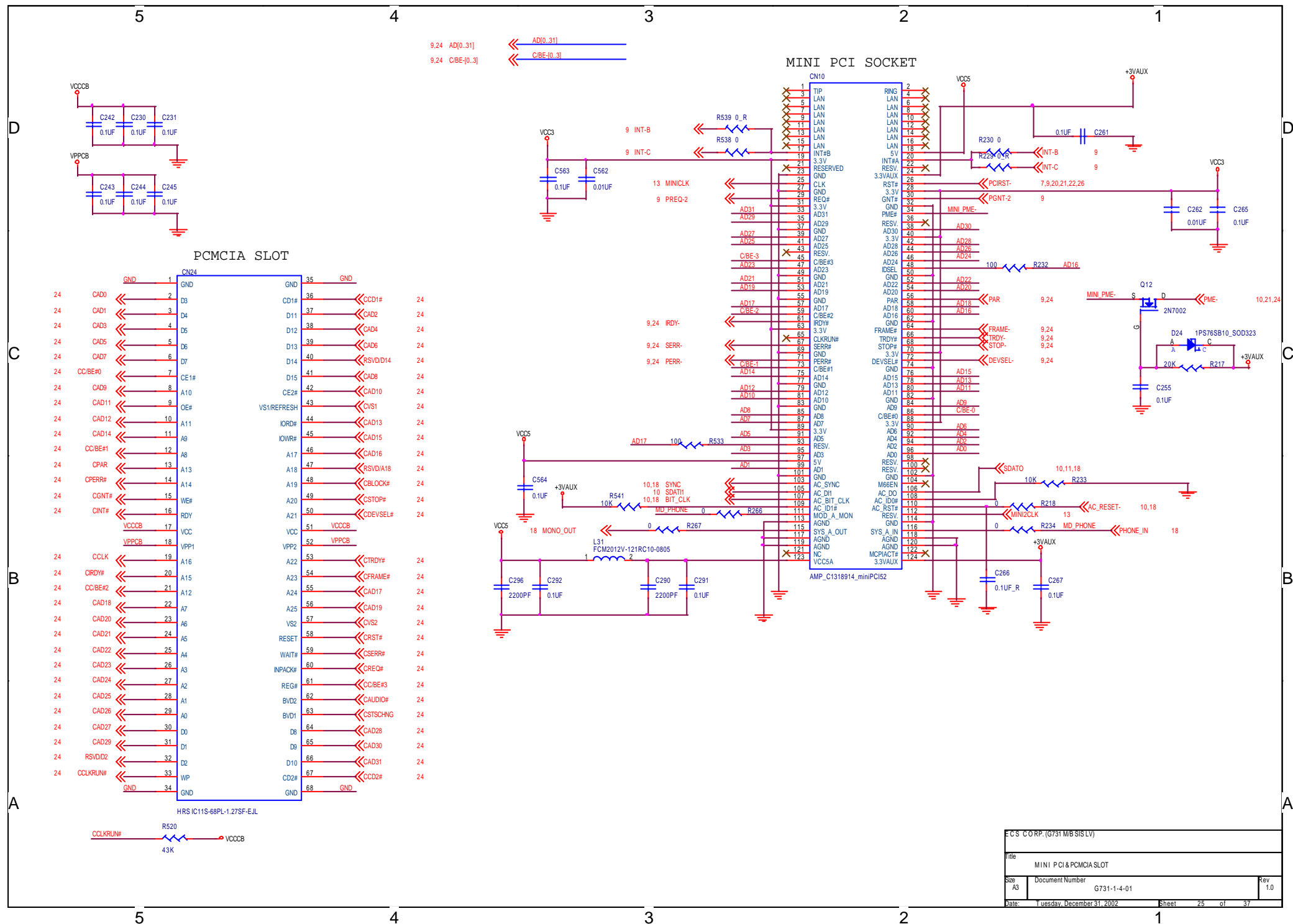


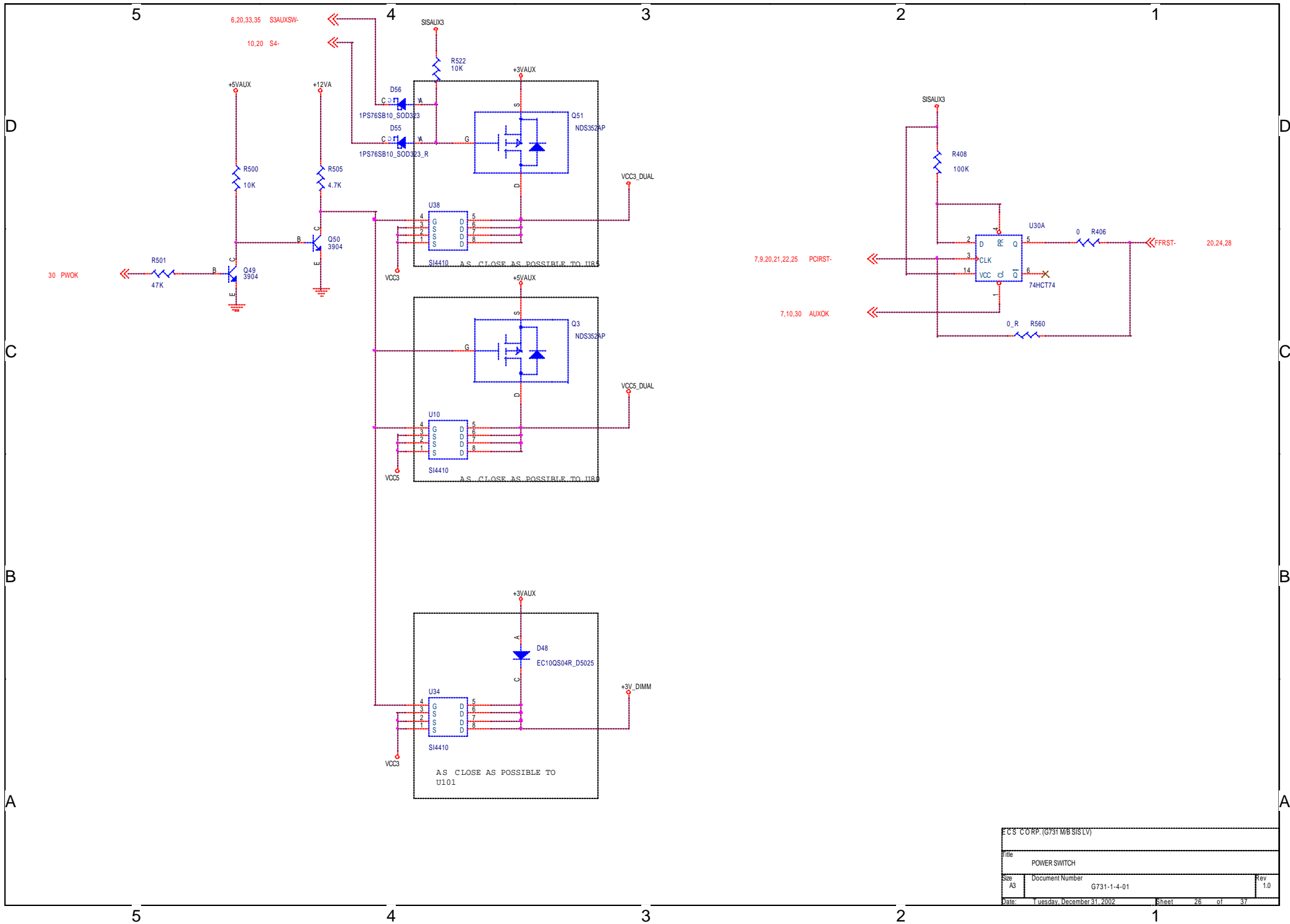
ECS CORP.(G731 M8 SIS LV)			
Title HDD & CD-ROM CONN.			
Size A3	Document Number G731-1-4-01		Rev 1.0
Date	Tuesday December 31 2002	Sheet	10 of 37





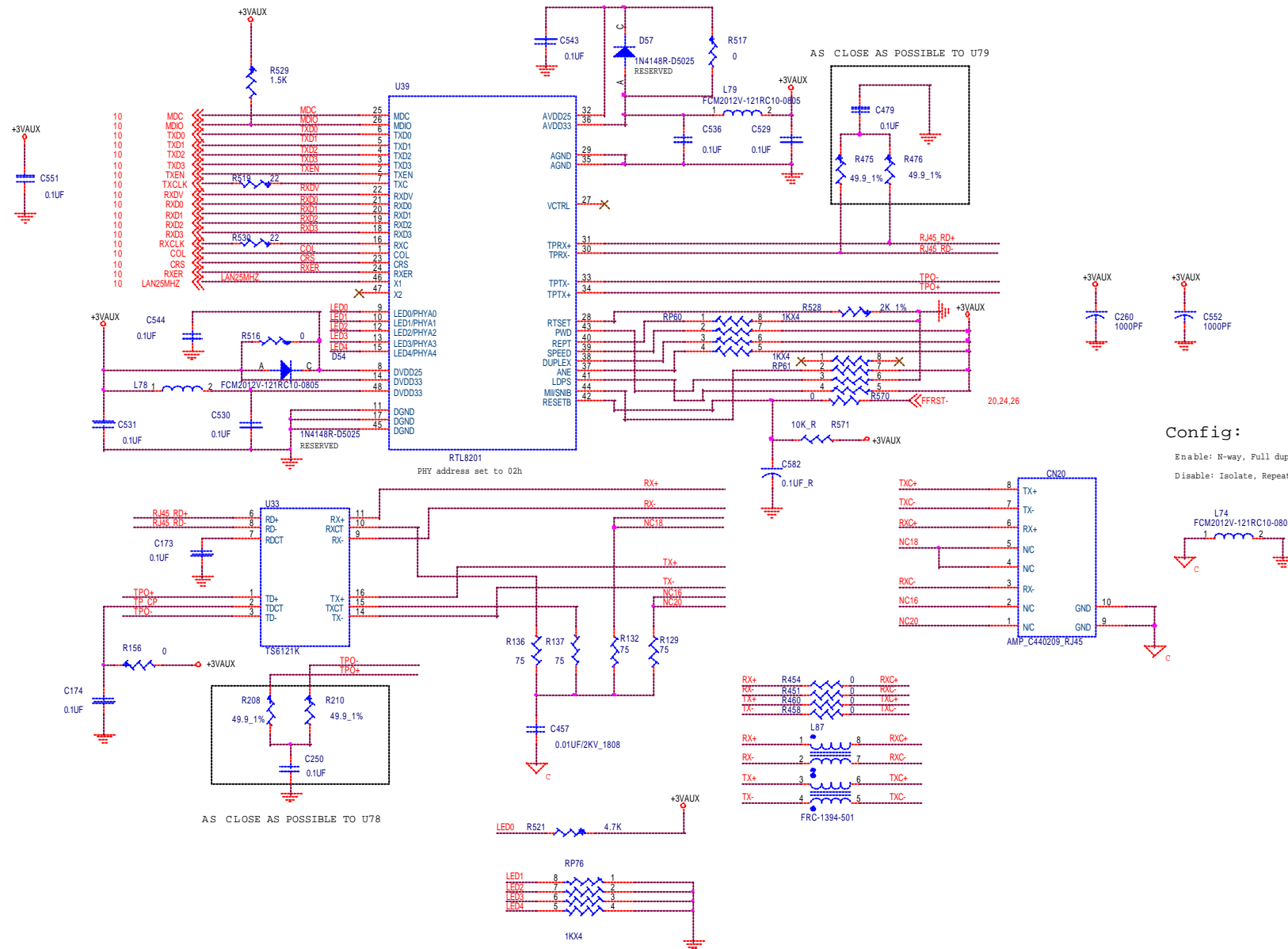






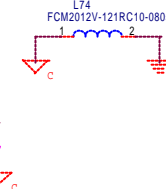
ECS CORP. (G731MB/SISLV)			
Title POWER SWITCH			
Size A3	Document Number G731-1-4-01		Rev 1.0
Date: Tuesday, December 31, 2002		Sheet 26 of 37	

RTL8201

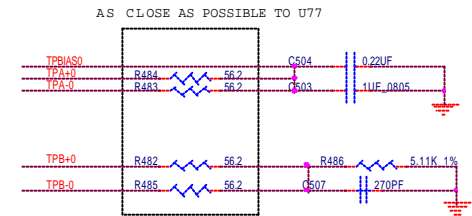
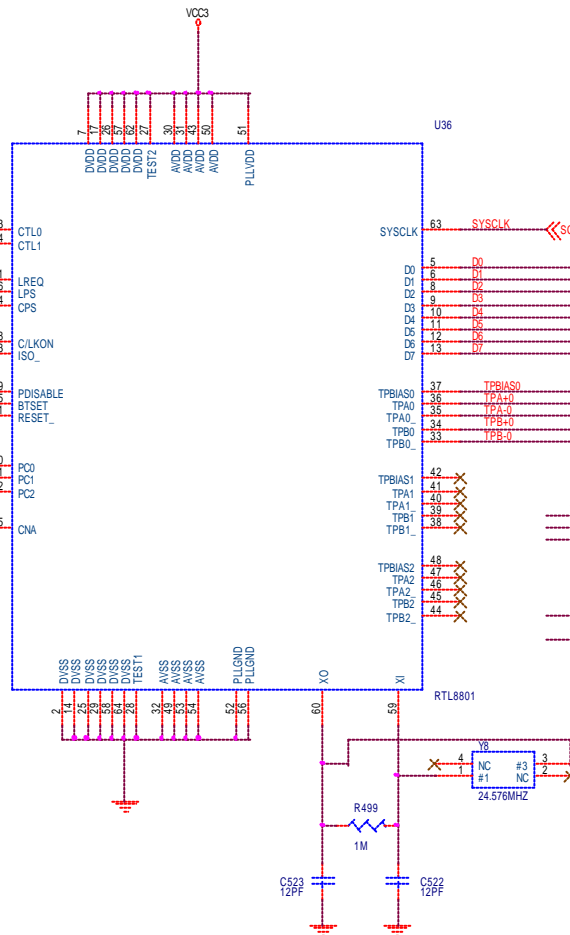
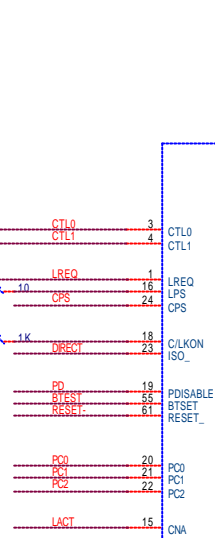
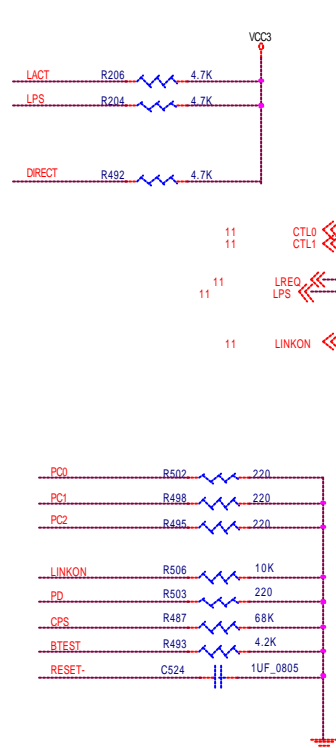


Config:

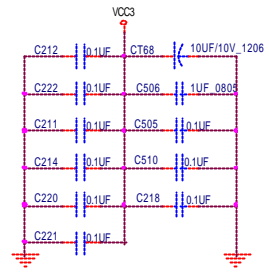
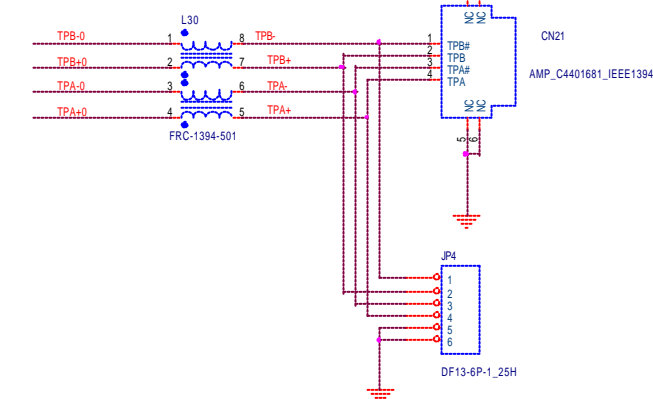
Enable: N-way, Full duplex, 100Mbps, Link Down Power Saving
Disable: Isolate, Repeater Mode



ECS CORP. (G731 MB SIS LV)			
Title LAN			
Size A3	Document Number G731-1-4-01		Rev 1.0
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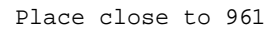


AS CLOSE AS POSSIBLE TO U77

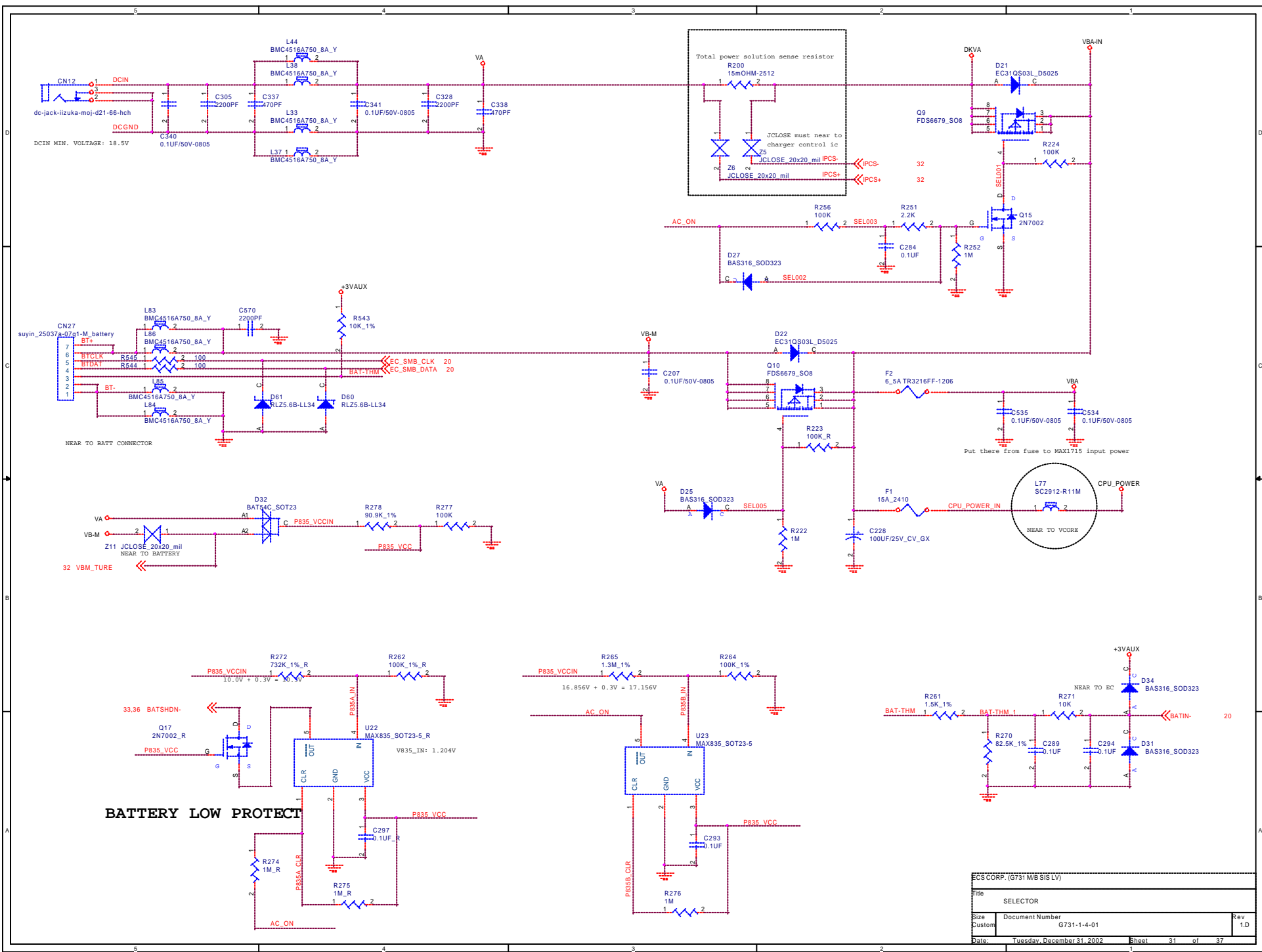


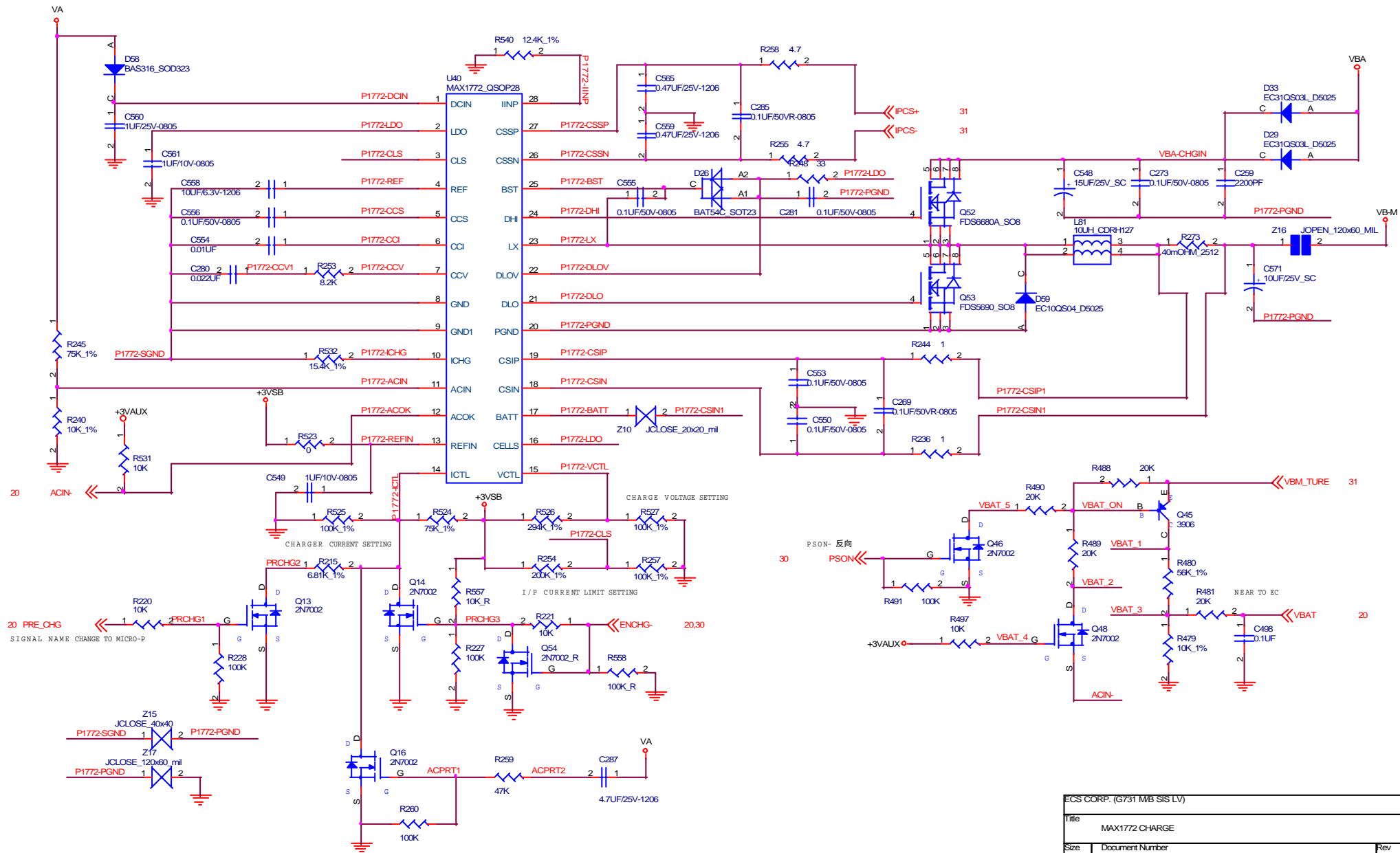
FCS CORP. (G731MBSISLV)			
Title			
IEEE 1394			
Size	Document Number	Rev	
A3	G731-1-4-01	1.0	
Date:	Tuesday, December 31, 2002	Sheet	29 of 37

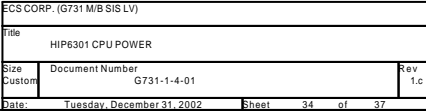
SiS is not responsible for RTC problems from foreign designs.



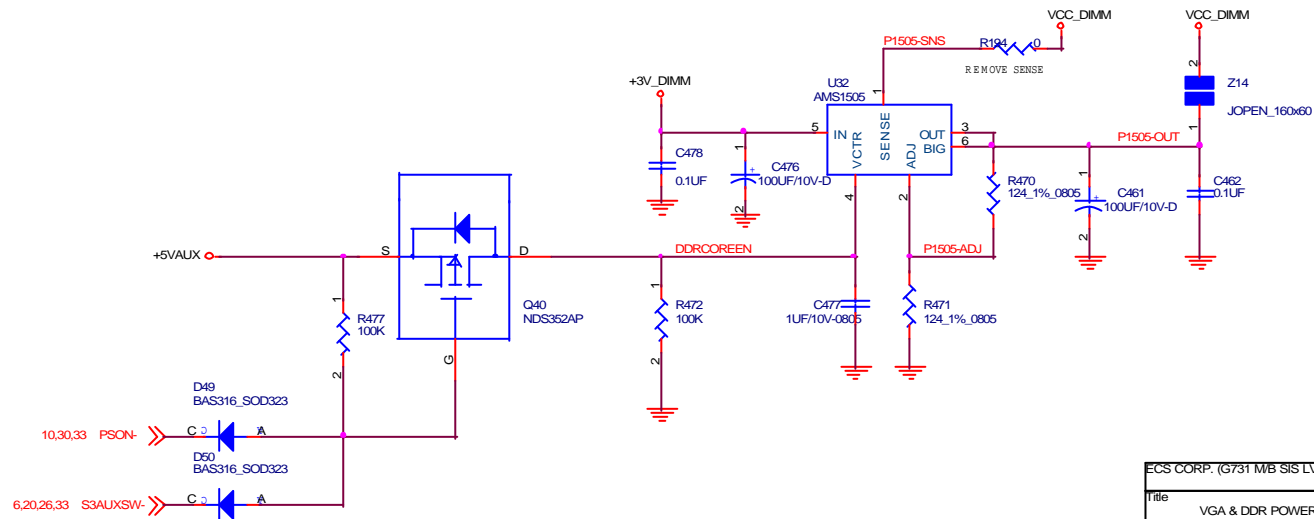
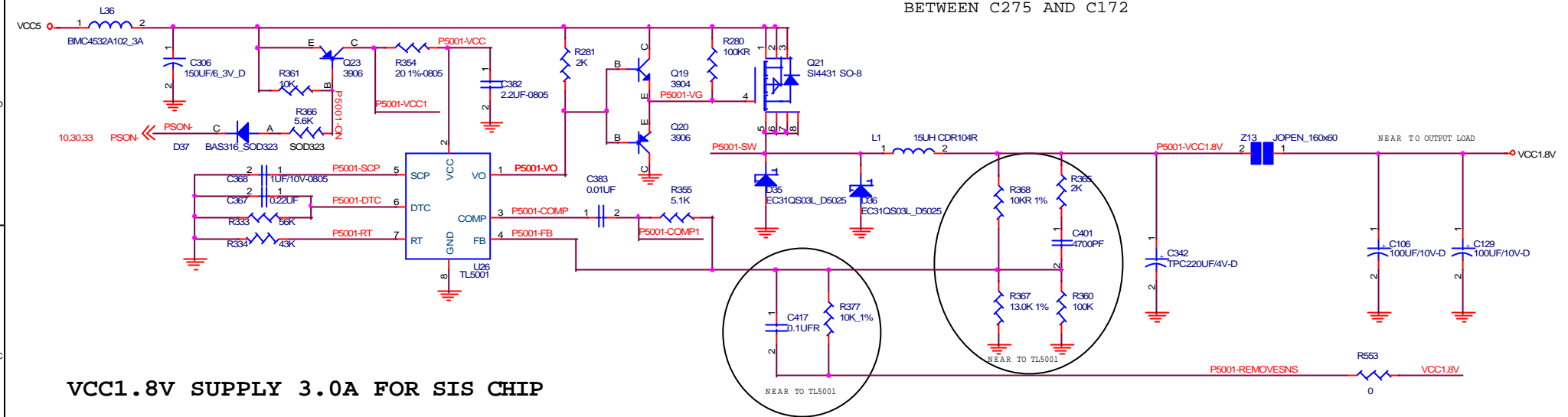
ECS CORP.(G3TMB SIS LV)			
Title			
R T C & POWER SWITCH			
Size	Document Number		Rev
A3	G731-1-4-01		1.0
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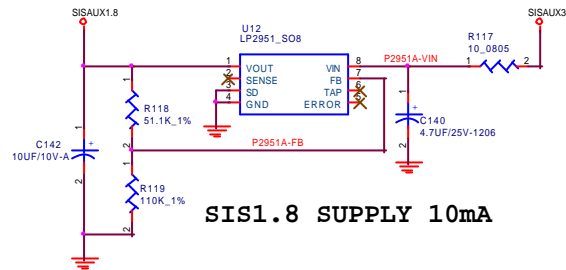




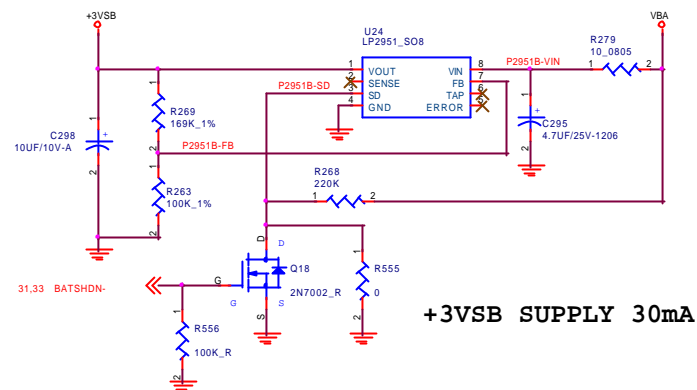
BETWEEN C275 AND C172



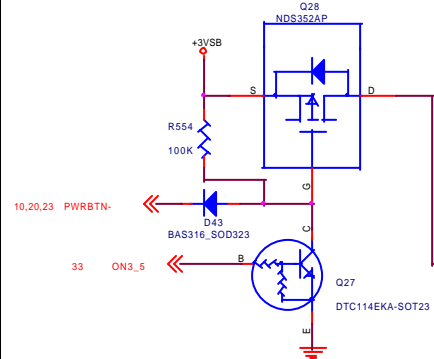
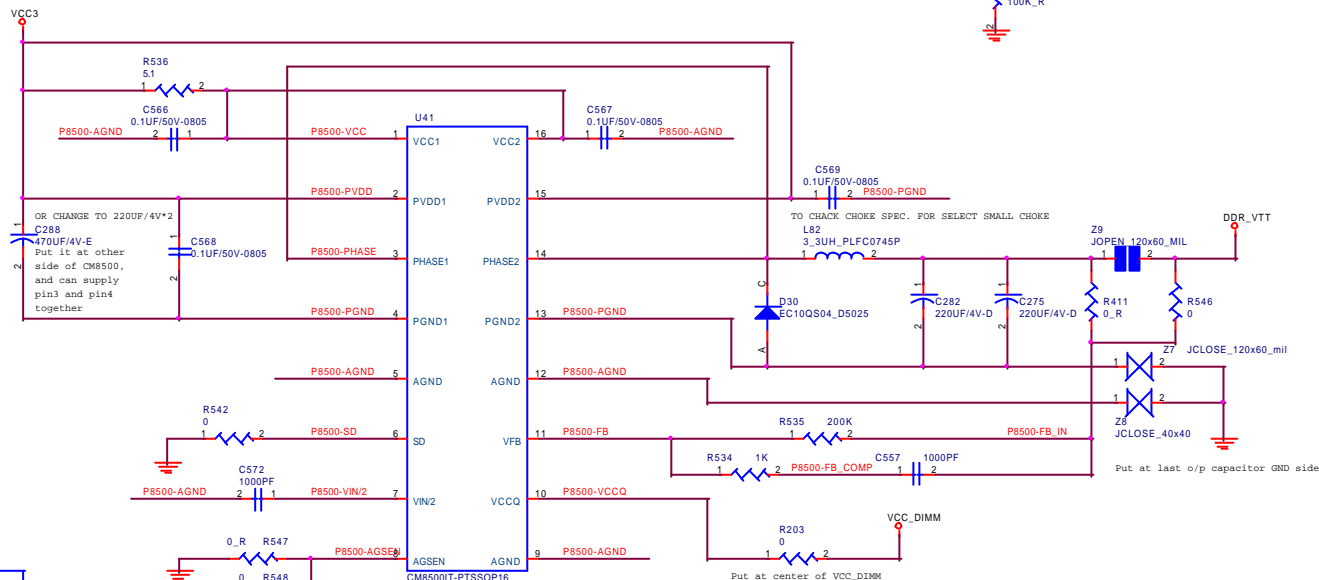
ECS CORP. (G731 MB SIS LV)		
Title		
VGA & DDR POWER		
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SIS1.8 SUPPLY 10mA



+3VSB SUPPLY 30mA



ECS CORP. (G731 M/B SIS LV)			
Title	DDR_VTT & STAND BY POWER		
Size	Document Number	Rev	
Custom	G731-1-4-01	1.0	
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HISTORY:

1.0 : INITIAL

1.A : To improve a stable switch waveform on VCC1.8V

1.B : Decrease TV-OUT EMI and Headphone high-frequence noise

1.C : ISL6206 shortage.

1.D : 68UF/25V_CV_AX shortage.

1.E : Add new EMI solution for passing LGA test.

ECS CORP. (G731 MB SIS LV)			
Title HISTORY			
Size B	Document Number G731-1-4-01		Rev 1.E
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