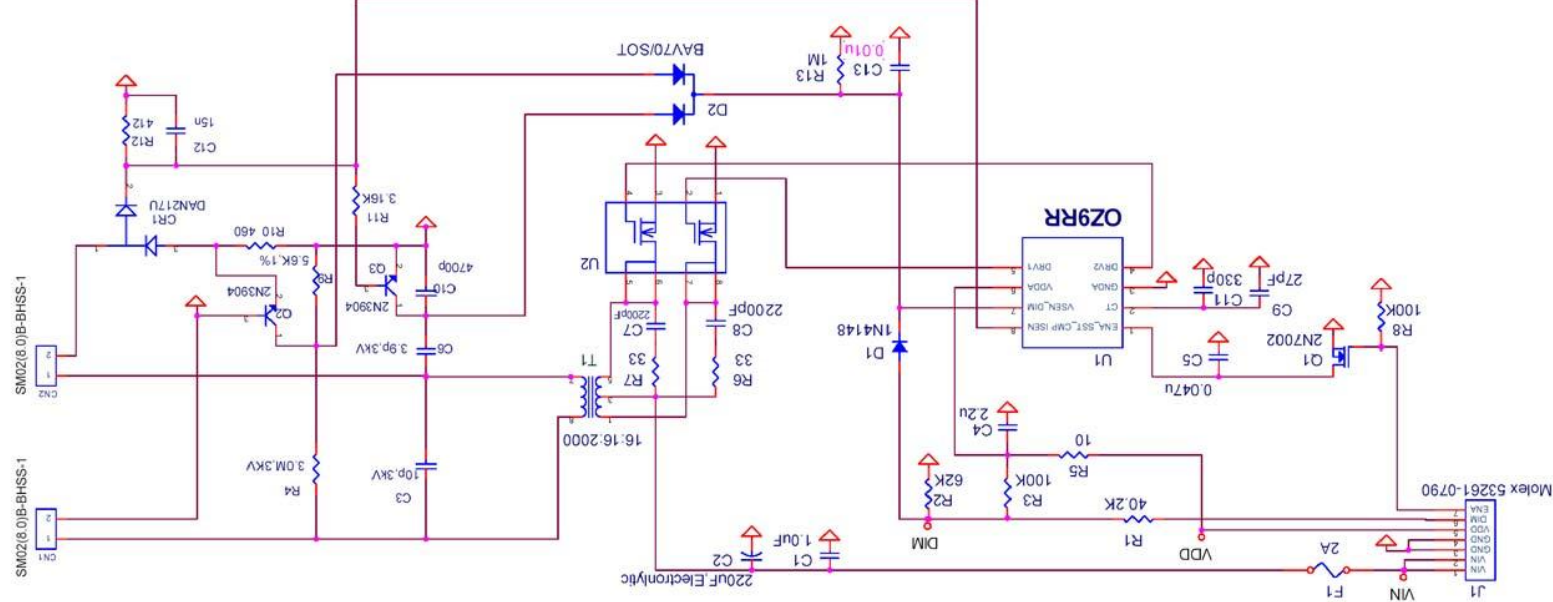


Figure 2. OZ9RR Typical Application Circuit: Separate Ground



VIN : 10.8V -13.2V
VDD : 4.75V--5.25V
ENA : 0V---1V, enable; 3V---5V, disable
DIM: 0.5V Min. Brightness; 3.6V Max. Brightness

Note: Each CCFL has its individual ground

Refer to: OZ9RR PCB Layout Design Guideline

Note: Each CCFL has its individual ground