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Design of an Efficient Rectifier Circuit for RF Energy Harvesting System

Parna Kundu (Datta), Juin Acharjee and Kaushik Mandal

Abstract

An efficient rectifier system along with an impedance matching network is proposed in this article. Impedance matching network is designed using two microstrip lines. Rectifier system for better RF to DC conversion is designed using a bridge rectifier. The proposed rectifier system provides a maximum efficiency of 50%. The impedance matching network improves the overall system performances significantly. The circuit simulator ADS 2015 is used for this system design. Performances of the proposed system are analysed using simulation results only. This proposed rectifier system along with the impedance matching network can be useful for the design of an efficient RF energy harvesting system.

Keywords:

Impedance matching network and rectifier circuit;
Full wave bridge rectifier;
Energy converter;
ADS-2015 circuit simulator;
GSM 900.

I. INTRODUCTION

E-recently, radio frequency (RF) energy harvesting using microstrip antenna has gained a great research interest for low power applications. It consists of microwave antenna that collect ambient electromagnetic energy from the surrounding environment and a rectifier circuit which convert the collected RF energy to dc power. Since available energy can be received from any direction, an omnidirectional antenna with high gain is required to receive both horizontally and vertically polarized waves. Antenna can be designed for single band as well as multiple band reception. This basic multiband antenna with higher gain can be used in a RF energy harvesting system to collect ambient RF energy from the surrounding environment/BTS towers. An impedance matching network is essential for the efficient transmission of received power to the rectifier stage. After that the RF signals is converted in to DC using highly efficient rectifier circuits. This is illustrated using the following simplified block diagram Fig. 1.

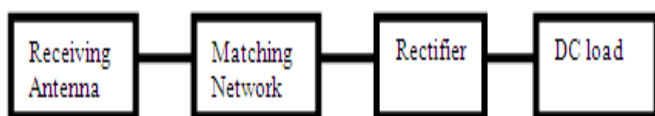


Fig. 1. Block diagram of a RF energy harvesting system.

Rectenna, short for rectifier and antenna was first designed by Brown using dipole antenna [1]. A fractal multiband

antenna for energy harvesting applications [2], array with a reconfigurable dc output network for harvesting base-station power [3], optimal design of an antenna array for energy harvesting [4], and enhanced RF power harvesting [5] are reported for RF energy harvesting system design. A differential microstrip antenna [6] with improved gain for RF energy harvesting was presented by Arrawatia et al. The antenna was designed and fabricated for GSM900 band (890-960 MHz) with a gain of 8.5 dBi and peak efficiency of 65.3%. The same group of researcher also proposed a broad band bent triangular omnidirectional antenna [7] for RF energy harvesting. The antenna has a bandwidth of 850MHz to 1.94GHz with a peak efficiency of 60%. S. Ghosh et al. proposed a circularly polarized efficient multiband microstrip antenna [8] to gather the electromagnetic energy which are radiated from the target cell towers in the GSM and CDMA bands [10]. By using a schottky diodes these energy converted in into DC power [11]. General conversion efficiency of the traditional rectenna system was 23% for frequency band of GSM 900 [12]. Rectification of the RF energy received by the antenna is very much important in the rectenna design. The efficiency of an energy harvesting system depends on RF to DC conversion efficiency of the rectifier and receiving antenna design.

This paper presents the design of an efficient impedance matching network for 2.5 GHz and a rectifier circuit which can be used in rectenna [14] systems for higher RF to DC conversion efficiency. These circuits are designed using ADS 2015. Efficiency of the rectifier is calculated and the performance of the system is compared to understand the effect of an impedance matching network.

II. DESIGN OF PROPOSED IMPEDANCE MATCHING NETWORK AND RECTIFIER CIRCUIT

The antenna for the energy harvesting system is considered to be operated at 2.5 GHz with output impedance of 50 ohm. The input impedance of the rectifier is to be matched with the output impedance of the antenna to maximize the power transfer and minimize the signal reflection from the load. A Bridge rectifier is used for RF to DC conversion. The input impedance of the bridge rectifier is calculated. Two centre taps are added to the input network and the output network to find out the input impedance of the rectifier [15]. Now if the output load (R_D) is equally divided then there will be no current flow along the connection between the input and output centre taps. That means the alternating voltages V_1 and V_2 must be symmetric about a DC offset at half the output voltage.

$$\text{If, } V_{in} = V_p \sin \phi$$

$$\text{then } V_1 = (V_m + V_p \sin \phi) / 2 \dots\dots\dots(1)$$

$$\text{and } V_2 = (V_m - V_p \sin \phi) / 2$$

These two voltages are identical apart from 180° phase difference and the AC component in each case is half of the input voltage. It appears that the bridge circuit splits the input voltage into two halves in antiphase. These two halves are then simultaneously applied to a pair of voltage doubler. Since there is no overall voltage multiplication

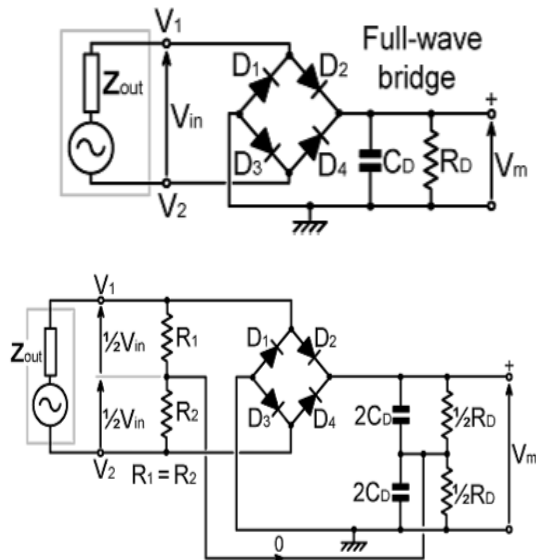


Fig. 2. General circuit diagram of the Bridge rectifier (a) Full wave bridge rectifier, (b) Full wave bridge rectifier (adding centre taps).

The input impedance is

$$R_{zin} = (R_D + 2R_{diode}) / 2 \dots\dots\dots(2)$$

In this paper R_D is considered as 500 ohm and $C_D = 0.5\text{pF}$. A Schottky diode (HSM8101) having cut-in voltage 0.25 V and dynamic resistance 9.64 ohm is used as rectifying element in the bridge rectifier circuit. Now, from equation 2 the calculated input impedance (R_{zin}) of the rectifier is 259.64 ohm.

The impedance matching circuit is designed by adding two microstrip lines having $W_1 = 0.257045\text{mm}$, $L_1 = 15.6270\text{mm}$, and $W_2 = 0.257045\text{mm}$, $L_2 = 4.944530\text{mm}$.

III. RESULT AND ANALYSIS

The S_{11} characteristic of the matching network is shown in Fig. 3. As per the requirement it is matched at the 2.5 GHz to transfer maximum power from the antenna to the load.

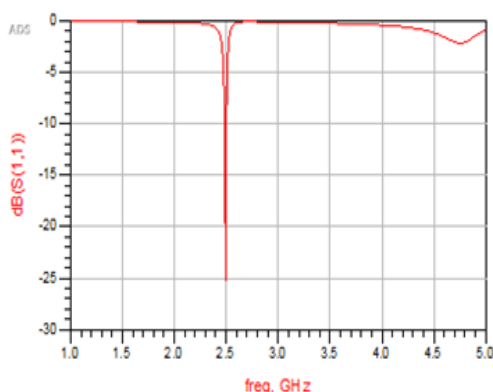


Fig. 3. Effect of Reflection Coefficient (S_{11}) at 2.5GHz

Schematic circuit connectivity for the rectifier system along with the impedance matching network is presented in the Fig. 4.

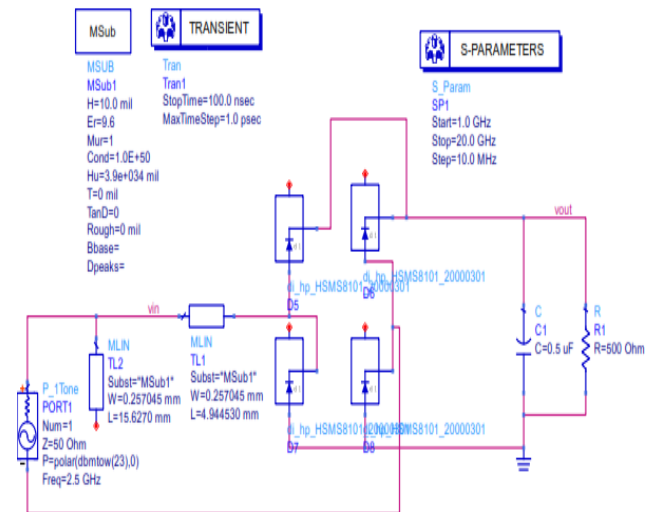


Fig.4. Schematic circuit connectivity for the rectifying system along with the impedance matching network.

Calculation of Rectifier Efficiency

The antenna is directly connected with the simply bridge rectifier circuit with impedance matching. The efficiency of the RF to DC conversion is defined as the ratio of the output dc power (P_{out}) and input RF power (P_{in}).

The efficiency of the rectifier (η) can be calculated by

$$\eta = \frac{P_{out}}{P_{in}} \dots\dots\dots(3)$$

$$\text{Where, } P_{out} = V_{out,DC}^2 / R_{load} \dots\dots\dots(4)$$

V_{out} = Output DC Voltage

R_{load} = Resistive load present at the output

Efficiency of the rectifier is calculated for different input power and presented in the Table 1.

Table 1. Calculated Efficiency of the Bridge Rectifier

Input Power(dBm)	Input Power(mWatt)	$V_{out}(\text{Volt})$	Rectifier Efficiency (%)
0	1	0.131	3.43
5	3.16	0.502	15.94
7	5.012	0.742	21.96
10	10	1.23	30.26
12	15.84	1.68	35.63
15	31.62	2.58	42.10
17	50.12	3.39	45.86
20	100	5.01	50.20
23	199.52	6.94	48.28

The rectifier along with impedance matching network is highly efficient with maximum efficiency of 50.20% for the 20dBm input power. The proposed rectifier system is able to produce sufficient DC voltage form low incident RF power. This simple rectifier system is very efficient for design of a RF energy harvesting system. The input power versus efficiency variation is shown in Fig. 5.

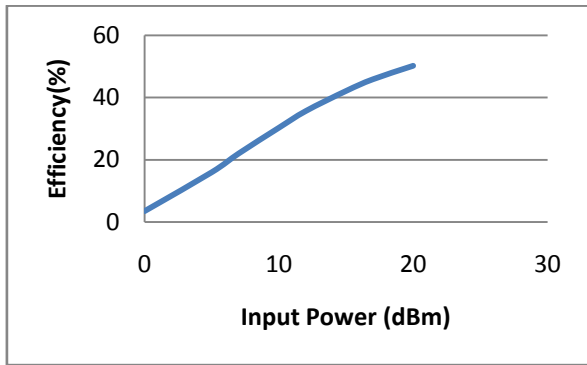


Fig.5. Efficiency of the bridge rectifier for different input power

A comparative study of the rectifier performance, with and without impedance matching network is shown in Fig.6. It is observed that due to impedance matching the efficiency of RF to DC conversion is so high.

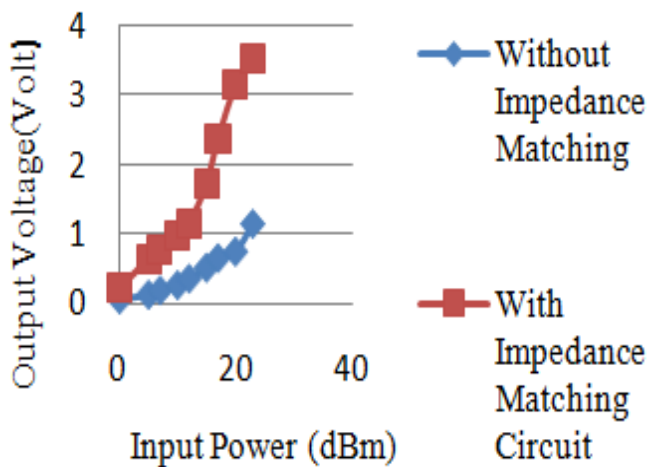


Fig.6. Output voltage versus input power.

IV CONCLUSION

An efficient rectifier system for RF energy harvesting system is designed using ADS-2015 circuit simulator. It consists of an impedance matching network and a bridge rectifier. Schottky diode is used as a rectifying element as it has low forward voltage drop and a very fast switching speed. Impedance matching network effectively improves the system performance. This system is found suitable for a part of RF energy harvesting system.

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