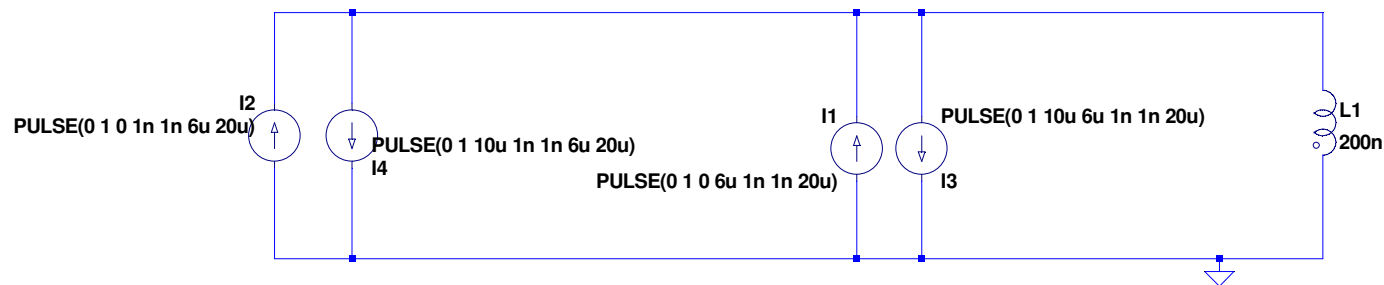


FULL BRIDGE PRIMARY CURRENT "IMITATOR"

K L1 L2 1



.tran 0 10m 0 startup

