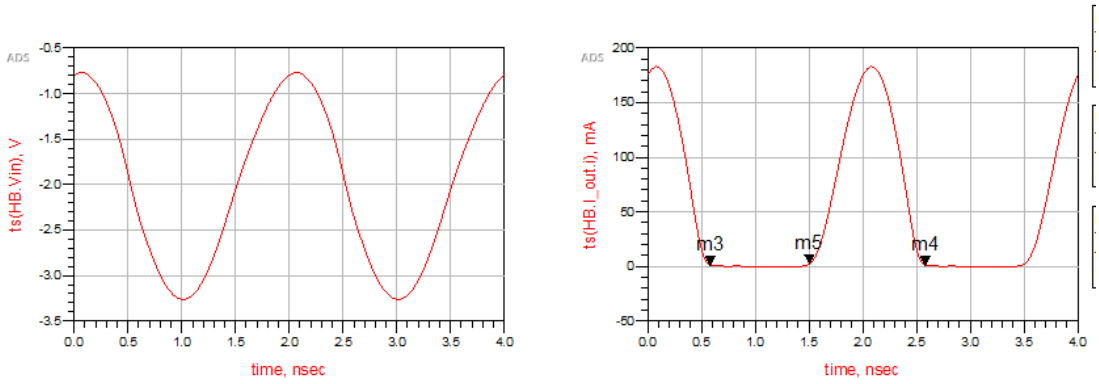
Hello friends,

In ADS, I simulated one FET transistor with a bias of Vgs=-2V and Vgd=12V. I'm trying to figure out the whether Vgs=-2V is the most right value in class C PA design, so I simulate the time domain waveform in ADS.

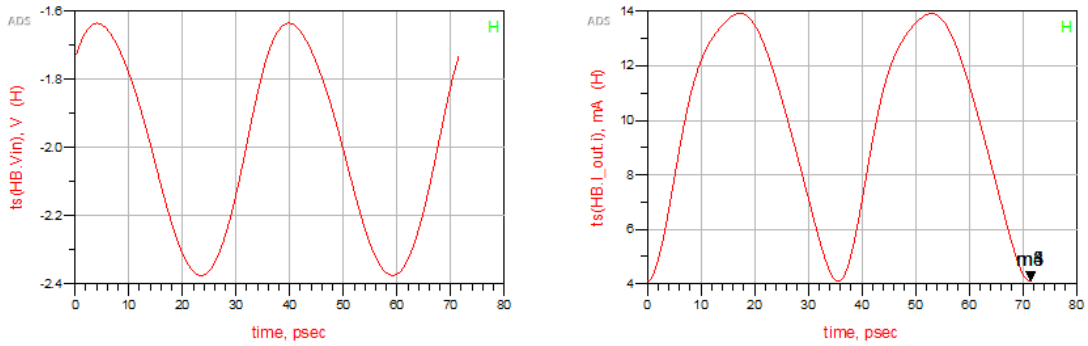
1. f_{in}=0.5 GHz

time domain waveform of gate voltage and drain current in 0.5GHz



2. f_{in}=28 GHz

time domain waveform of gate voltage and drain current in 28GHz



So…, why there is no obvious conduction zone and cutoff zone in 28GHz as 0.5GHz does? Is there any good way to simulate Class C PA?