

Compal confidential

Schematics Document
Mobile Yonah uFCPGA with Intel
Calistoga_PM+ICH7-M core logic
2005-06-01

REV:0.1

Security Classification		Compal Secret Data		Title	
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Model:HAQAA

File Name : LA-3011

NAPA

Mobile Yonah

uFCPGA-479 CPU

page 4,5,6

Thermal Sensor
ADM1032AR

page 4

Clock Generator
ICS9LPRS325AKLFT

page 15

Accelerometer
KXP84-0200

page 28

Intel Calistoga MCH
945PM
PCBGA 1466
page 7,8,9,10,11,12

H_A#(3...31)

FSB

533/667MHz

H_D#(0...63)

DDR2 -400/533/667

Dual Channel

DDR2-SO-DIMM X2
BANK 0, 1, 2, 3

page 13,14

PCI-E x 16

LCD CONN

page 17

nVIDIA G72/G73
ATI M52/M54

page 18

CRT / TV-OUT

page 16

PCI-E BUS

10/100/1000 LAN
82562GX/82573E/82573V

page 26,27

Mini Express Card

page 28

RJ45/11 CONN

page 26

PCI BUS

CardBus Controller
TI PCI7412

page 22,23

Slot 0

page 23

1394 port

page 22

5in1 Slot

page 22

DMI

Intel ICH7-M

mBGA-652

page 19,20,21,22

USB2.0

Azalia

USB conn x1
(Docking)

page 34 (port 6)

USB2.0 HUB /
FP Conn

page 38 (port 0)

USB conn x2

page 24 (port 4,5)

BT Conn

page 24 (port 7)

USB conn x2
(Sub Board)

page 24 (port 2,3)

FingerPrinter UPEK TCS3C/TCD42A
USBx1

page 38

MDC1.5

page 37

Audio CKT
ALC861

page 29

AMP & Audio Jack
TPA0232

page 30

SATA Master

SATA HDD Connector

page 19

PATA Slave

PATA ODD Connector

page 19

LPC BUS

TPM1.2
SLB9635TT1.2

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Super I/O
LPC47N217&207

page 33,38

FIR

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ENE KB910QF

page 25

CIR

page 33

Touch Pad

page 37

Int.KBD

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BIOS

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LED

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RTC CKT.

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Docking

page 34

Power On/Off CKT.

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DC/DC Interface CKT.

page 39

Power Circuit DC/DC

Page,40,41,42,43,44,45,46

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Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (18.5V)	NA	NA	NA
B+	AC or battery power rail for power circuit	NA	NA	NA
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+VCCP	1.05V power rail for Processor I/O and MCH/ICH core power	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDRII Vtt	ON	OFF	OFF
+1.5VS	1.5V switched power rail for PCI-E interface	ON	OFF	OFF
+1.8V	1.8V power rail for DDRII	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail for MCH video PLL	ON	OFF	OFF
+2.5VALW	3.3V always on power rail	ON	ON	ON*
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+RTC_VCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

Internal PCI Devices

DEVICE	Bus	PCI Device ID	IDSEL #
LAN	1	D8	AD24
Azalia	0	D27	AD11
PCI-E	0	D28	AD12
USB1.1/2.0	0	D29	AD13
PCI to PCI (DMI to PCI)	0	D30	AD14
AC97 MODEM	0	D30	AD14
AC97 Audio	0	D30	AD14
PATA/SATA	0	D31	AD15
LPC I/F	0	D31	AD15
SMBUS	0	D31	AD15
CPU I/F	0	D31	AD15
DMA	0	D31	AD15
PMU	0	D31	AD15

External PCI Devices

DEVICE	PCI Device ID	IDSEL #	REQ/GNT #	PIRQ
CARD BUS	D6	AD22	2	ABCD

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 1%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0.100 V
1	8.2K +/- 1%	0.216 V	0.250 V	0.289 V
2	18K +/- 1%	0.436 V	0.503 V	0.538 V
3	33K +/- 1%	0.712 V	0.819 V	0.875 V
4	56K +/- 1%	1.036 V	1.185 V	1.264 V
5	100K +/- 1%	1.453 V	1.650 V	1.759 V
6	200K +/- 1%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

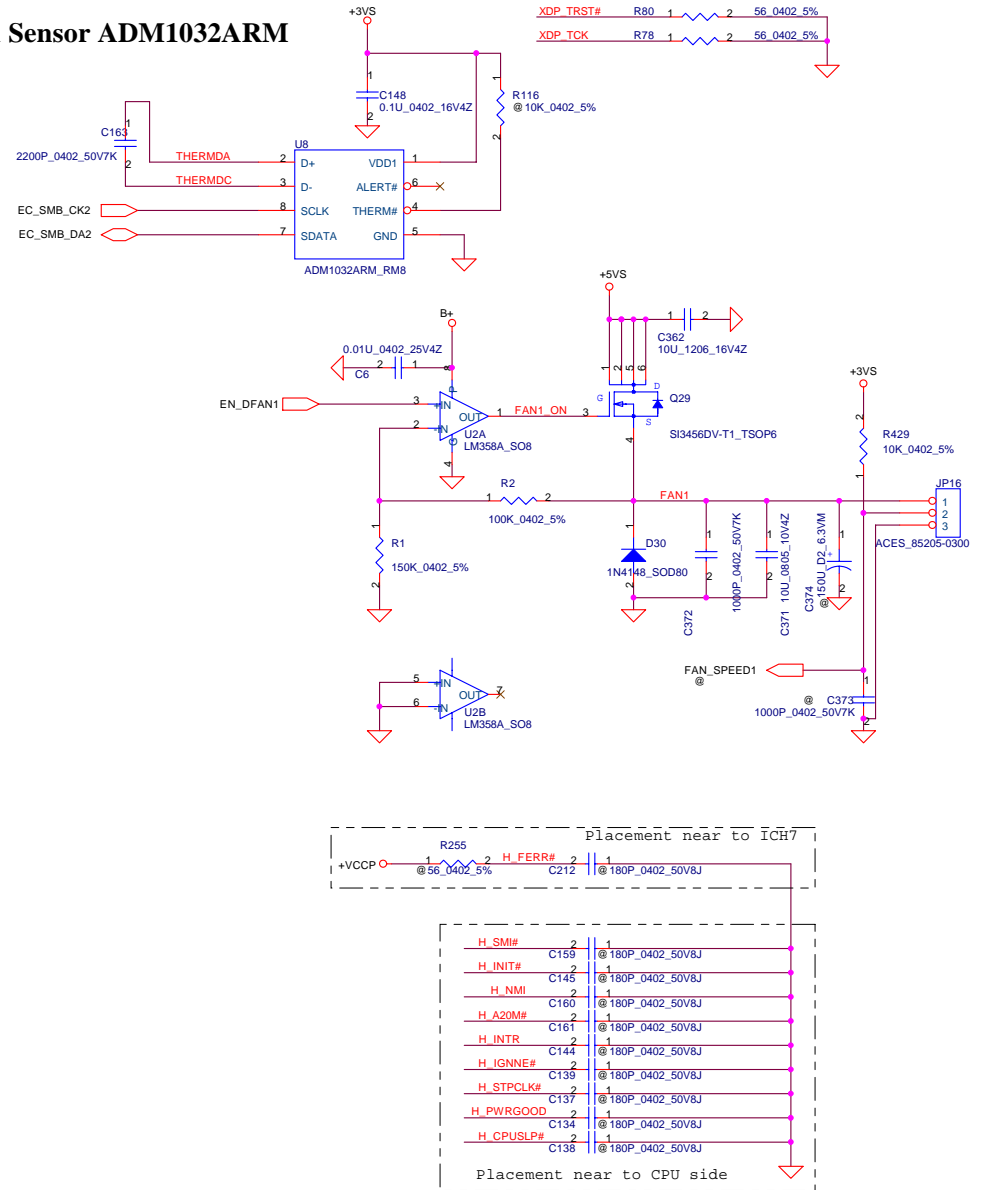
SKU ID Table

SKU ID	SKU
0	
1	
2	
3	
4	
5	
6	
7	

BTO Option Table

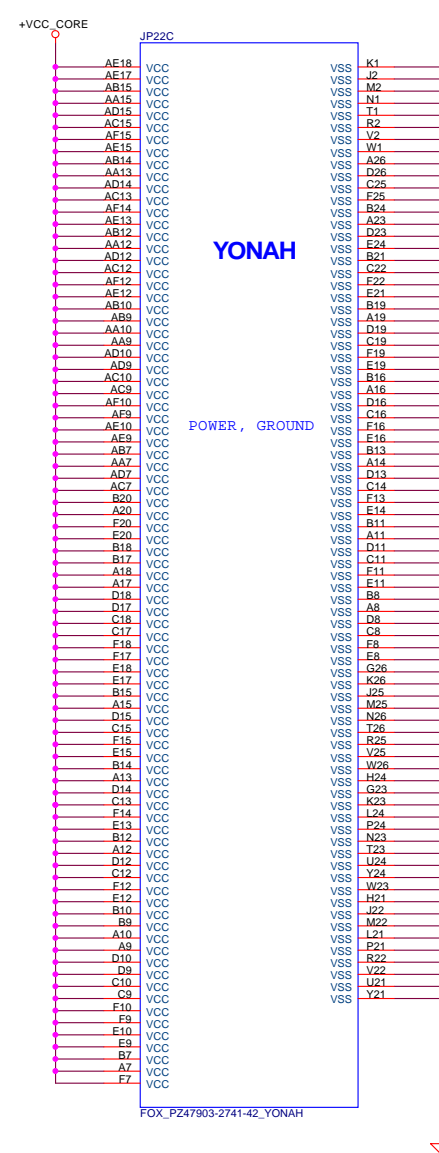
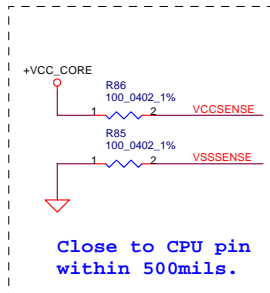
BTO Item	BOM Structure
VGA	PM@ GM@
LAN	82573G@ 82573L@ 82573@ 82562@
INT MIC.	45MIC@
DOCKING	WD@ WOD@
CIR	CIR@
FIR	FIR@
SUPER I/O	217@ SI207@
Battery	45@

Thermal Sensor ADM1032ARM

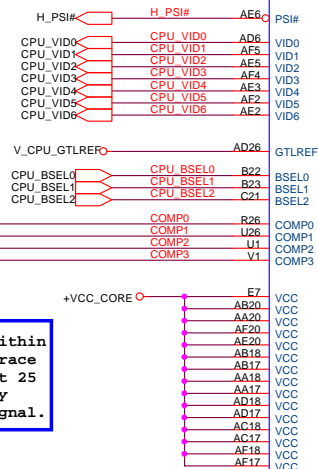


H_THERMDA, H_THERMDC routing together.
Trace width / Spacing = 10 / 10 mil

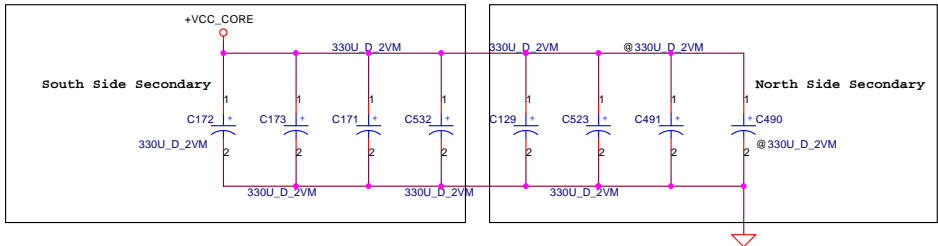
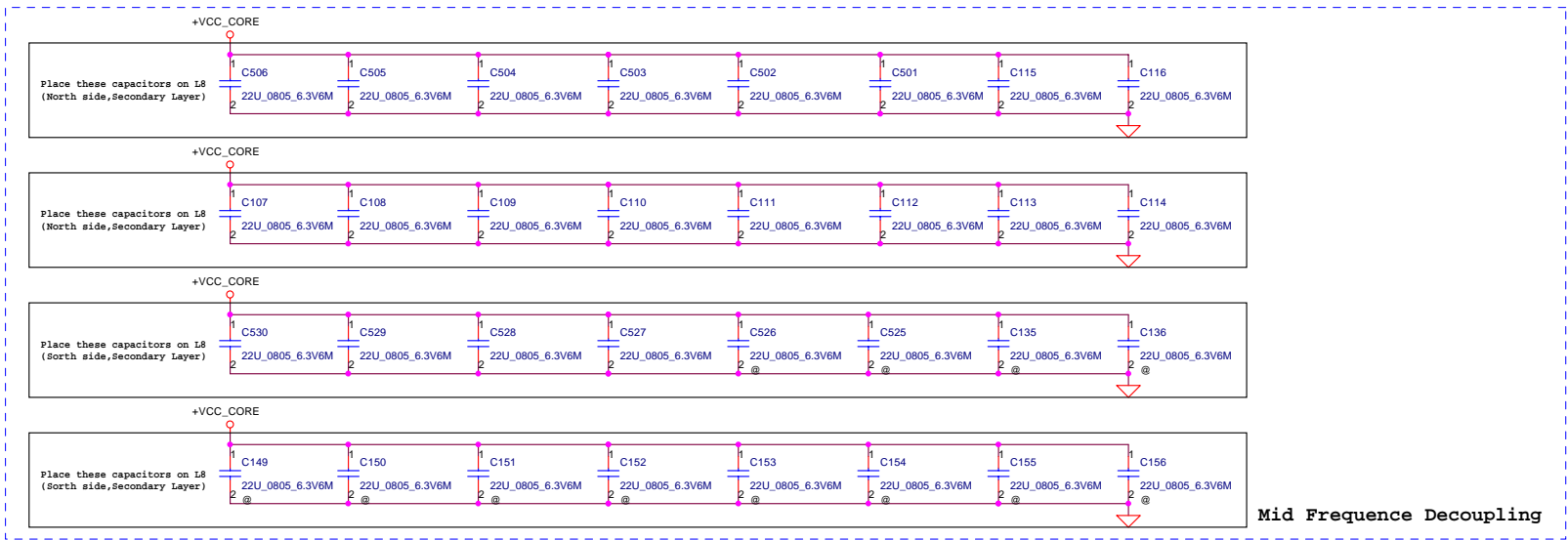
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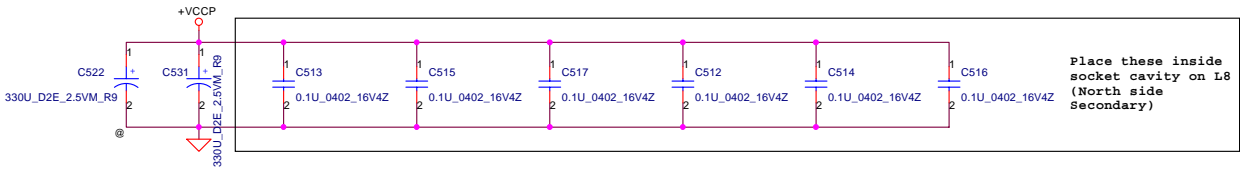
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1



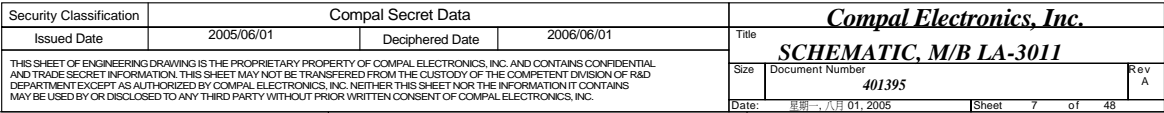
Resistor placed within
0.5" of CPU pin.Trace
should be at least 25
mils away from any
other toggling signal.

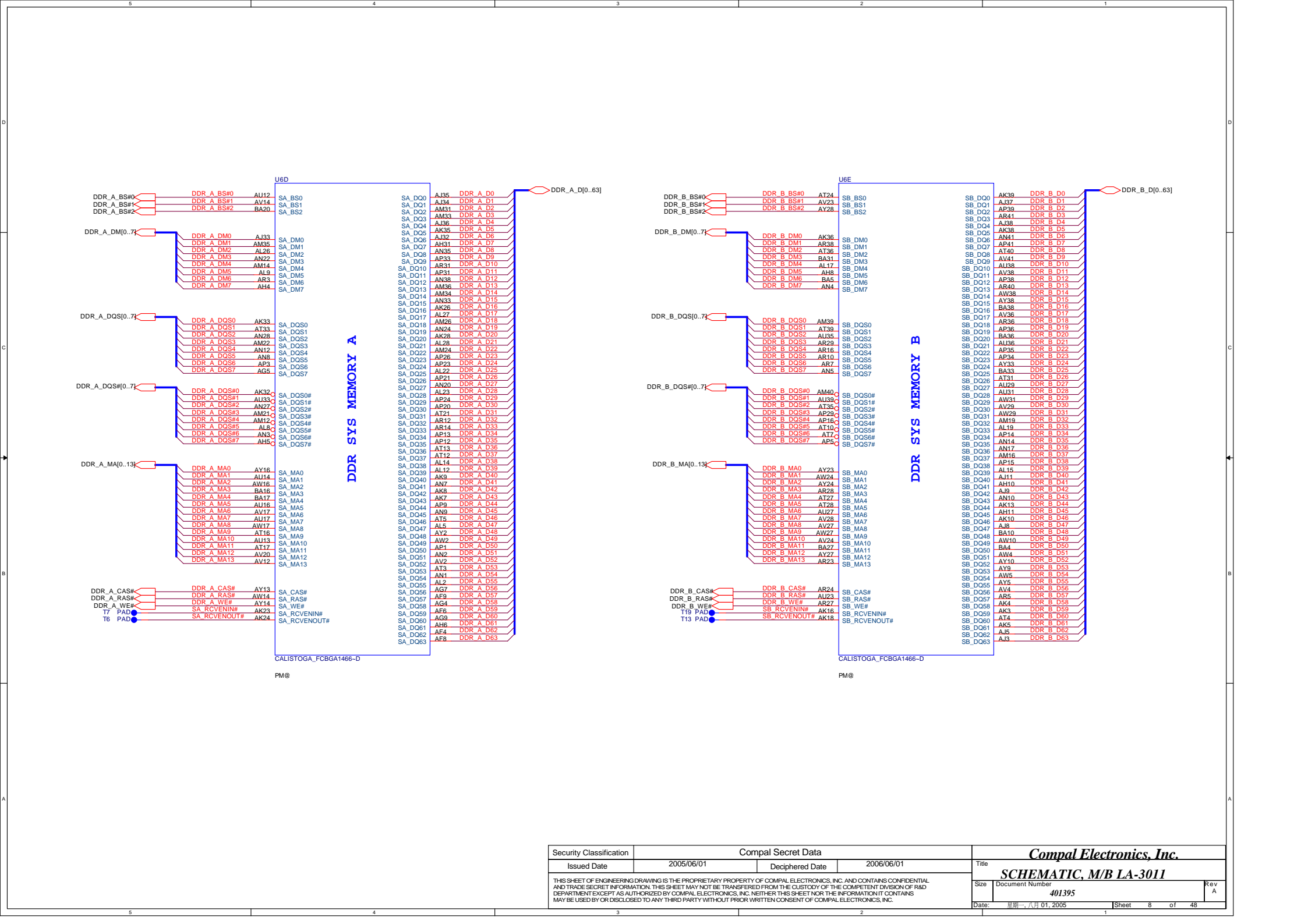


ESR <= 1.5m ohm
Capacitor > 1980uF

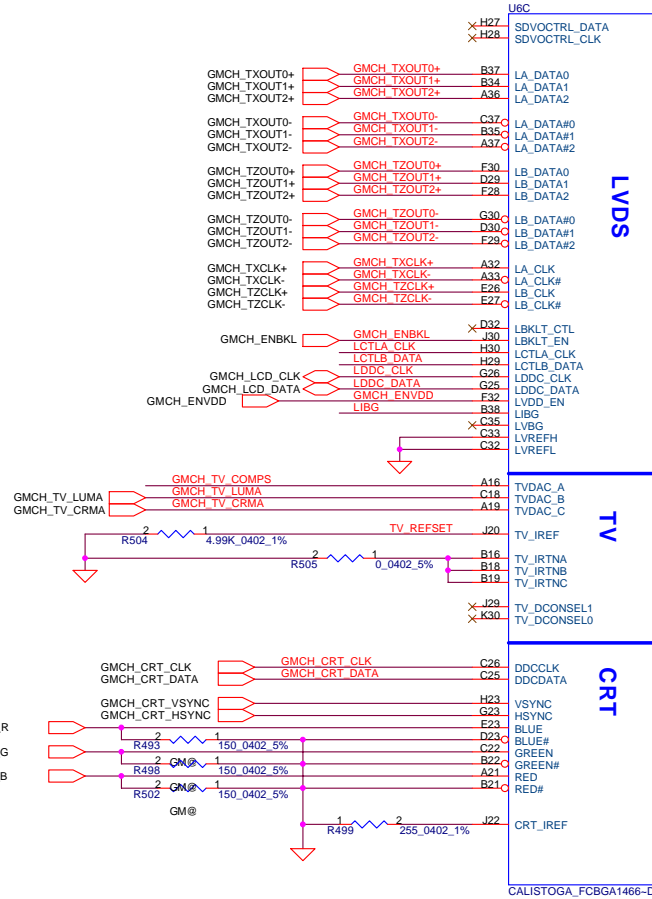
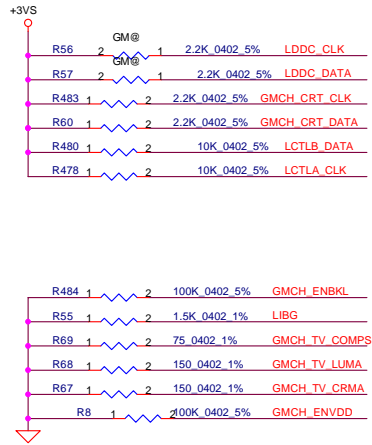


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PEGCOMP trace width and spacing is 18/25 mils.

EXP_COMP1	D40	PEGCOMP	1	+1.5VS_PCIE
EXP_COMP0	D38			
EXP_RXN0	F34	PCIE GTX C MRX N0		
EXP_RXN1	G38	PCIE GTX C MRX N1		
EXP_RXN2	H34	PCIE GTX C MRX N2		
EXP_RXN3	J38	PCIE GTX C MRX N3		
EXP_RXN4	L34	PCIE GTX C MRX N4		
EXP_RXN5	M38	PCIE GTX C MRX N5		
EXP_RXN6	N34	PCIE GTX C MRX N6		
EXP_RXN7	P38	PCIE GTX C MRX N7		
EXP_RXN8	R34	PCIE GTX C MRX N8		
EXP_RXN9	T38	PCIE GTX C MRX N9		
EXP_RXN10	V34	PCIE GTX C MRX N10		
EXP_RXN11	W38	PCIE GTX C MRX N11		
EXP_RXN12	Y34	PCIE GTX C MRX N12		
EXP_RXN13	AB38	PCIE GTX C MRX N13		
EXP_RXN14	AC34	PCIE GTX C MRX N14		
EXP_RXN15	AC38	PCIE GTX C MRX N15		
EXP_RXP0	D34	PCIE GTX C MRX P0		
EXP_RXP1	F38	PCIE GTX C MRX P1		
EXP_RXP2	G34	PCIE GTX C MRX P2		
EXP_RXP3	H38	PCIE GTX C MRX P3		
EXP_RXP4	J34	PCIE GTX C MRX P4		
EXP_RXP5	L38	PCIE GTX C MRX P5		
EXP_RXP6	M34	PCIE GTX C MRX P6		
EXP_RXP7	N38	PCIE GTX C MRX P7		
EXP_RXP8	P34	PCIE GTX C MRX P8		
EXP_RXP9	R38	PCIE GTX C MRX P9		
EXP_RXP10	T34	PCIE GTX C MRX P10		
EXP_RXP11	V38	PCIE GTX C MRX P11		
EXP_RXP12	W34	PCIE GTX C MRX P12		
EXP_RXP13	Y38	PCIE GTX C MRX P13		
EXP_RXP14	AA34	PCIE GTX C MRX P14		
EXP_RXP15	AB38	PCIE GTX C MRX P15		

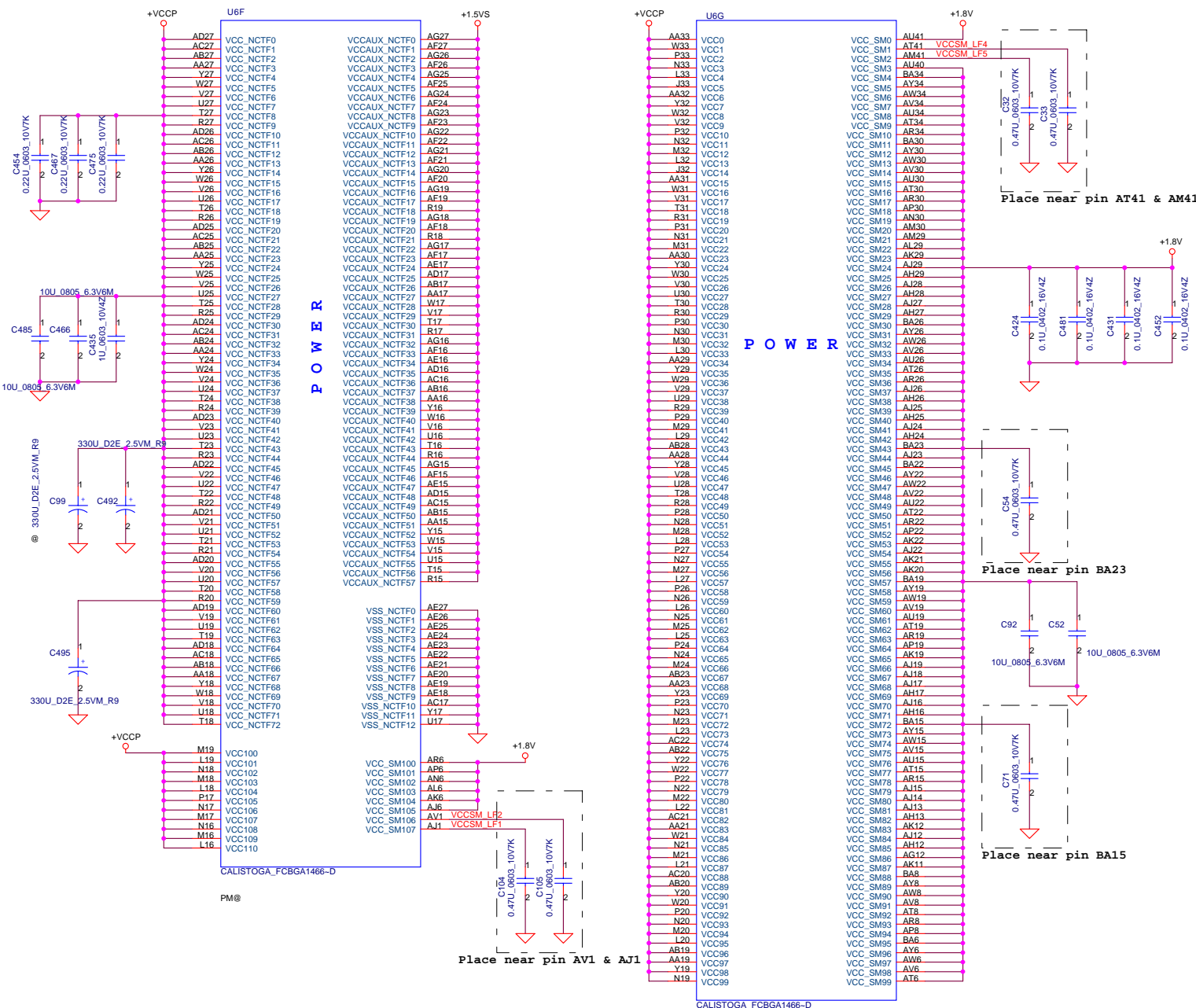
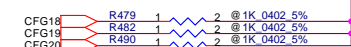
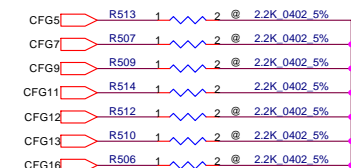
EXP_TXN0	F36	C400	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N0
EXP_TXN1	G40	C36	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N1
EXP_TXN2	H36	C384	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N2
EXP_TXN3	J40	C25	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N3
EXP_TXN4	L36	C398	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N4
EXP_TXN5	M40	C38	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N5
EXP_TXN6	N36	C382	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N6
EXP_TXN7	P40	C27	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N7
EXP_TXN8	R36	C386	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N8
EXP_TXN9	T40	C40	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N9
EXP_TXN10	V36	C380	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N10
EXP_TXN11	W40	C29	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N11
EXP_TXN12	Y36	C384	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N12
EXP_TXN13	AB40	C42	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N13
EXP_TXN14	AC36	C387	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N14
EXP_TXN15	AC40	C31	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N15
EXP_TXP0	D36	C401	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P0
EXP_TXP1	F40	C35	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P1
EXP_TXP2	G36	C385	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P2
EXP_TXP3	H40	C24	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P3
EXP_TXP4	J36	C399	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P4
EXP_TXP5	L40	C37	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P5
EXP_TXP6	M36	C383	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P6
EXP_TXP7	N40	C26	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P7
EXP_TXP8	P36	C387	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P8
EXP_TXP9	R40	C39	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P9
EXP_TXP10	T36	C381	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P10
EXP_TXP11	V40	C28	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P11
EXP_TXP12	W36	C385	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P12
EXP_TXP13	Y40	C41	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P13
EXP_TXP14	AA36	C388	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P14
EXP_TXP15	AB40	C30	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P15

Strap Pin Table

CFG[3:17] have internal pull up

CFG[19:18] have internal pull down

CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 *(Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU *(Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default) *
CFG11	0 = Calistoga * (According to Intel Napa Schematic Checklist & CRB Rev1.301 document 2.2Kohm pull-downs resistor required) 1 = Reserved
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation *(Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled *(Default)
CFG18	0 = 1.05V *(Default) 1 = 1.5V
CFG19	0 = Normal Operation *(Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present *(Default) 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. *(Default) 1 = PCIE/SDVO are operating simu.



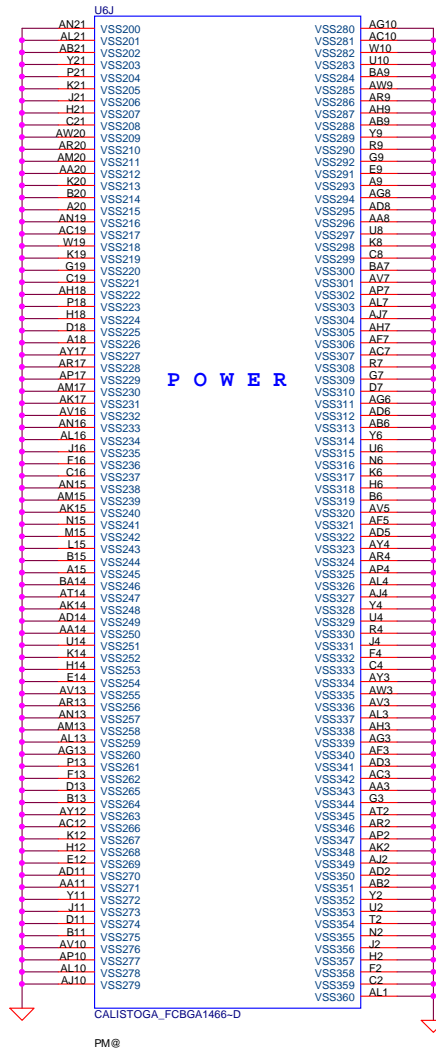
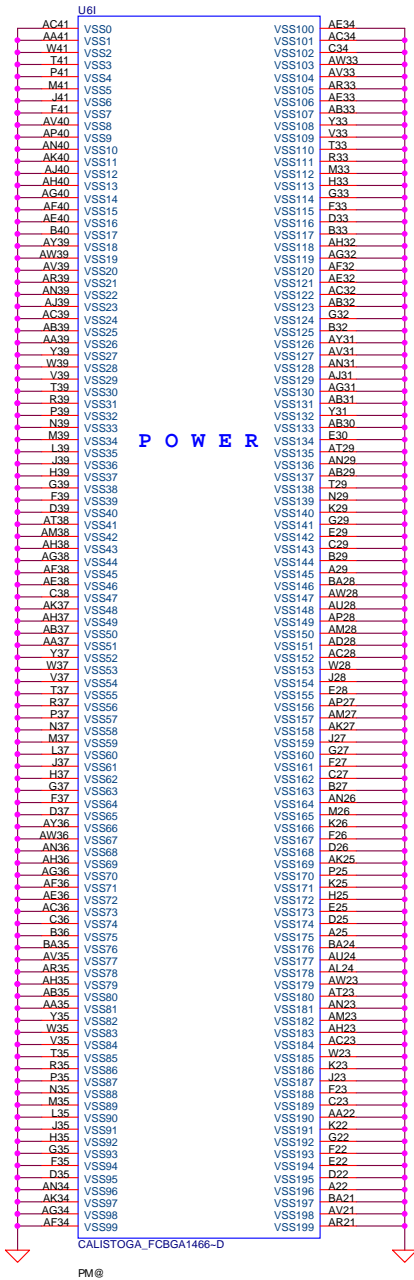
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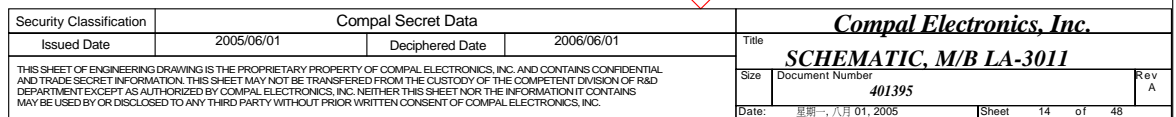
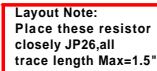
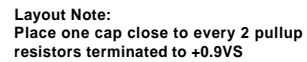
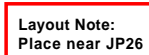
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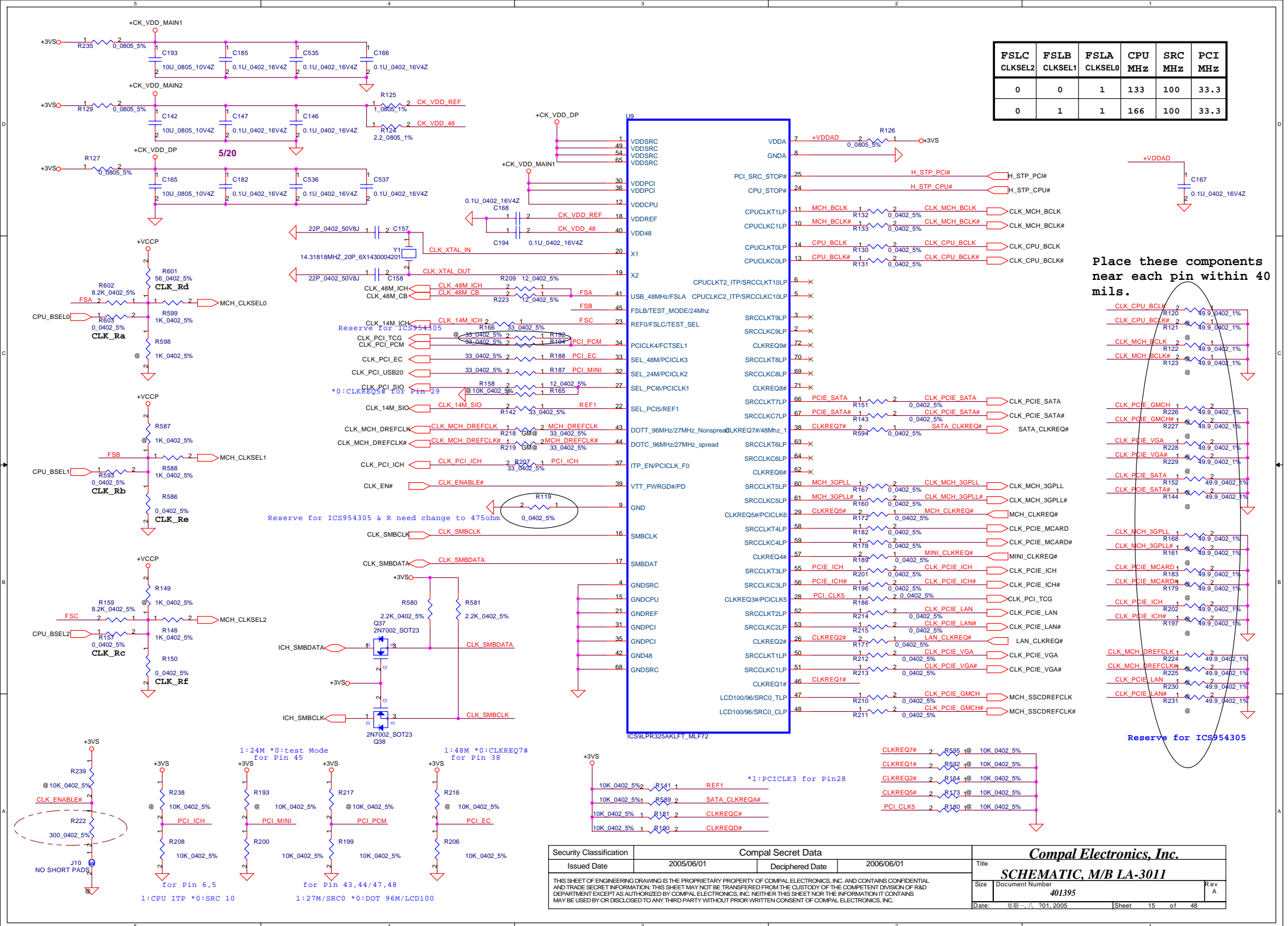
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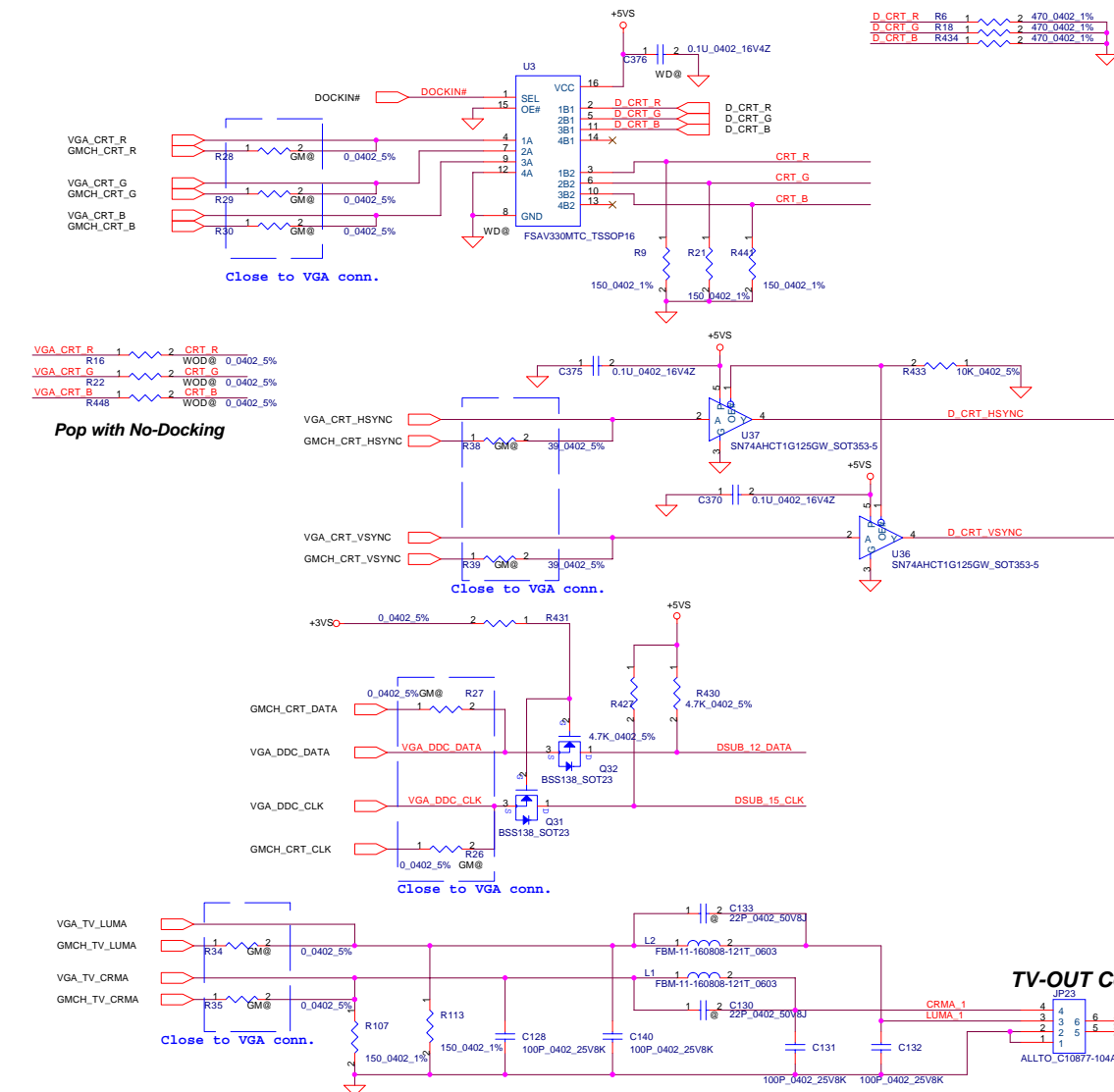
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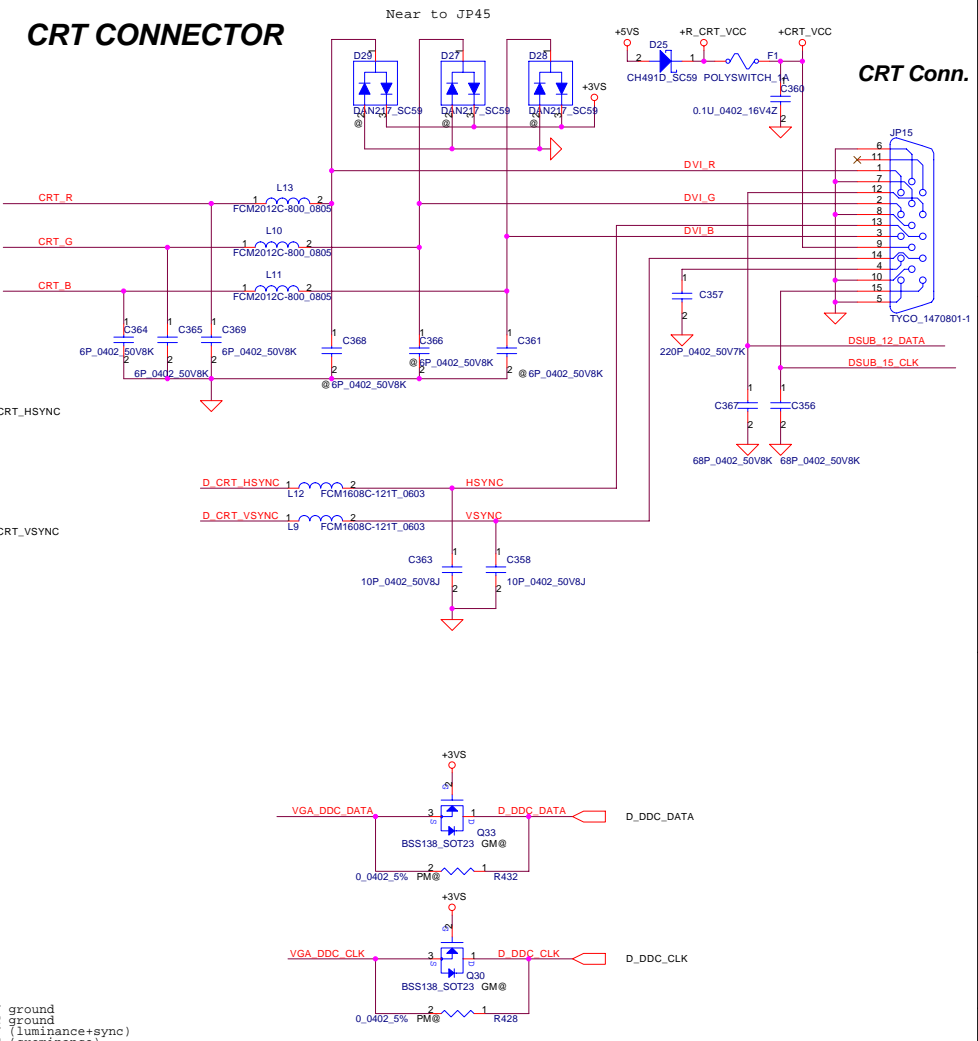


CRT Connector

Match with Docking resistor to 150ohm

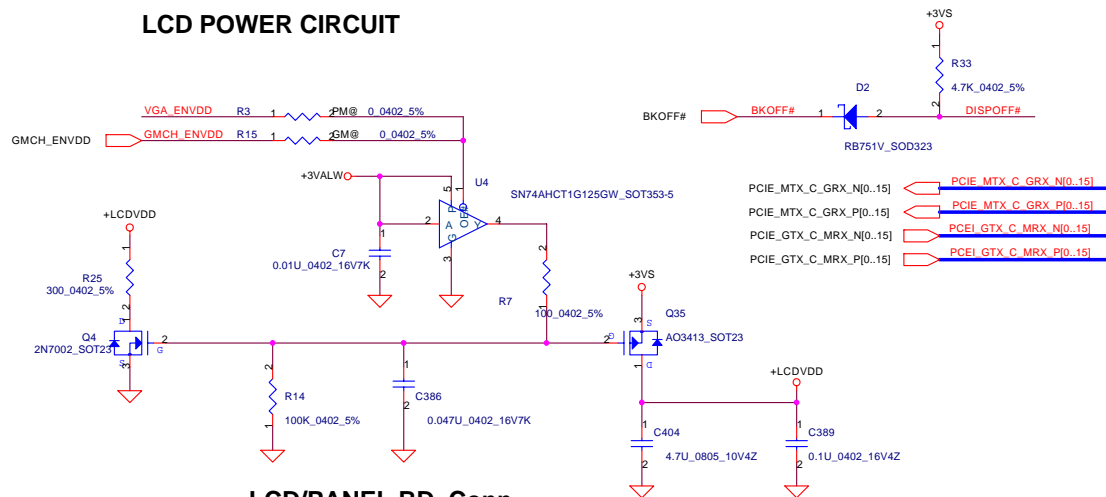


CRT CONNECTOR

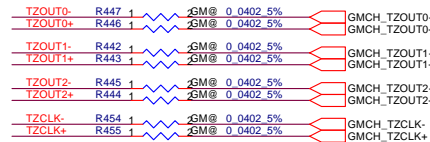
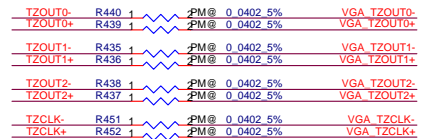
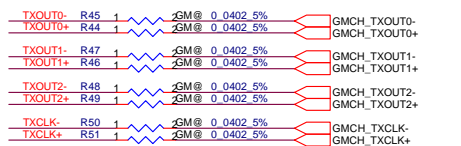
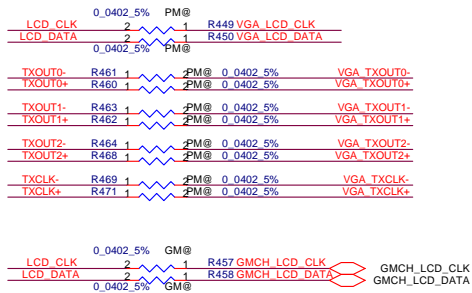
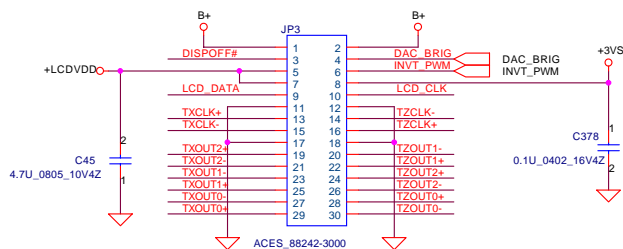


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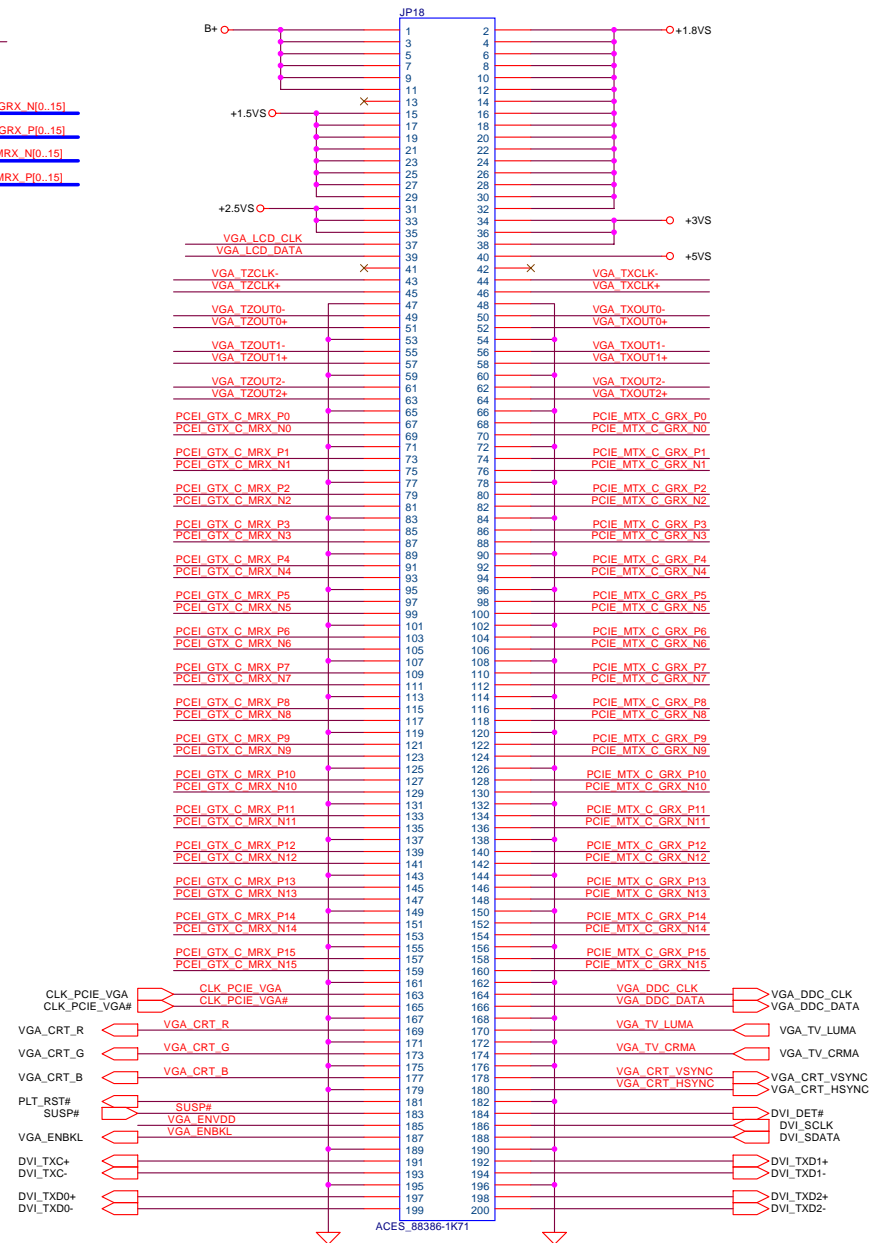
LCD POWER CIRCUIT



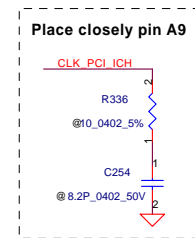
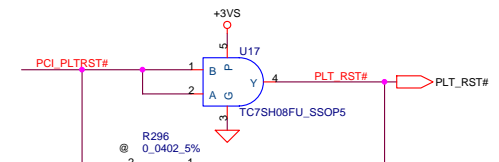
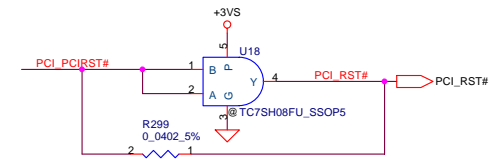
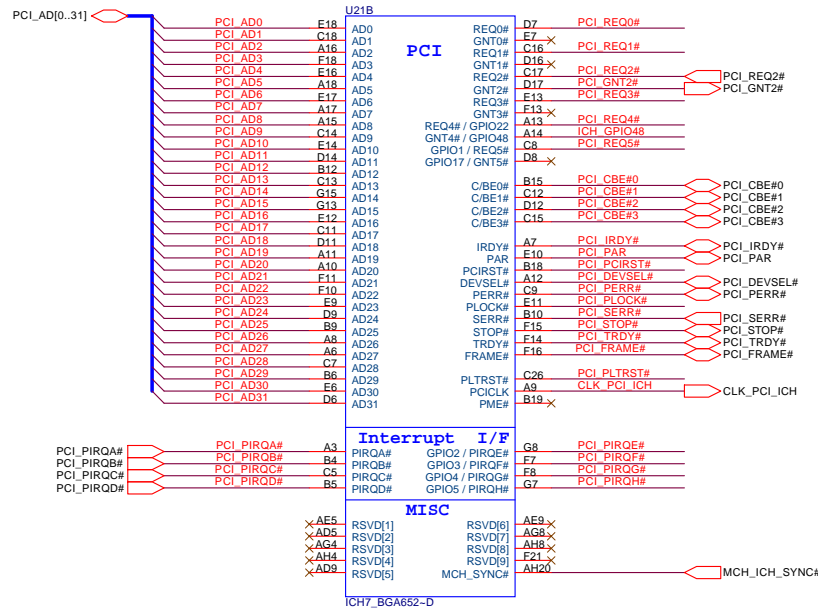
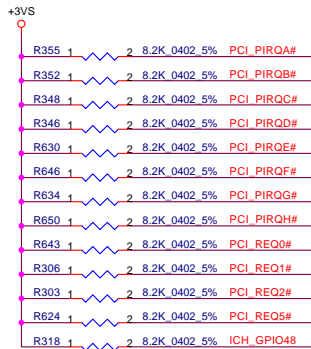
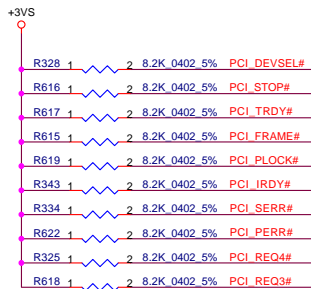
LCD/PANEL BD. Conn.



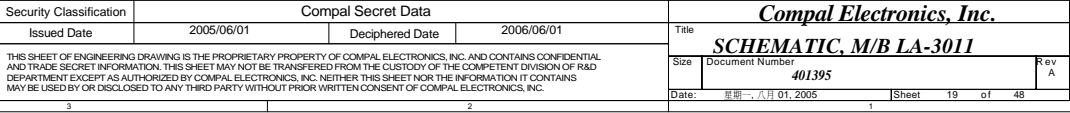
VGA BOARD Conn.

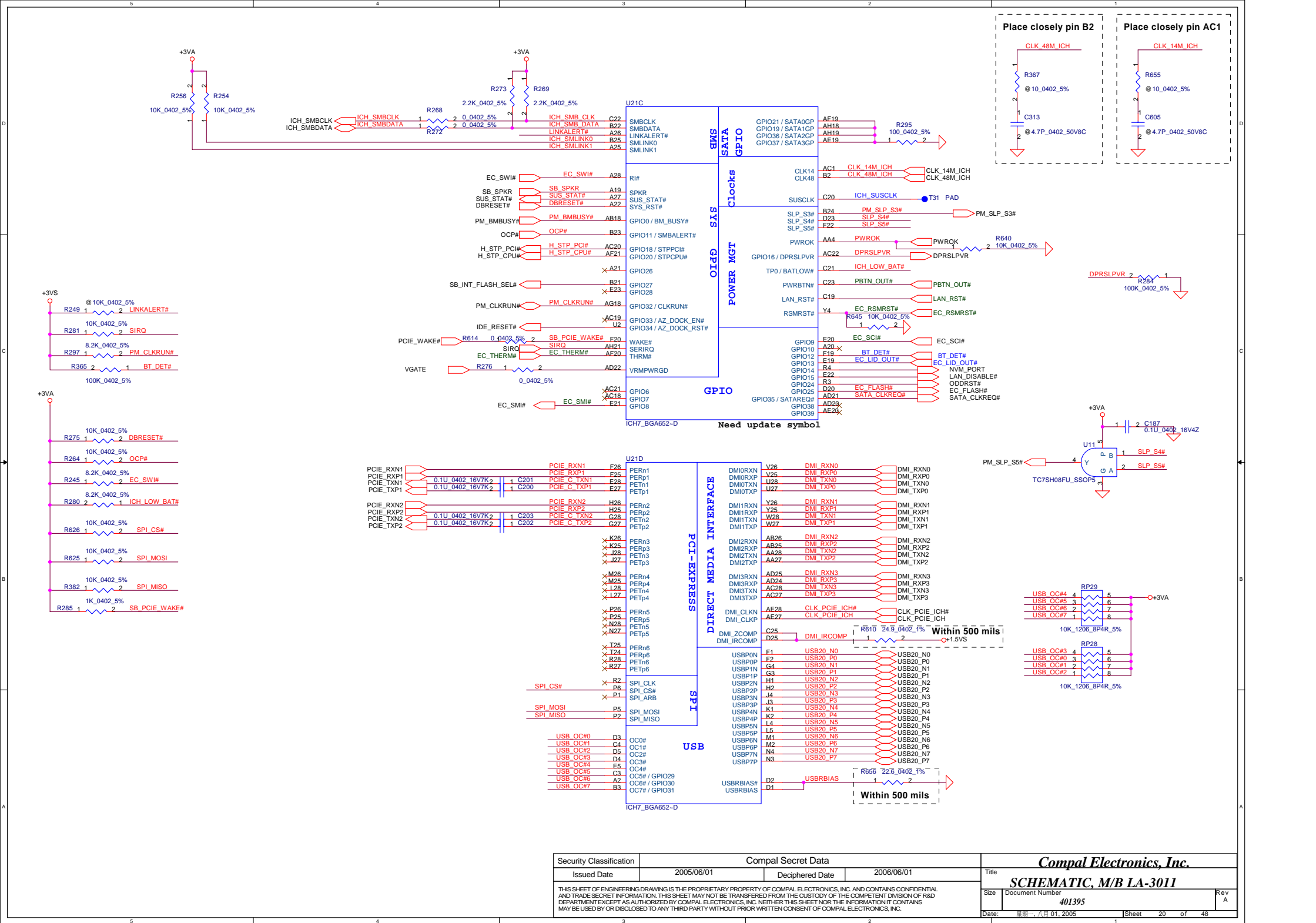


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				Document Number 401395	
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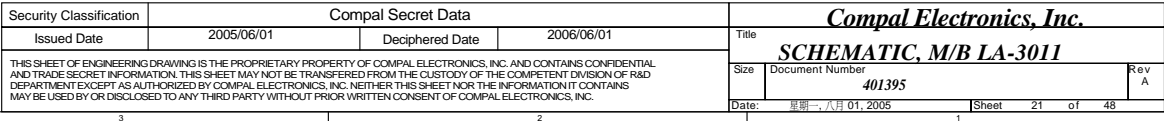


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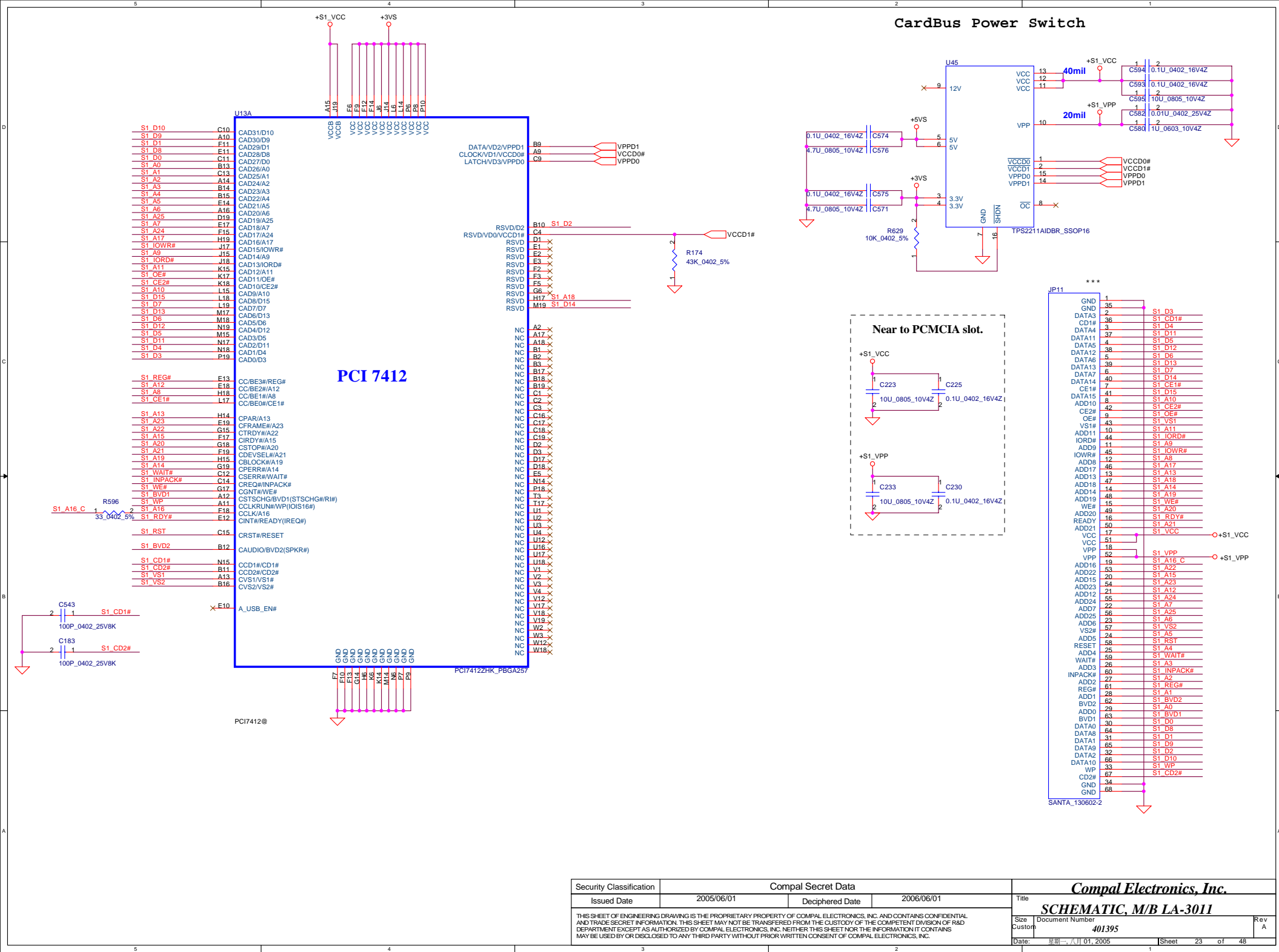




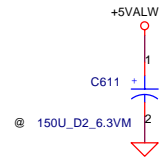
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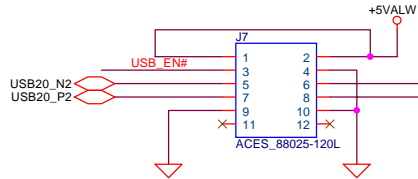
CardBus Power Switch



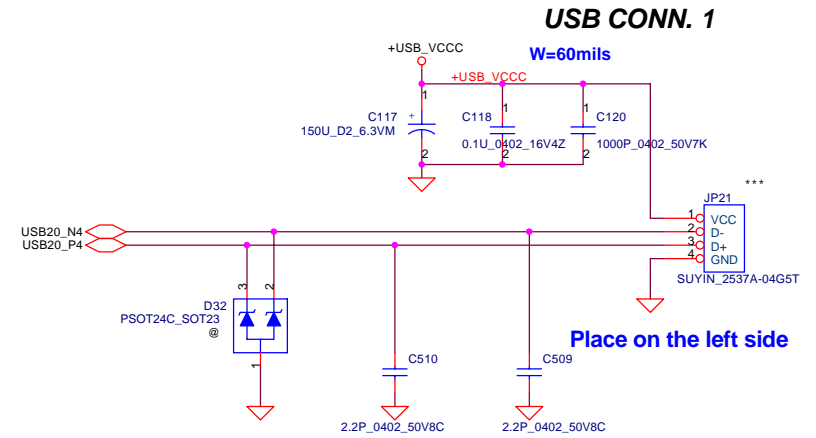
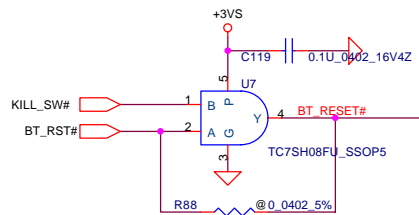
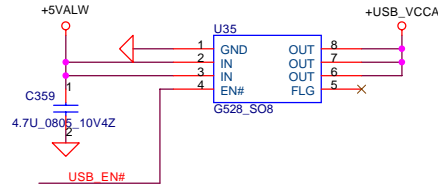
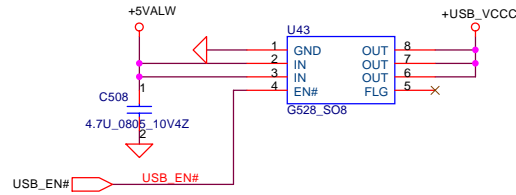
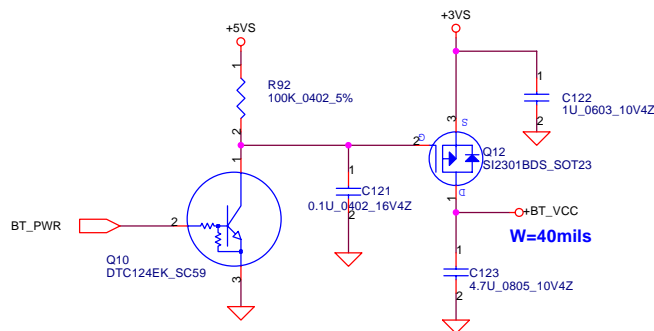
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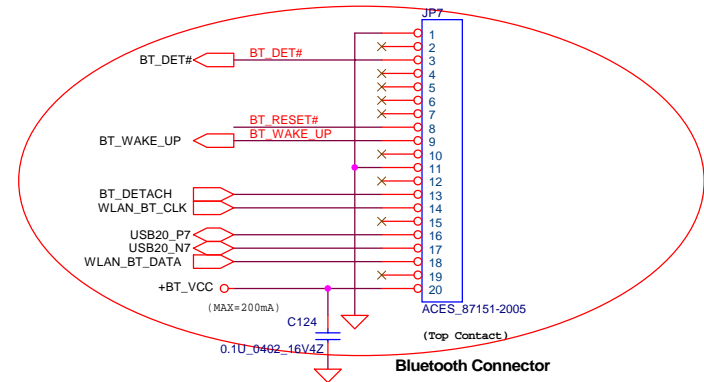
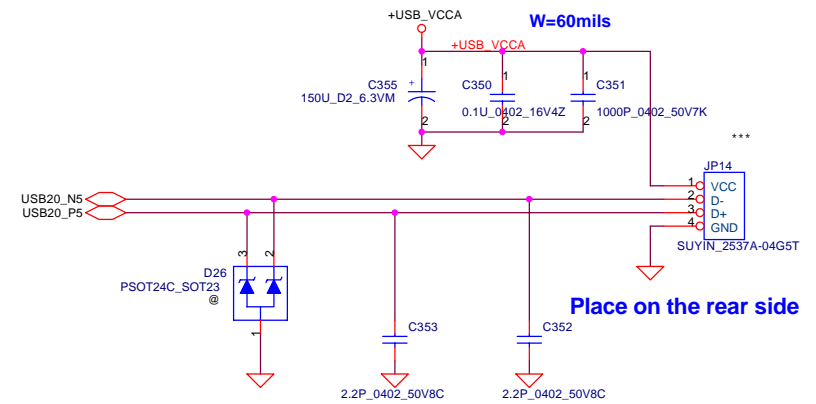
2 port in right side



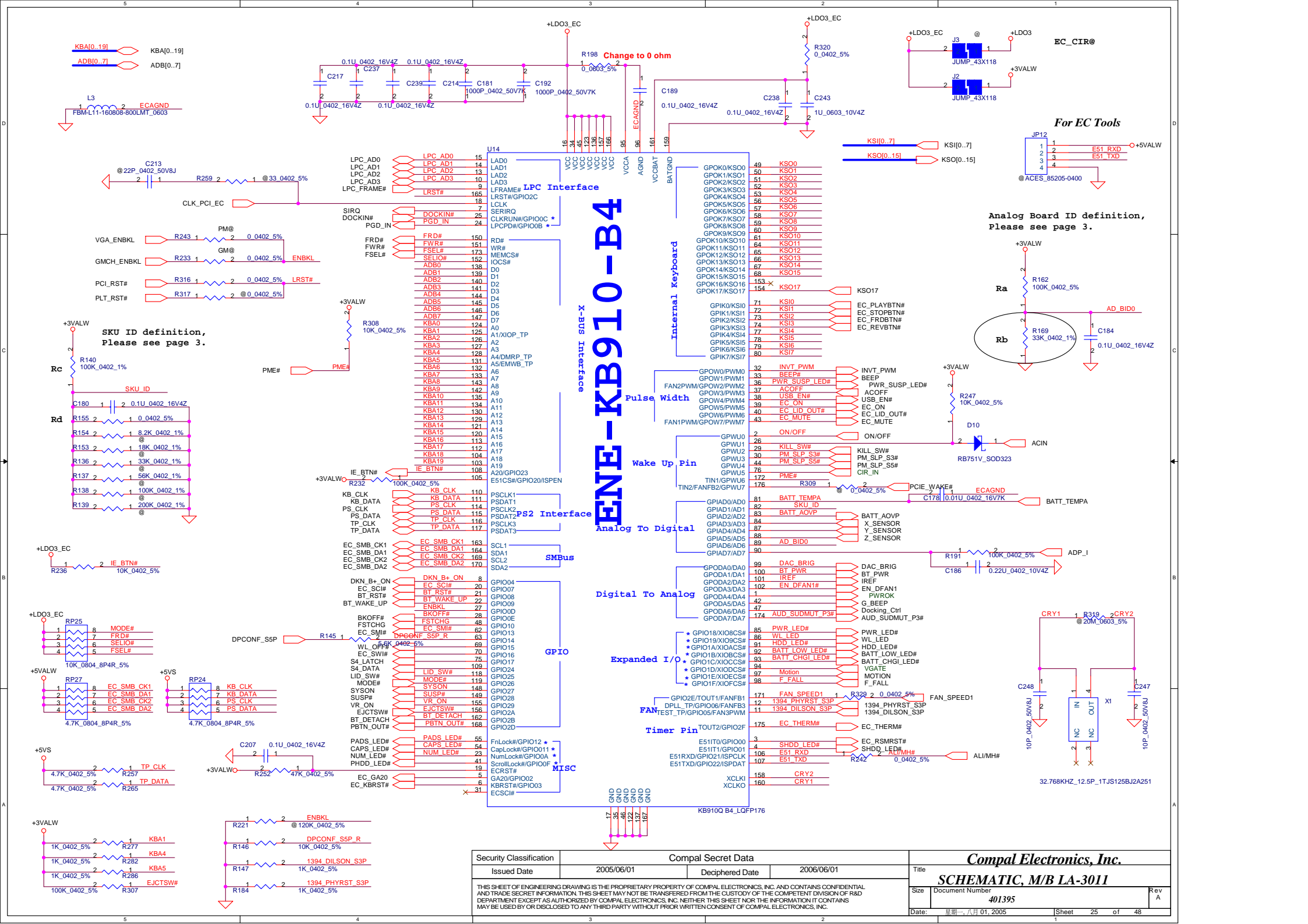
Bluetooth Conn.



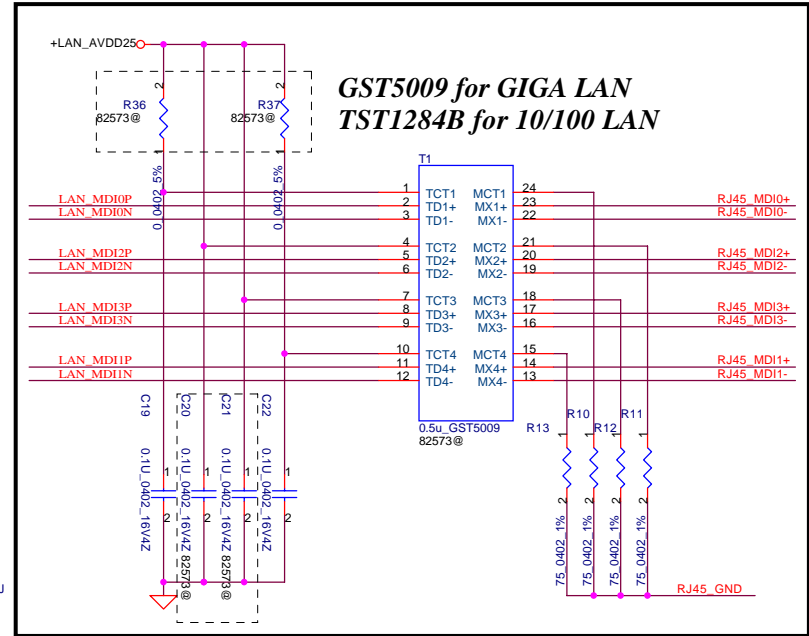
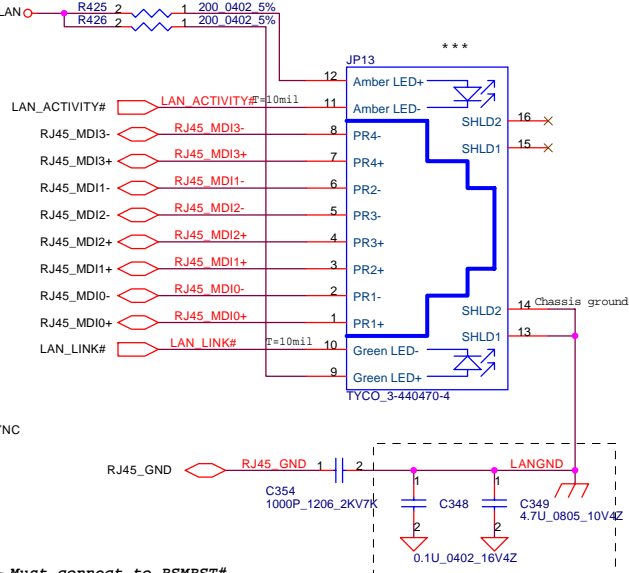
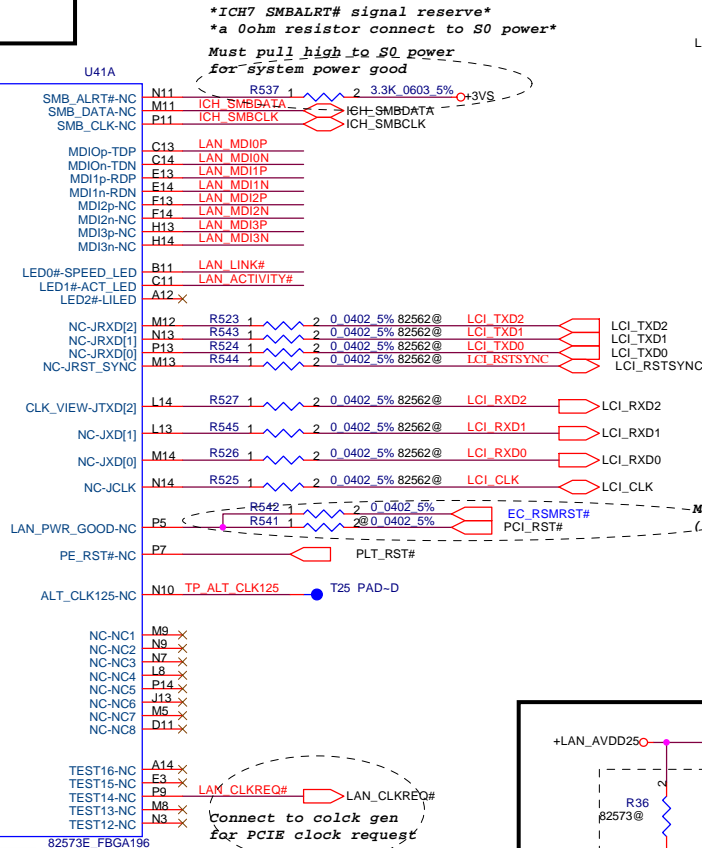
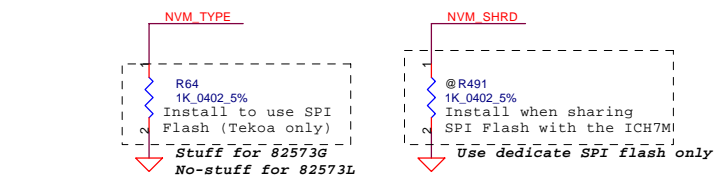
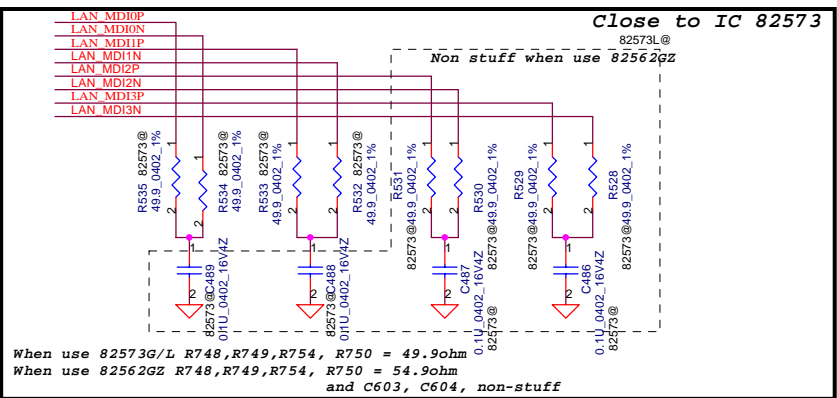
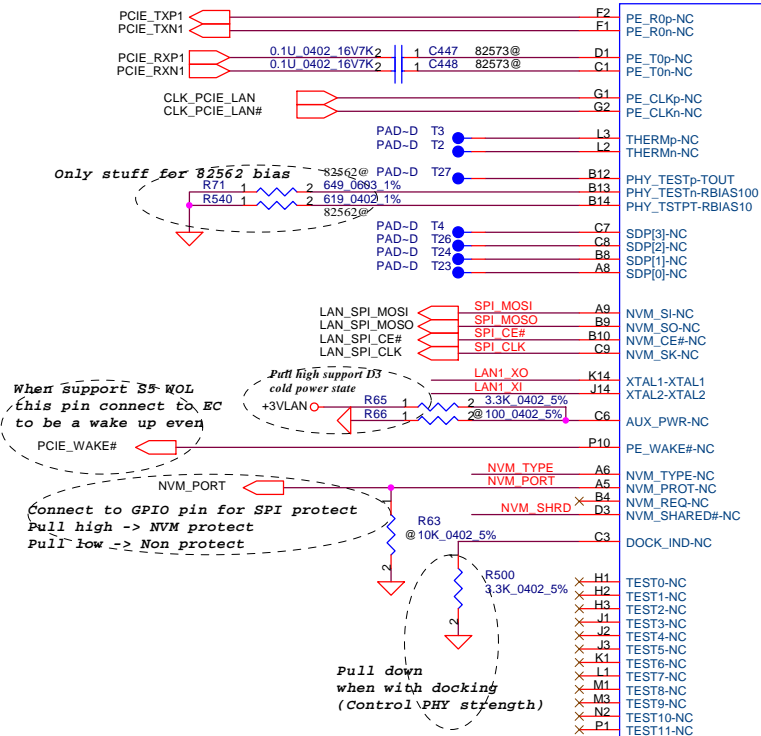
USB CONN.2



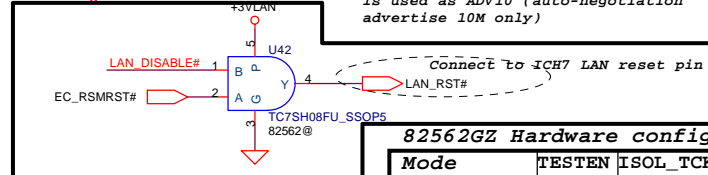
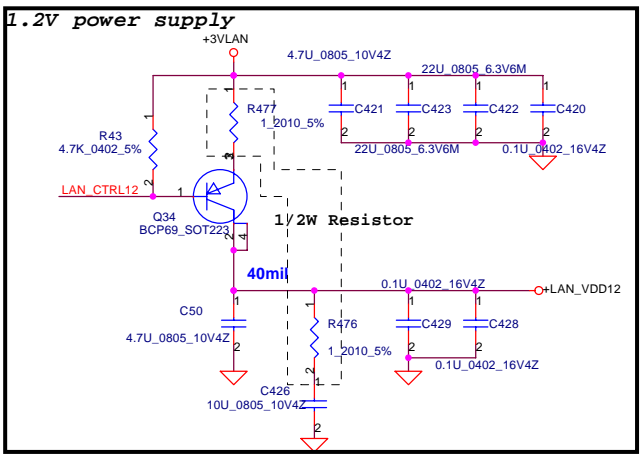
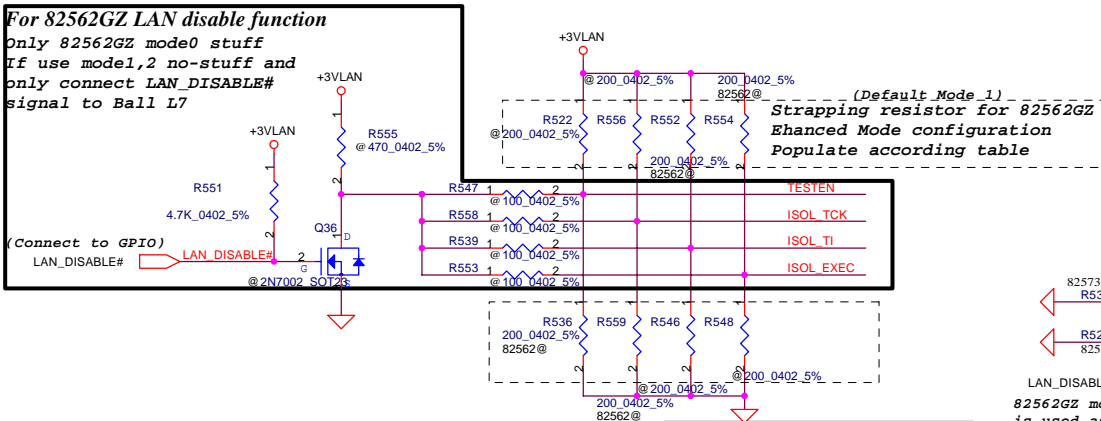
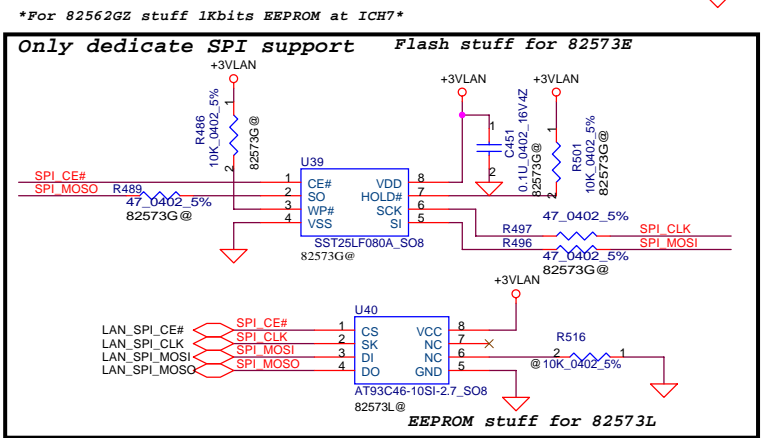
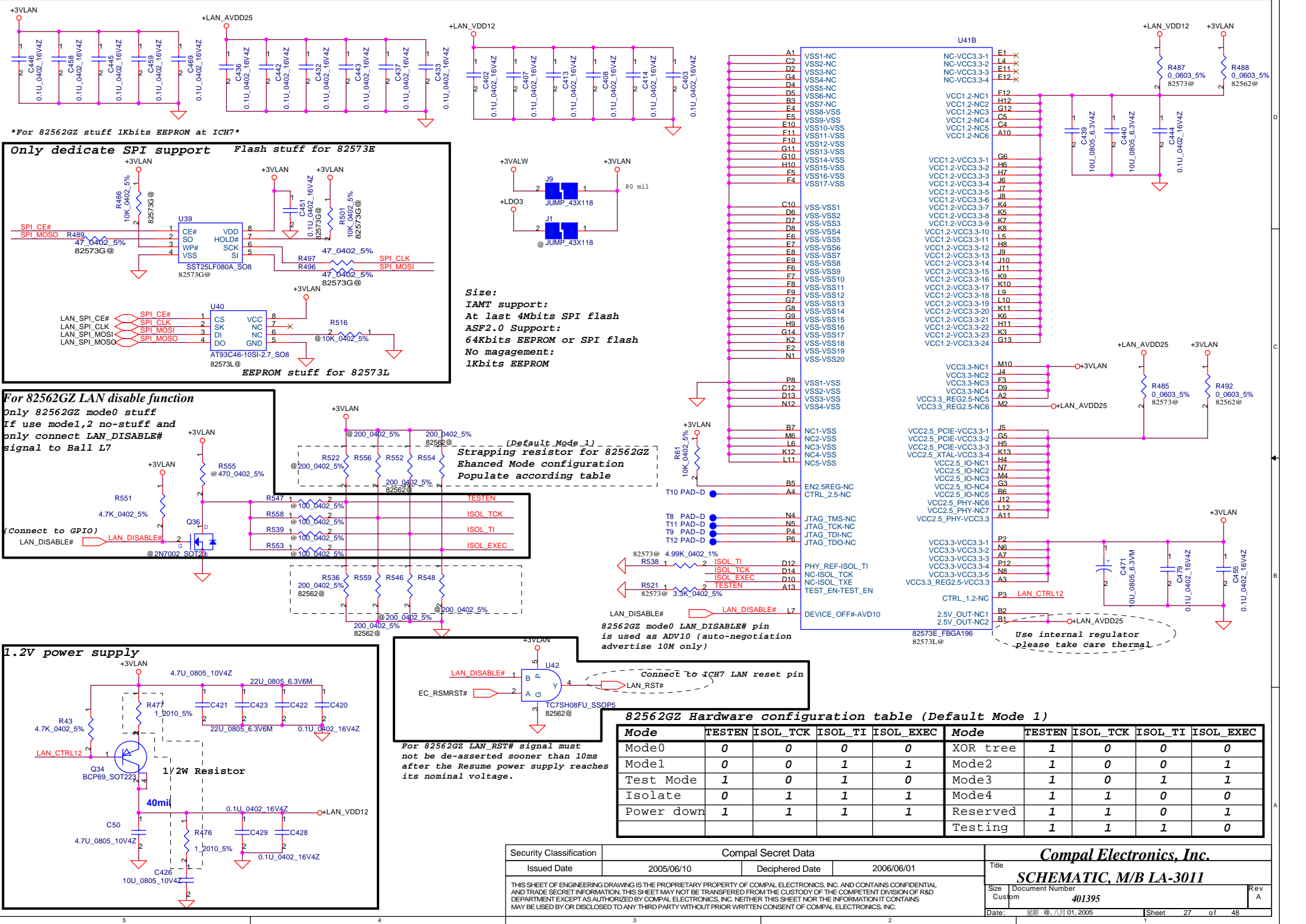
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```
Tekoa 82573E -> IAMT support only (WoL Support)
Vidalia 82573L -> No management
82562GZ -> 10/100 support
```

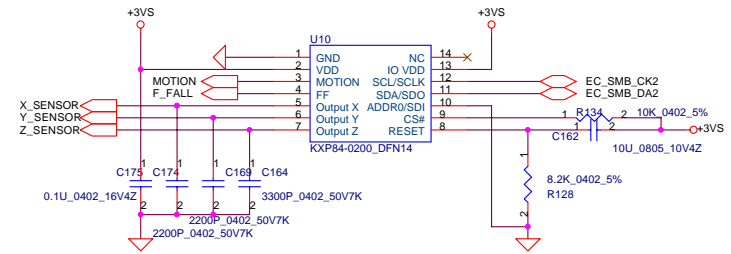
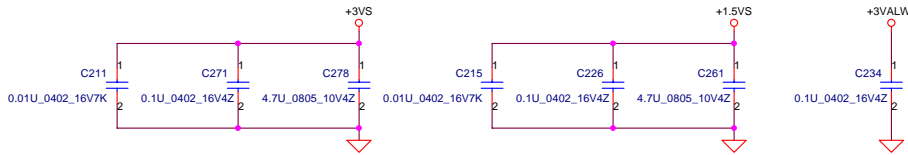


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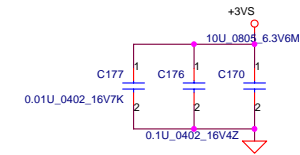
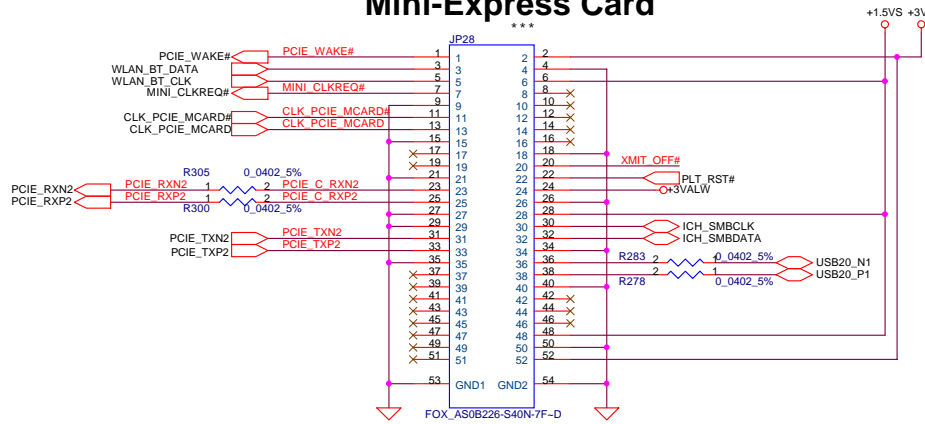


82562GZ Hardware configuration table (Default Mode 1)

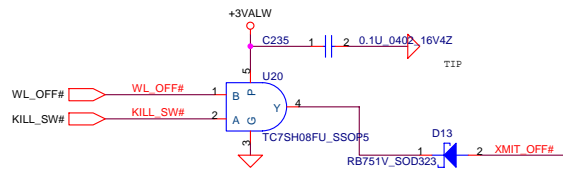
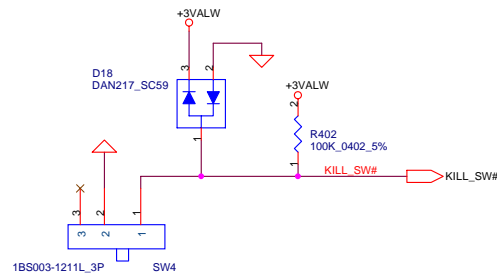
Mode	TESTEN	ISOL_TCK	ISOL_TI	ISOL_EXEC	Mode	TESTEN	ISOL_TCK	ISOL_TI	ISOL_EXEC
Mode0	0	0	0	0	XOR tree	1	0	0	0
Mode1	0	0	1	1	Mode2	1	0	0	1
Test Mode	1	0	1	0	Mode3	1	0	1	1
Isolate	0	1	1	1	Mode4	1	1	0	0
Power down	1	1	1	1	Reserved	1	1	0	1
					Testing	1	1	1	0



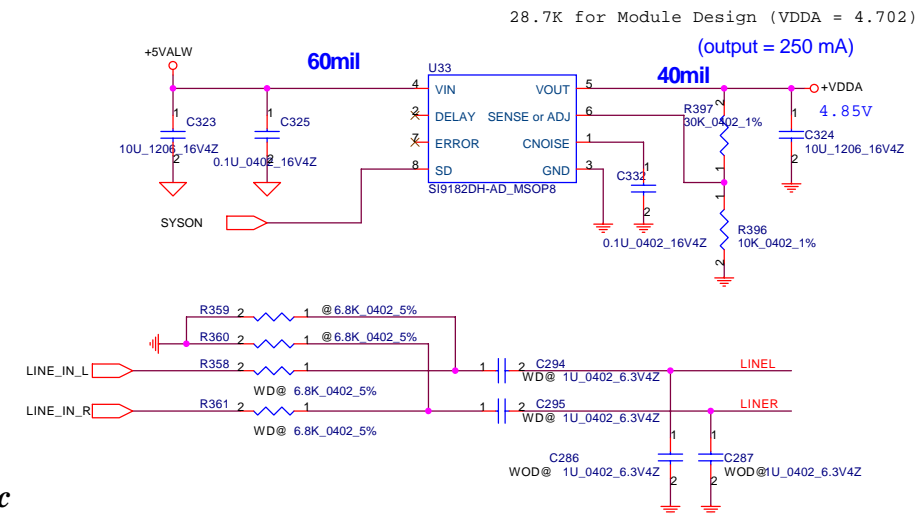
Mini-Express Card



Kill Switch



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The schematic diagram illustrates the ALC861-GR_LQFP48 audio codec, a 48-pin chip, and its connections to various components and signal lines. The chip is shown with its pins and internal connections, and the components are shown with their values and connections.

Pin Connections:

- Pin 1:** +VDDA (40mil trace)
- Pin 2:** +VDDA (40mil trace)
- Pin 3:** +VDDA (40mil trace)
- Pin 4:** +VDDA (40mil trace)
- Pin 5:** +VDDA (40mil trace)
- Pin 6:** +VDDA (40mil trace)
- Pin 7:** +VDDA (40mil trace)
- Pin 8:** +VDDA (40mil trace)
- Pin 9:** +VDDA (40mil trace)
- Pin 10:** +VDDA (40mil trace)
- Pin 11:** +VDDA (40mil trace)
- Pin 12:** +VDDA (40mil trace)
- Pin 13:** +VDDA (40mil trace)
- Pin 14:** +VDDA (40mil trace)
- Pin 15:** +VDDA (40mil trace)
- Pin 16:** +VDDA (40mil trace)
- Pin 17:** +VDDA (40mil trace)
- Pin 18:** +VDDA (40mil trace)
- Pin 19:** +VDDA (40mil trace)
- Pin 20:** +VDDA (40mil trace)
- Pin 21:** +VDDA (40mil trace)
- Pin 22:** +VDDA (40mil trace)
- Pin 23:** +VDDA (40mil trace)
- Pin 24:** +VDDA (40mil trace)
- Pin 25:** +VDDA (40mil trace)
- Pin 26:** +VDDA (40mil trace)
- Pin 27:** +VDDA (40mil trace)
- Pin 28:** +VDDA (40mil trace)
- Pin 29:** +VDDA (40mil trace)
- Pin 30:** +VDDA (40mil trace)
- Pin 31:** +VDDA (40mil trace)
- Pin 32:** +VDDA (40mil trace)
- Pin 33:** +VDDA (40mil trace)
- Pin 34:** +VDDA (40mil trace)
- Pin 35:** +VDDA (40mil trace)
- Pin 36:** +VDDA (40mil trace)
- Pin 37:** +VDDA (40mil trace)
- Pin 38:** +VDDA (40mil trace)
- Pin 39:** +VDDA (40mil trace)
- Pin 40:** +VDDA (40mil trace)
- Pin 41:** +VDDA (40mil trace)
- Pin 42:** +VDDA (40mil trace)
- Pin 43:** +VDDA (40mil trace)
- Pin 44:** +VDDA (40mil trace)
- Pin 45:** +VDDA (40mil trace)
- Pin 46:** +VDDA (40mil trace)
- Pin 47:** +VDDA (40mil trace)
- Pin 48:** +VDDA (40mil trace)

Component Values:

- Resistors:** R392, R388, R390, R394, R393, R398, R389, R658, R400, R366, R395, R399.
- Capacitors:** C331, C305, C329, C322, C328, C303, C327, C617, C326, C319.
- Inductors:** L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, L16, L17, L18, L19, L20, L21, L22, L23, L24, L25, L26, L27, L28, L29, L30, L31, L32, L33, L34, L35, L36, L37, L38, L39, L40, L41, L42, L43, L44, L45, L46, L47, L48.

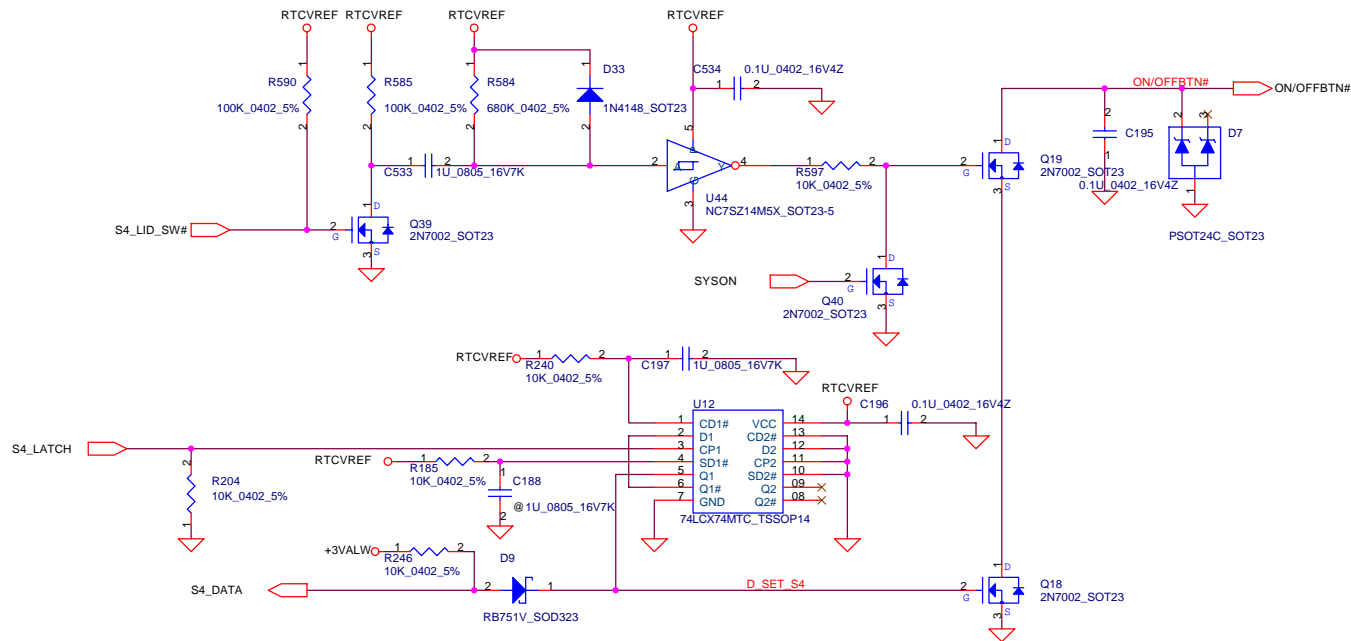
Signal Lines:

- LINE2_L, LINE2_R, MIC2_L, MIC2_R, LINE1_L, LINE1_R, CD_L, CD_R, CD_L_R, CD_R_R, CD_AGND_R, MIC1_L, MIC1_R, MONO_IN, ICH_RST_AUDIO#, ICH_SYNC_AUDIO, ICH_SDOUT_AUDIO, SPDIFO, DVSS1, DVSS2, ALC861-GR_LQFP48.**

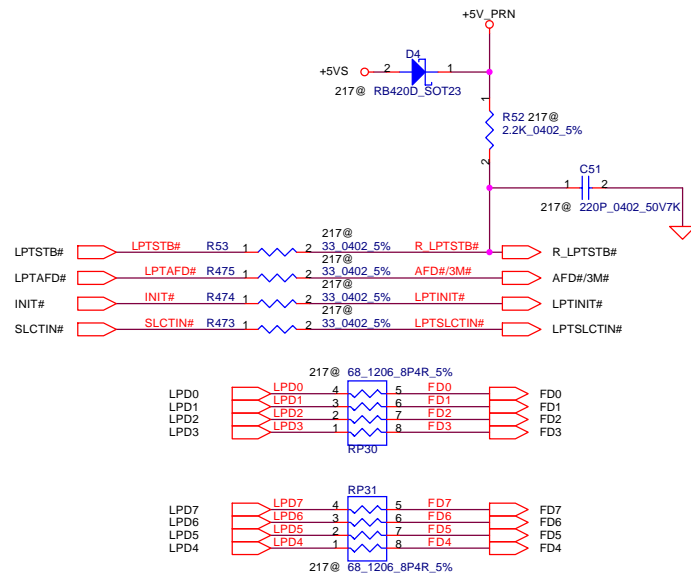
Other Components:

- U48:** ALC861-GR_LQFP48
- U49:** ALC861-GR_LQFP48
- U50:** ALC861-GR_LQFP48
- U51:** ALC861-GR_LQFP48
- U52:** ALC861-GR_LQFP48
- U53:** ALC861-GR_LQFP48
- U54:** ALC861-GR_LQFP48
- U55:** ALC861-GR_LQFP48
- U56:** ALC861-GR_LQFP48
- U57:** ALC861-GR_LQFP48
- U58:** ALC861-GR_LQFP48
- U59:** ALC861-GR_LQFP48
- U60:** ALC861-GR_LQFP48
- U61:** ALC861-GR_LQFP48
- U62:** ALC861-GR_LQFP48
- U63:** ALC861-GR_LQFP48
- U64:** ALC861-GR_LQFP48
- U65:** ALC861-GR_LQFP48
- U66:** ALC861-GR_LQFP48
- U67:** ALC861-GR_LQFP48
- U68:** ALC861-GR_LQFP48
- U69:** ALC861-GR_LQFP48
- U70:** ALC861-GR_LQFP48
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- U72:** ALC861-GR_LQFP48
- U73:** ALC861-GR_LQFP48
- U74:** ALC861-GR_LQFP48
- U75:** ALC861-GR_LQFP48
- U76:** ALC861-GR_LQFP48
- U77:** ALC861-GR_LQFP48
- U78:** ALC861-GR_LQFP48
- U79:** ALC861-GR_LQFP48
- U80:** ALC861-GR_LQFP48
- U81:** ALC861-GR_LQFP48
- U82:** ALC861-GR_LQFP48
- U83:** ALC861-GR_LQFP48
- U84:** ALC861-GR_LQFP48
- U85:** ALC861-GR_LQFP48
- U86:** ALC861-GR_LQFP48
- U87:** ALC861-GR_LQFP48
- U88:** ALC861-GR_LQFP48
- U89:** ALC861-GR_LQFP48
- U90:** ALC861-GR_LQFP48
- U91:** ALC861-GR_LQFP48
- U92:** ALC861-GR_LQFP48
- U93:** ALC861-GR_LQFP48
- U94:** ALC861-GR_LQFP48
- U95:** ALC861-GR_LQFP48
- U96:** ALC861-GR_LQFP48
- U97:** ALC861-GR_LQFP48
- U98:** ALC861-GR_LQFP48
- U99:** ALC861-GR_LQFP48
- U100:** ALC861-GR_LQFP48

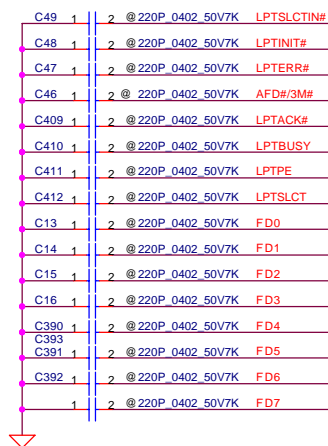
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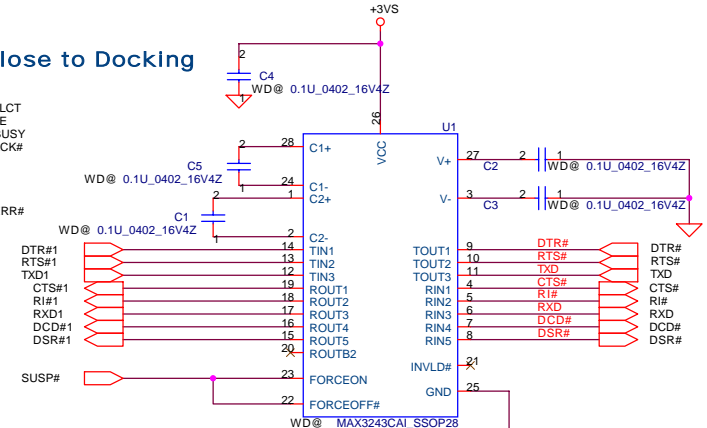
Docking Parallel Port



Close to Docking

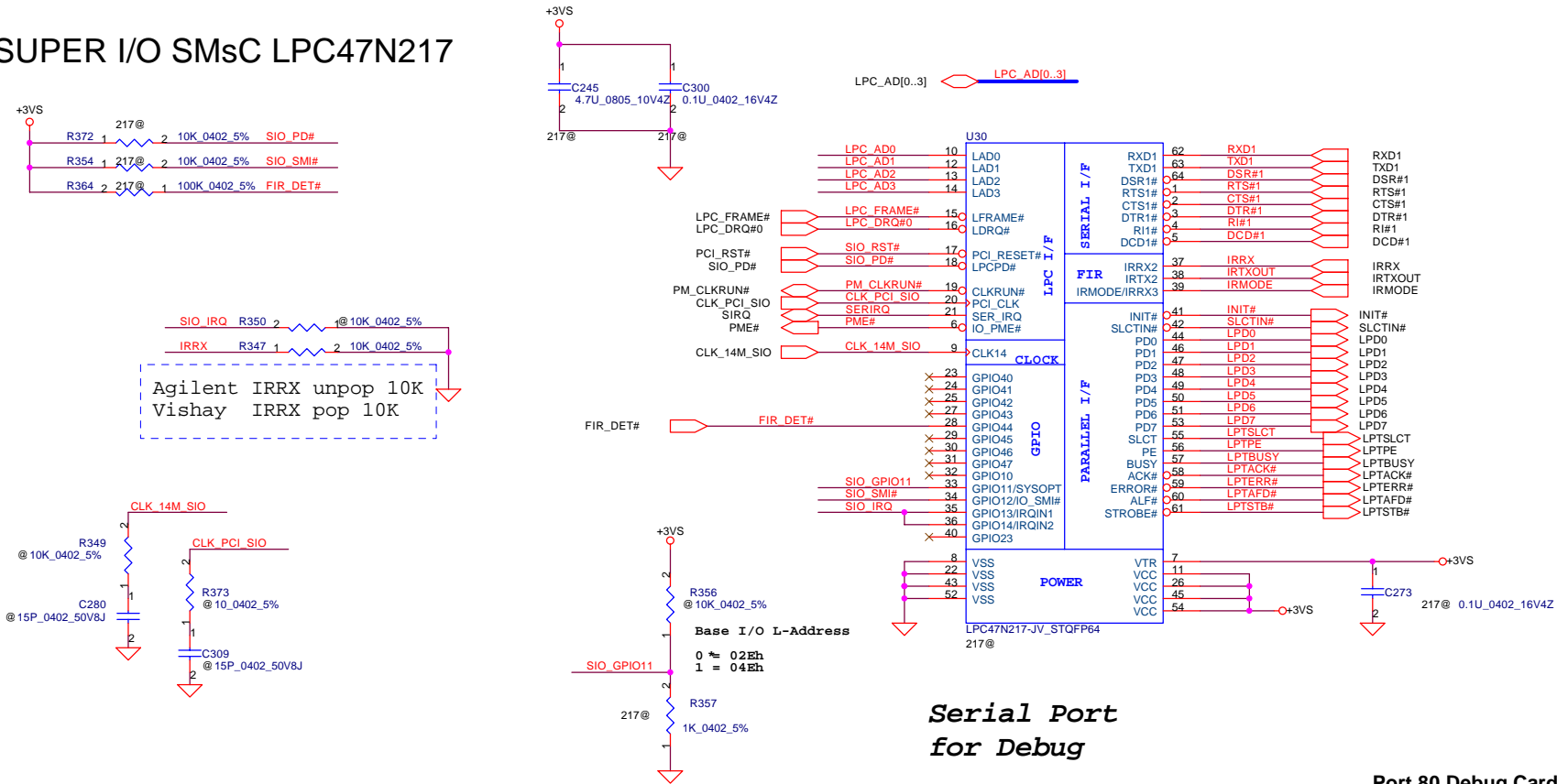


Close to Docking



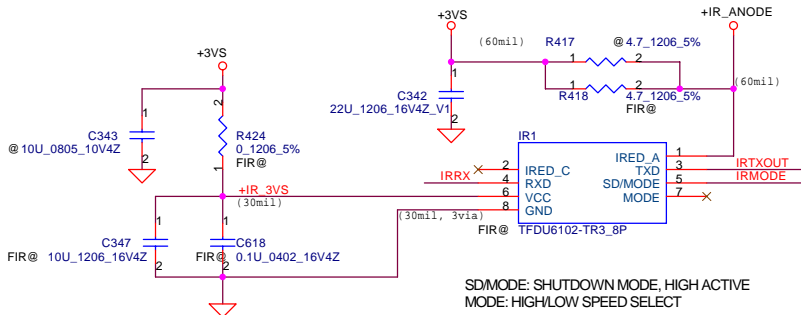
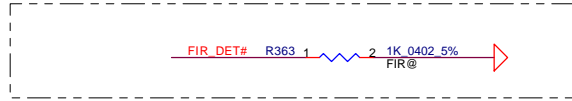
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Issued Date	2005/04/25	Deciphered Date	2006/06/01	Title	
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SUPER I/O SMsC LPC47N217

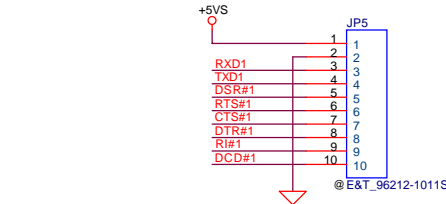


FIR Module

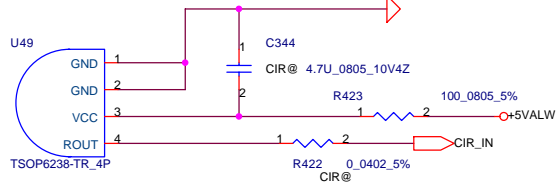
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L: R POP; FIR Enable
H: R De-POP/FIR Disable
```



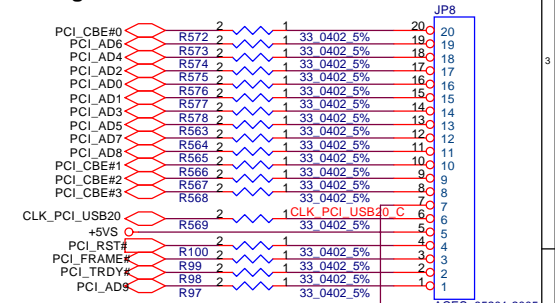
Serial Port for Debug



CIR

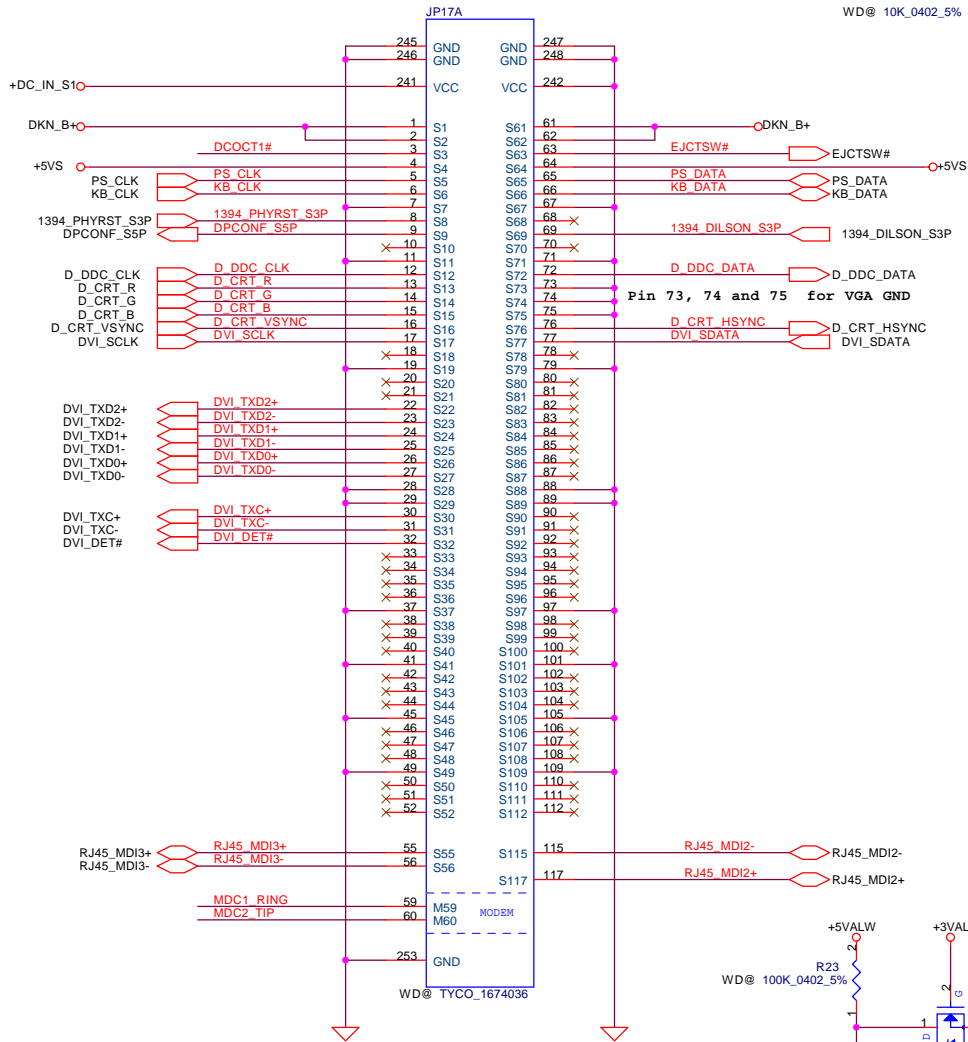


Port 80 Debug Card Connector

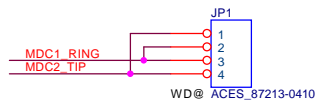


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Docking Conn.



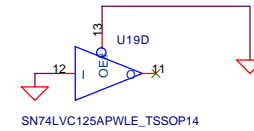
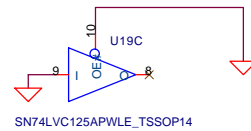
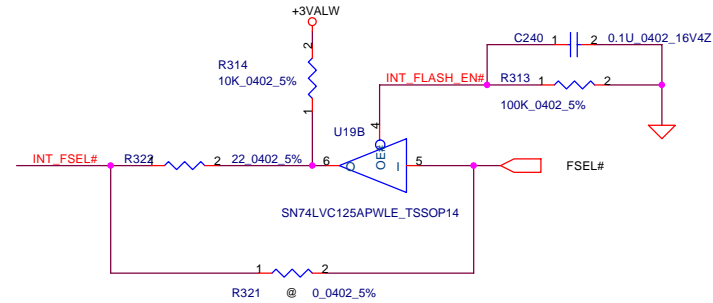
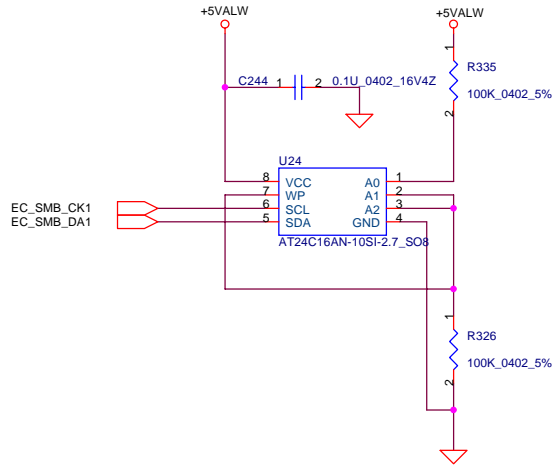
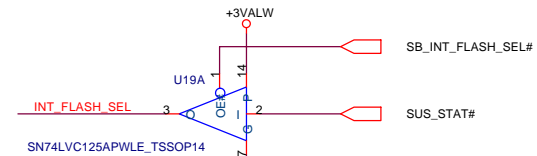
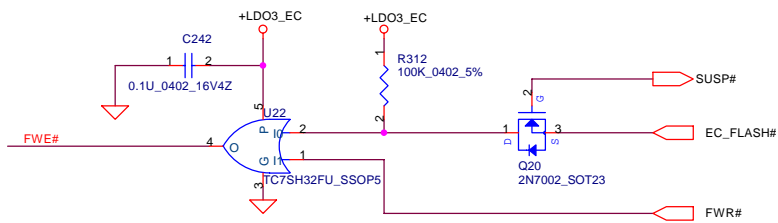
MDC to Docking Conn.



EC_SMB_DA2

WD@ 2N7002_SOT23

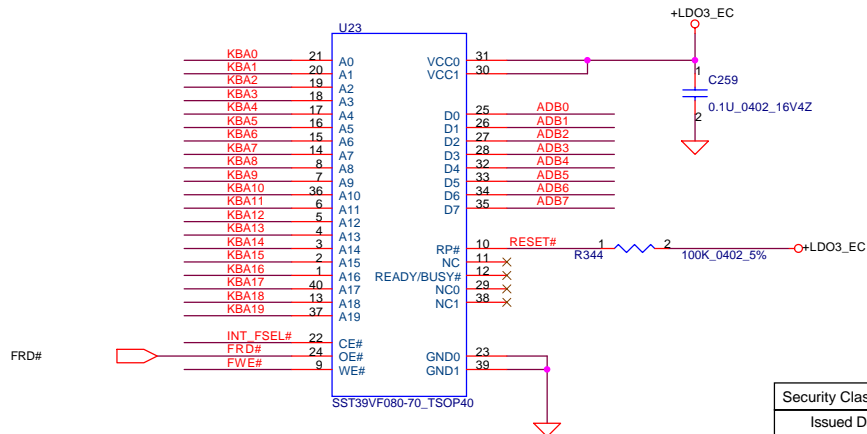
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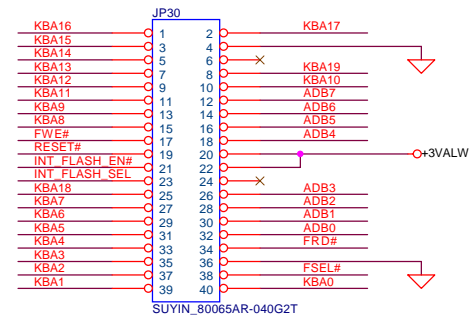
KBA[0..19] KBA[0..19]

ADB[0..7] ADB[0..7]

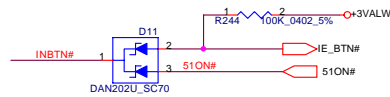
1MB Flash ROM



1MB ROM Socket

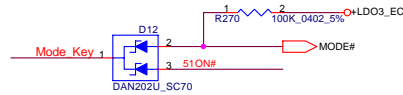


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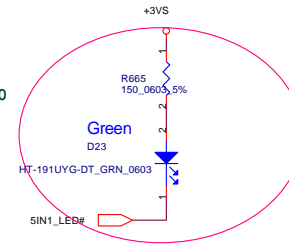


10C/10GC:Green:SC591UYG000
10/10C:Blue:SC5191NB000

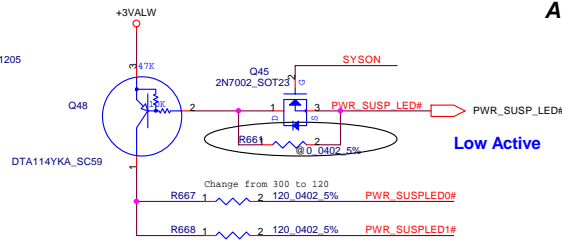
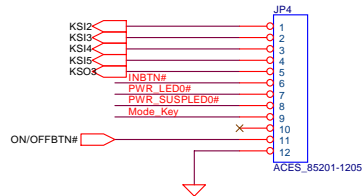
Vivace(SW-DJ) Button



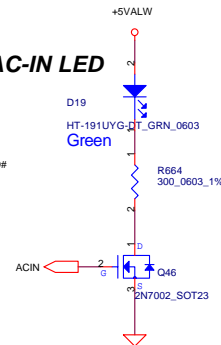
5 In 1 Card LED (Close to Socket)



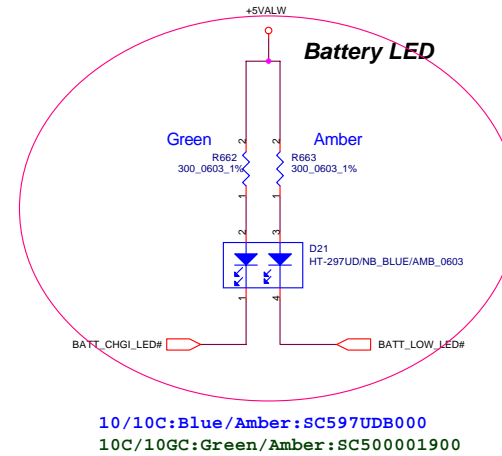
PWR BTN/B



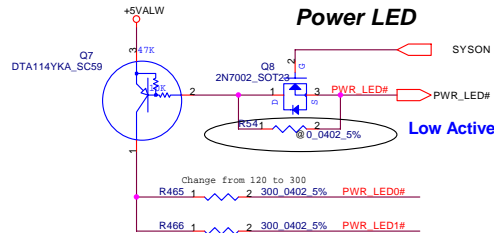
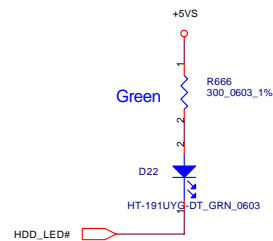
AC-IN LED



Battery LED



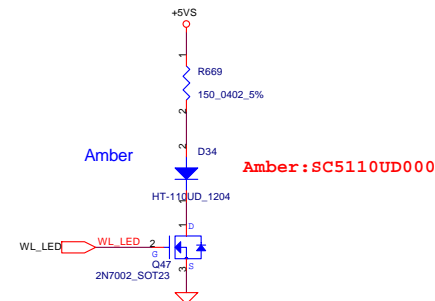
HDD LED



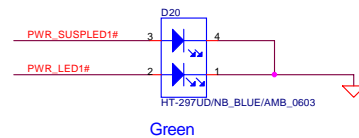
Power LED

Low Active

Wireless Lan LED



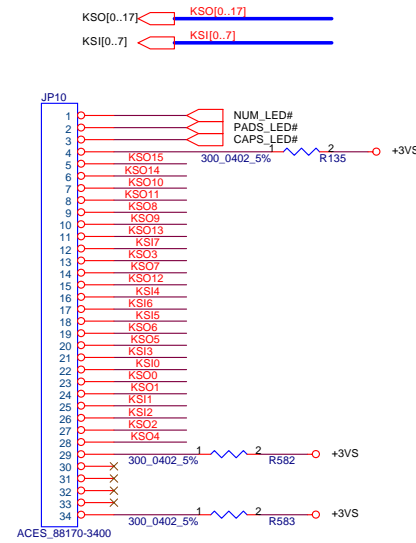
Amber



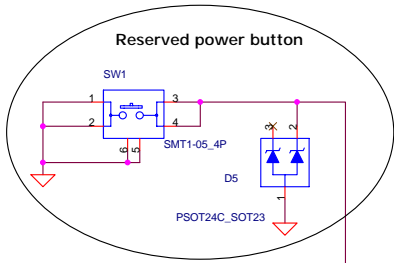
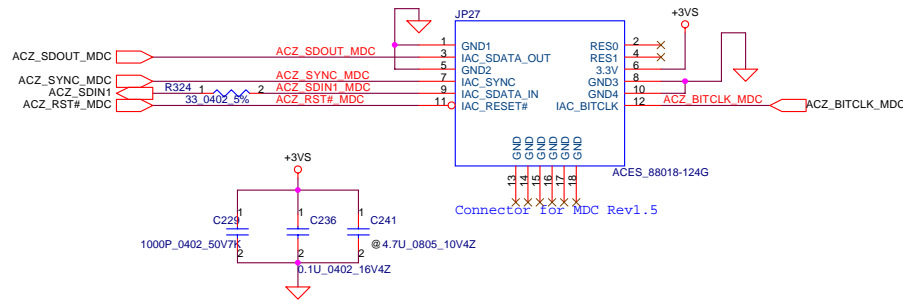
Green

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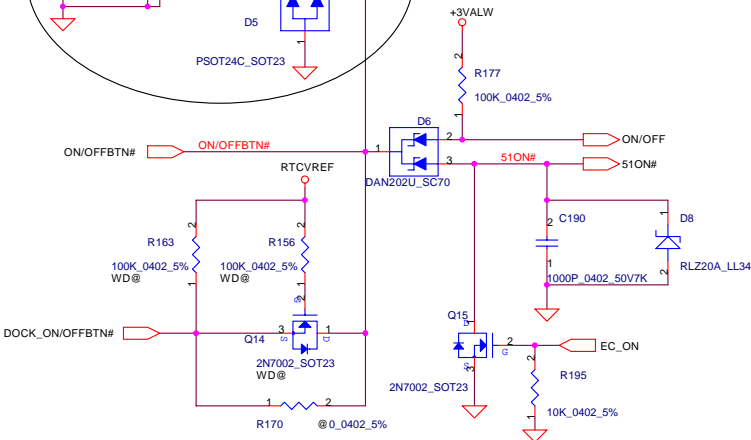
INT_KBD CONN.



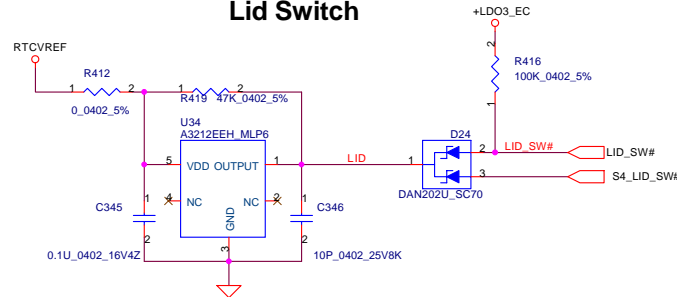
MDC 1.5 Conn.



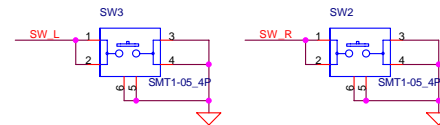
Power Button



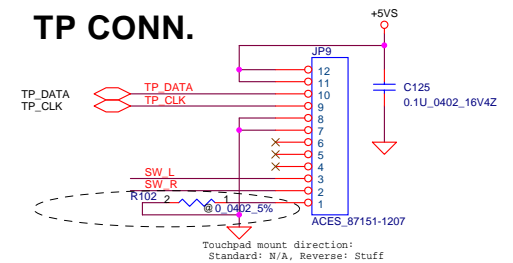
Lid Switch



TP Button



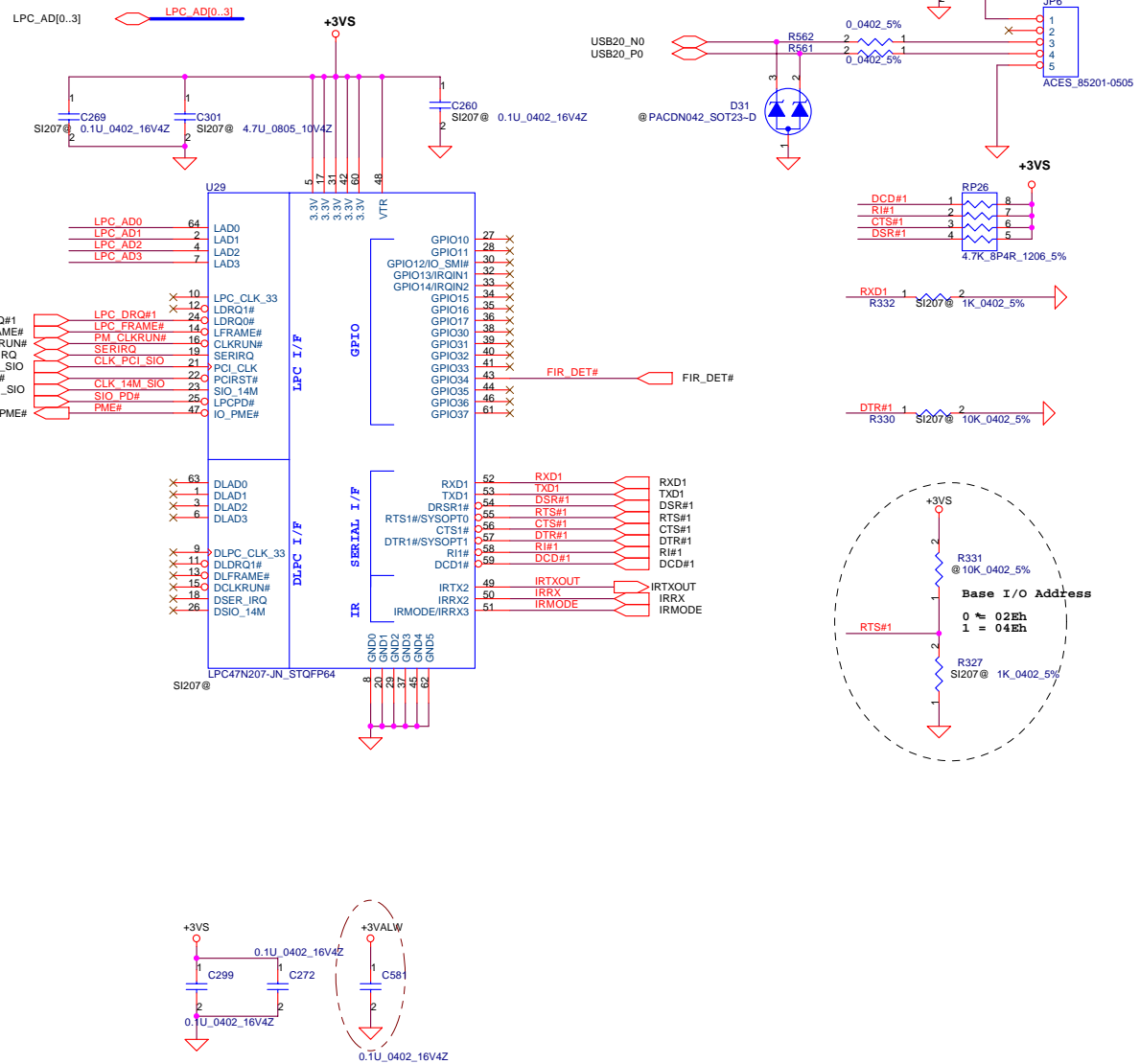
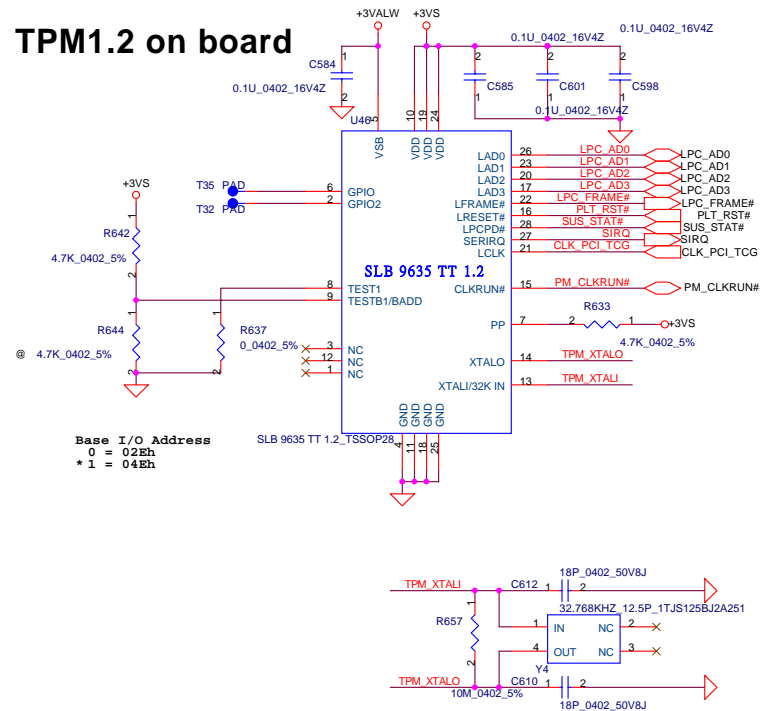
TP CONN.



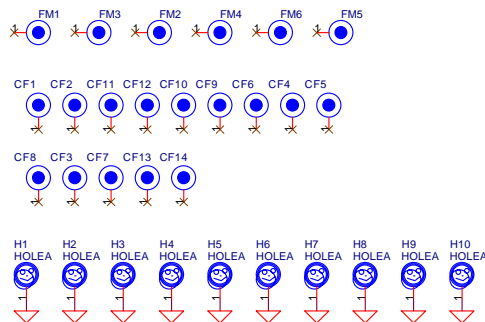
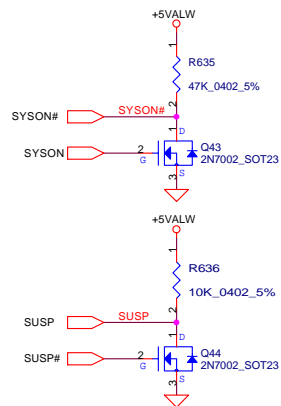
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Finger printer

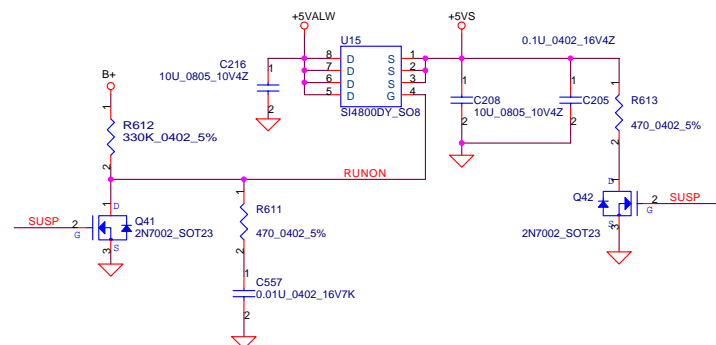
TPM1.2 on board



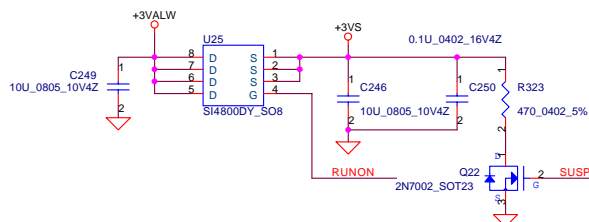
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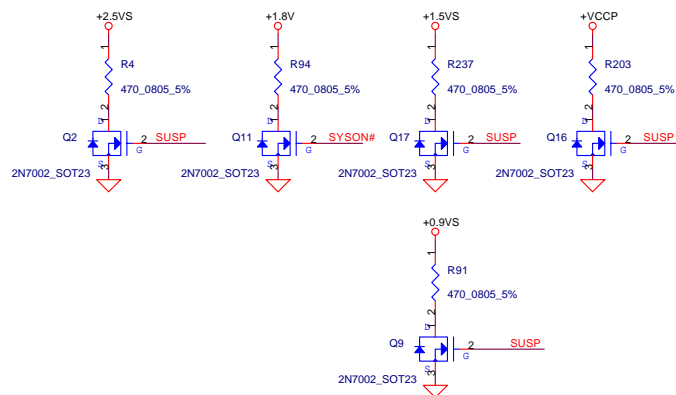
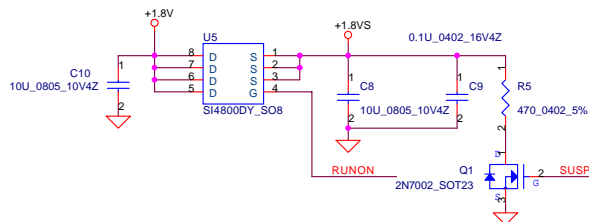
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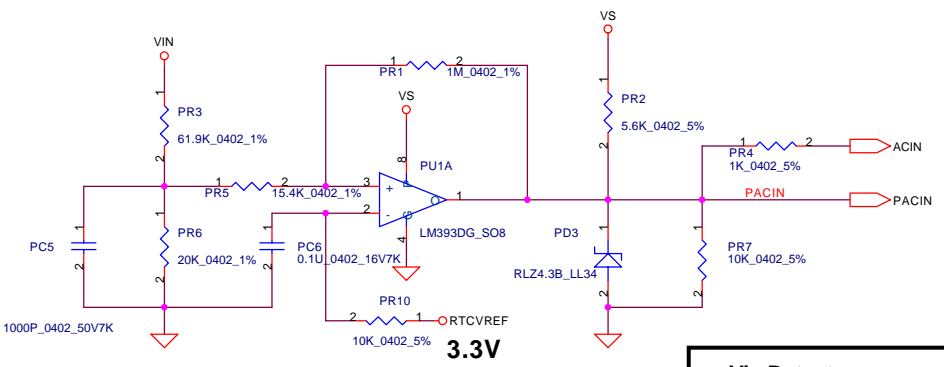
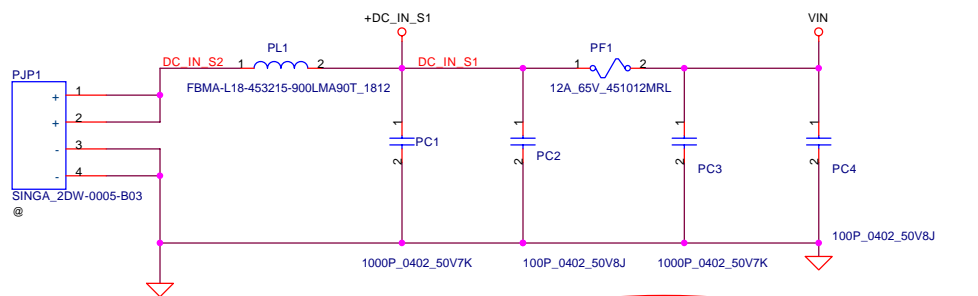
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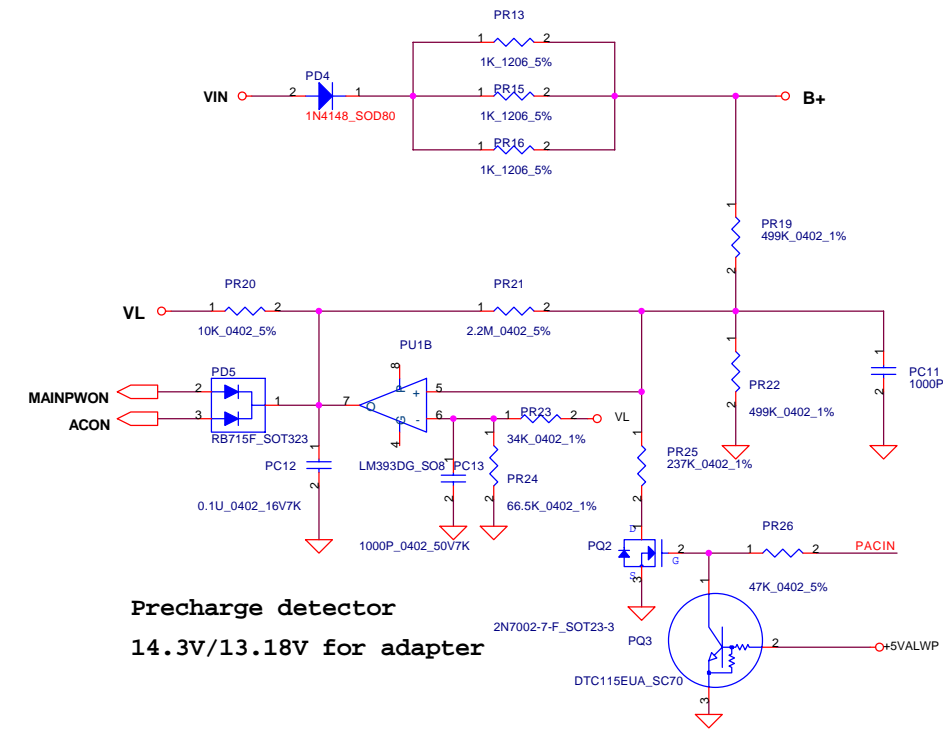
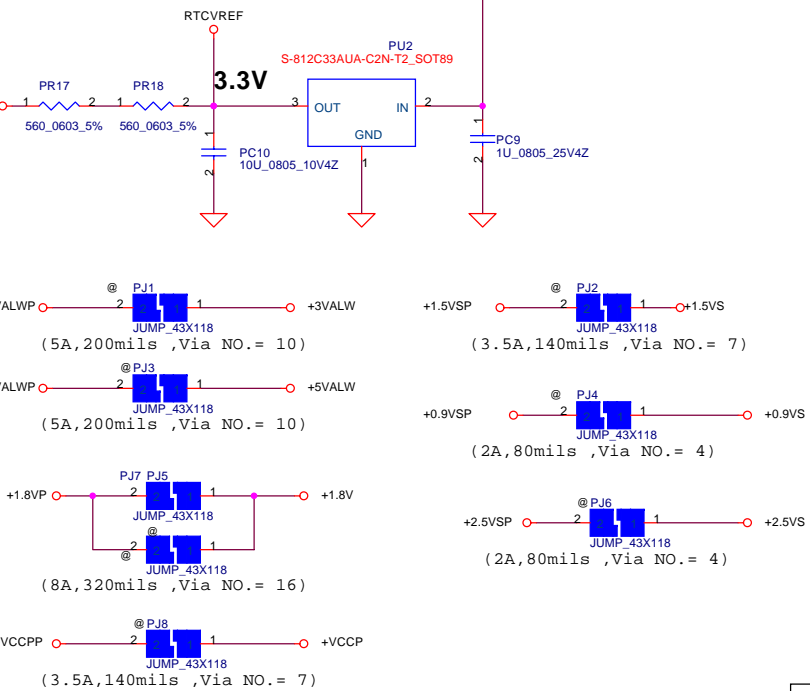
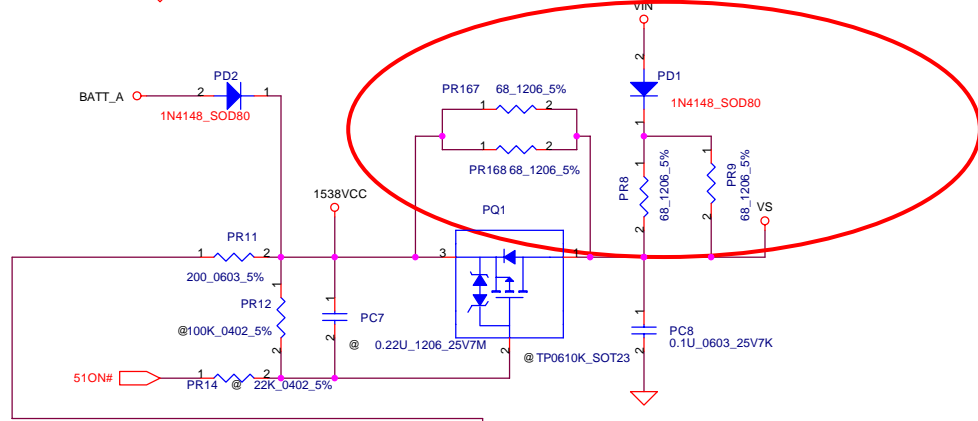
+1.8V to +1.8VS Transfer



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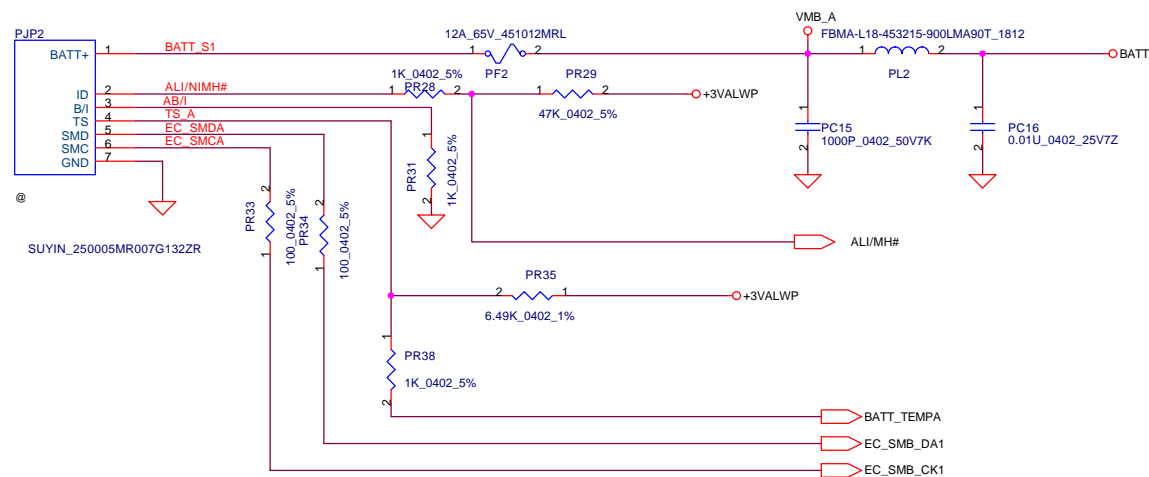


Vin Detector				
High	14.57	14.01	13.46	
Low	14.06	13.51	12.98	

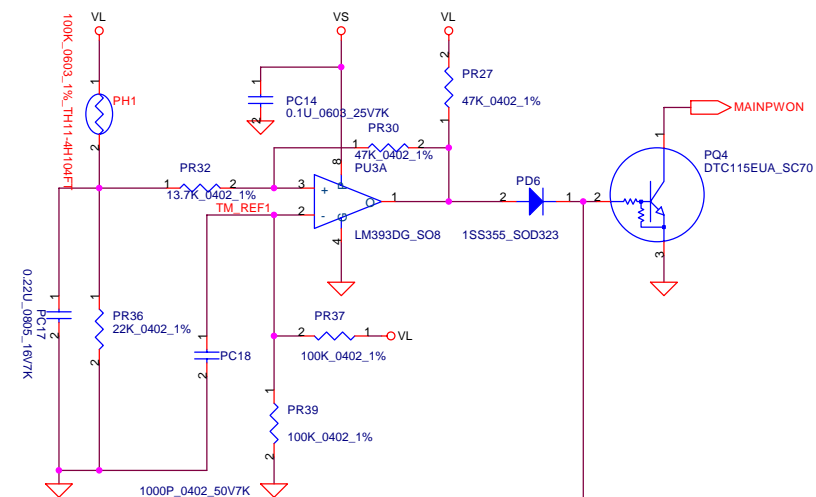


Precharge detector
14.3V/13.18V for adapter

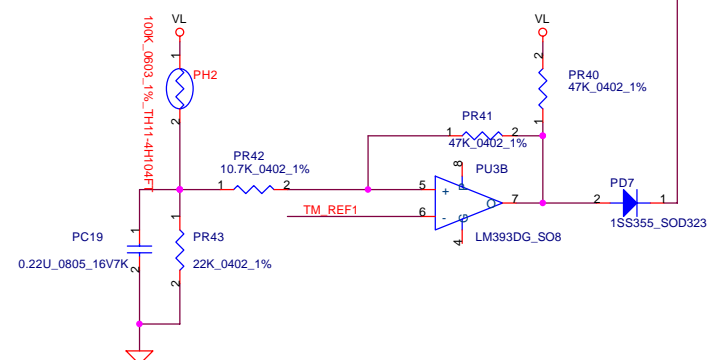
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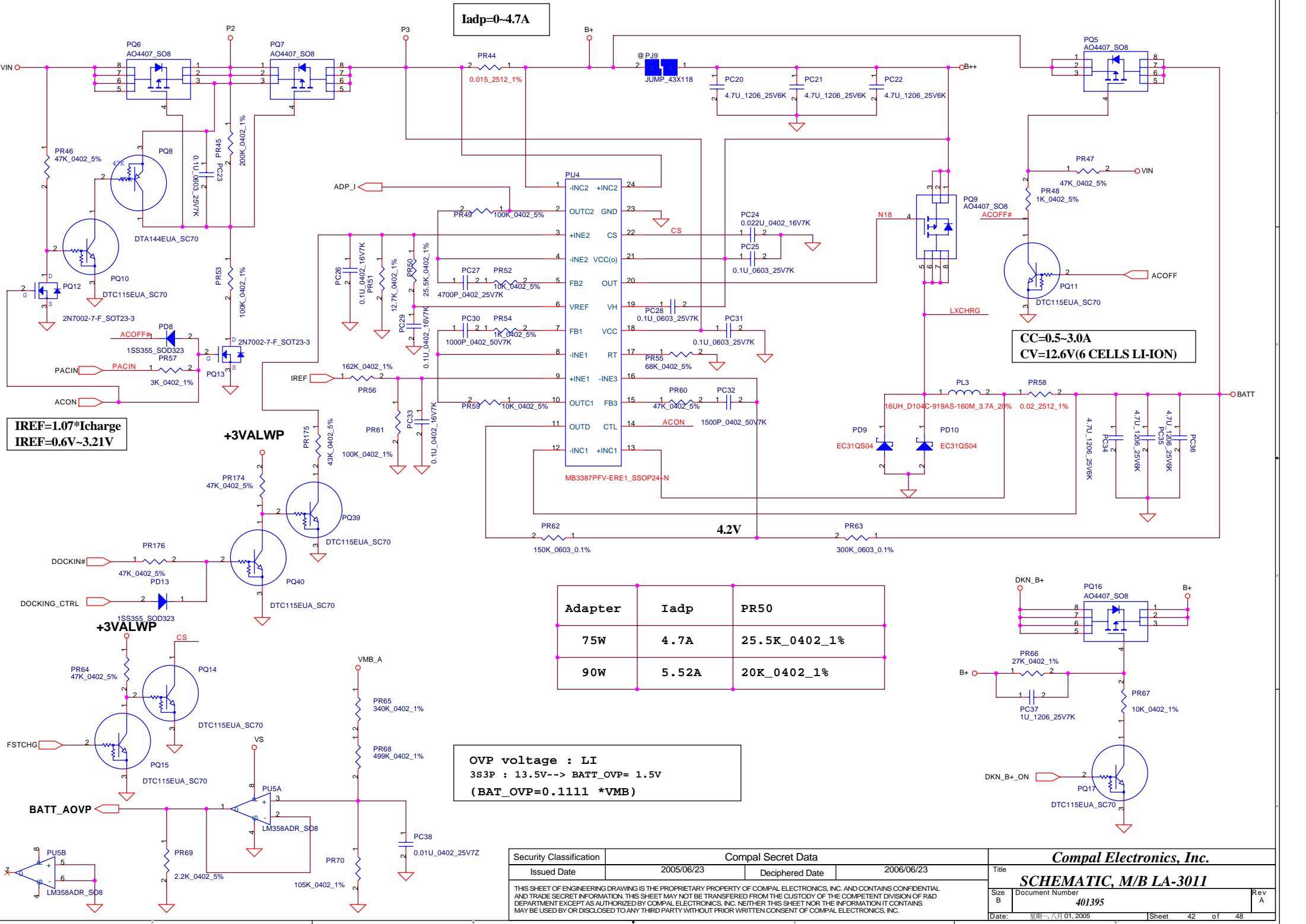
PH1 under CPU botten side :
CPU thermal protection at 84 degree C
Recovery at 45 degree C



PH2 near main Battery CONN :
BAT. thermal protection at 79 degree C
Recovery at 45 degree C



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Iadp=0-4.7A

IREF=1.07*Icharge
IREF=0.6V~3.21V

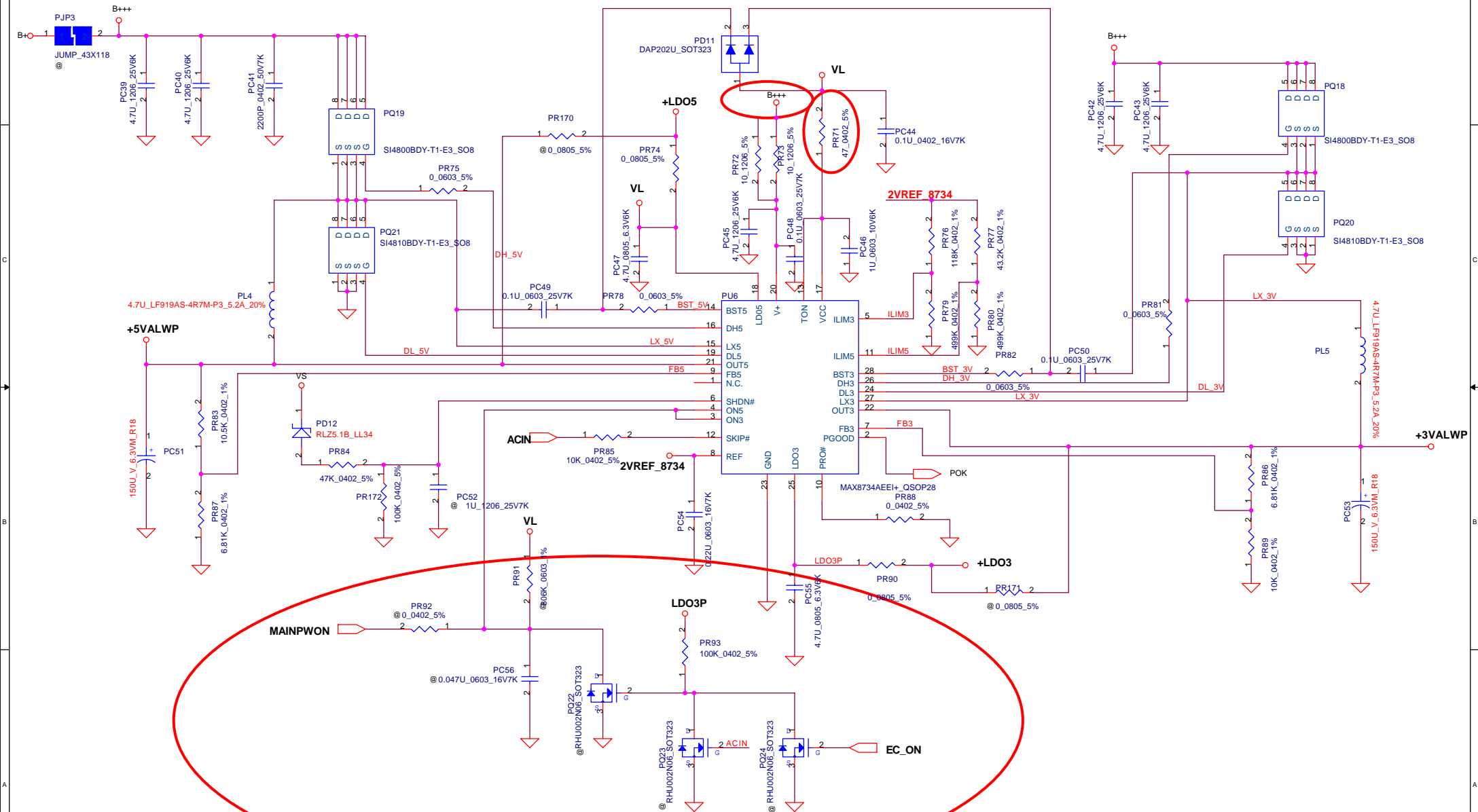
+3VALWP

CC=0.5~3.0A
CV=12.6V(6 CELLS LI-ION)

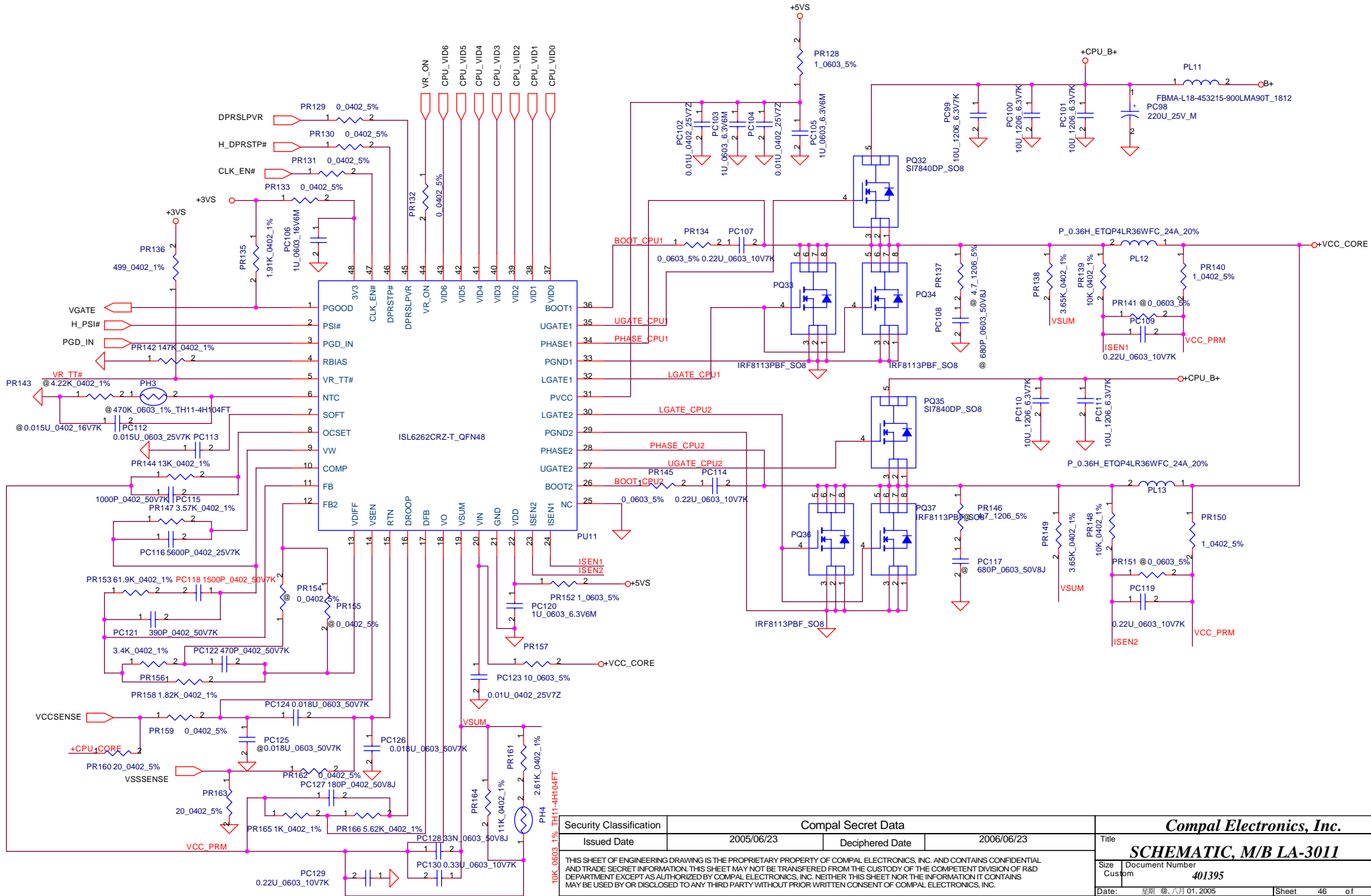
Adapter	Iadp	PR50
75W	4.7A	25.5K_0402_1%
90W	5.52A	20K_0402_1%

OVP voltage : LI
3S3P : 13.5V--> BATT_OVP= 1.5V
(BAT_OVP=0.1111 *VMB)

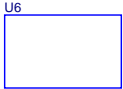
+3.3VALWP/+5VALWP



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