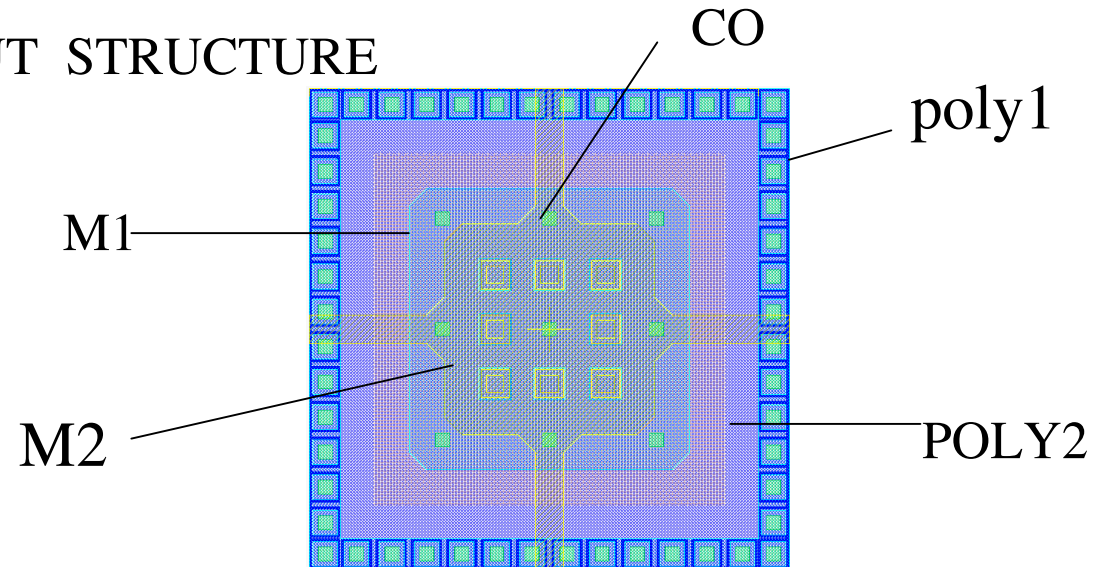


CMOS-CAPACITOR LAYOUT STRUCTURE

1、P1-P2 LAYOUT STRUCTURE



2、MOS LAYOUT STRUCTURE

