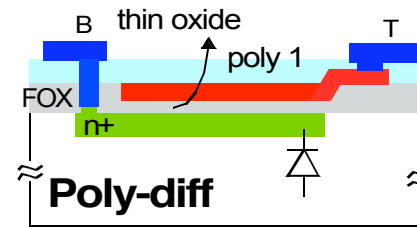
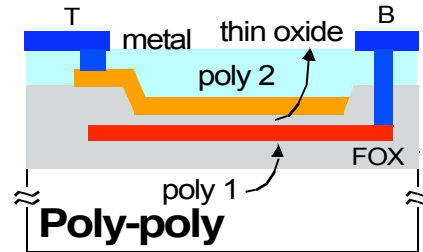
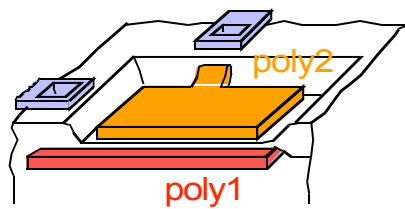
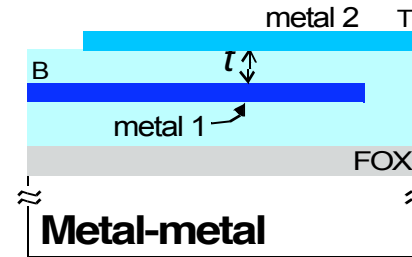
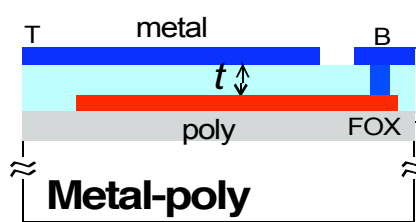
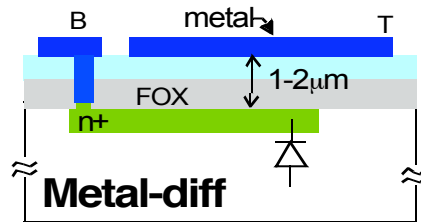


## Thin oxide capacitor ("analog" CMOS)



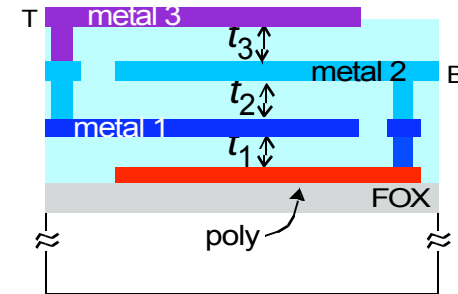
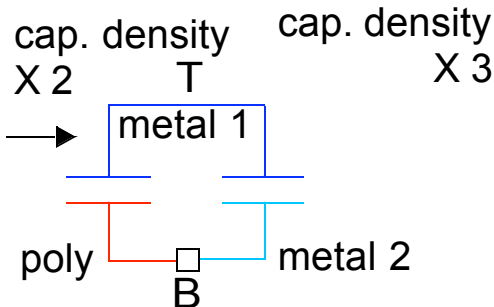
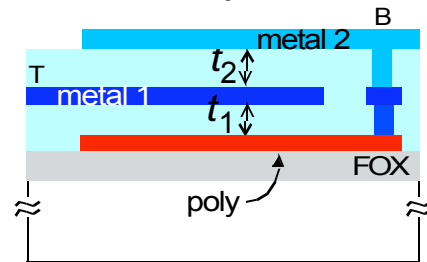
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Linearity	<div><div></div></div>
Temperature	<div><div></div></div>
Parasitics	<div><div></div></div>
Matching	<div><div></div></div>

## Digital CMOS compatible



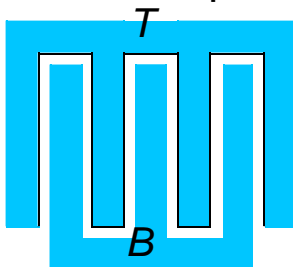
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Linearity	<div><div></div></div>
Temperature	<div><div></div></div>
Parasitics	<div><div></div></div>
Matching	<div><div></div></div>

### Multi-layer



- 😊 Polishing after dielectric deposition improves matching
- 😞 Lack of models

### Lateral capacitors



- 😊 Large density (combination with multi-layer)
- 😞 Lack of models (value? variations? ...)

**Best choice!**

**M-i-M**

Density	<div><div></div></div>
Linearity	<div><div></div></div>
Temperature	<div><div></div></div>
Parasitics	<div><div></div></div>
Matching	<div><div></div></div>