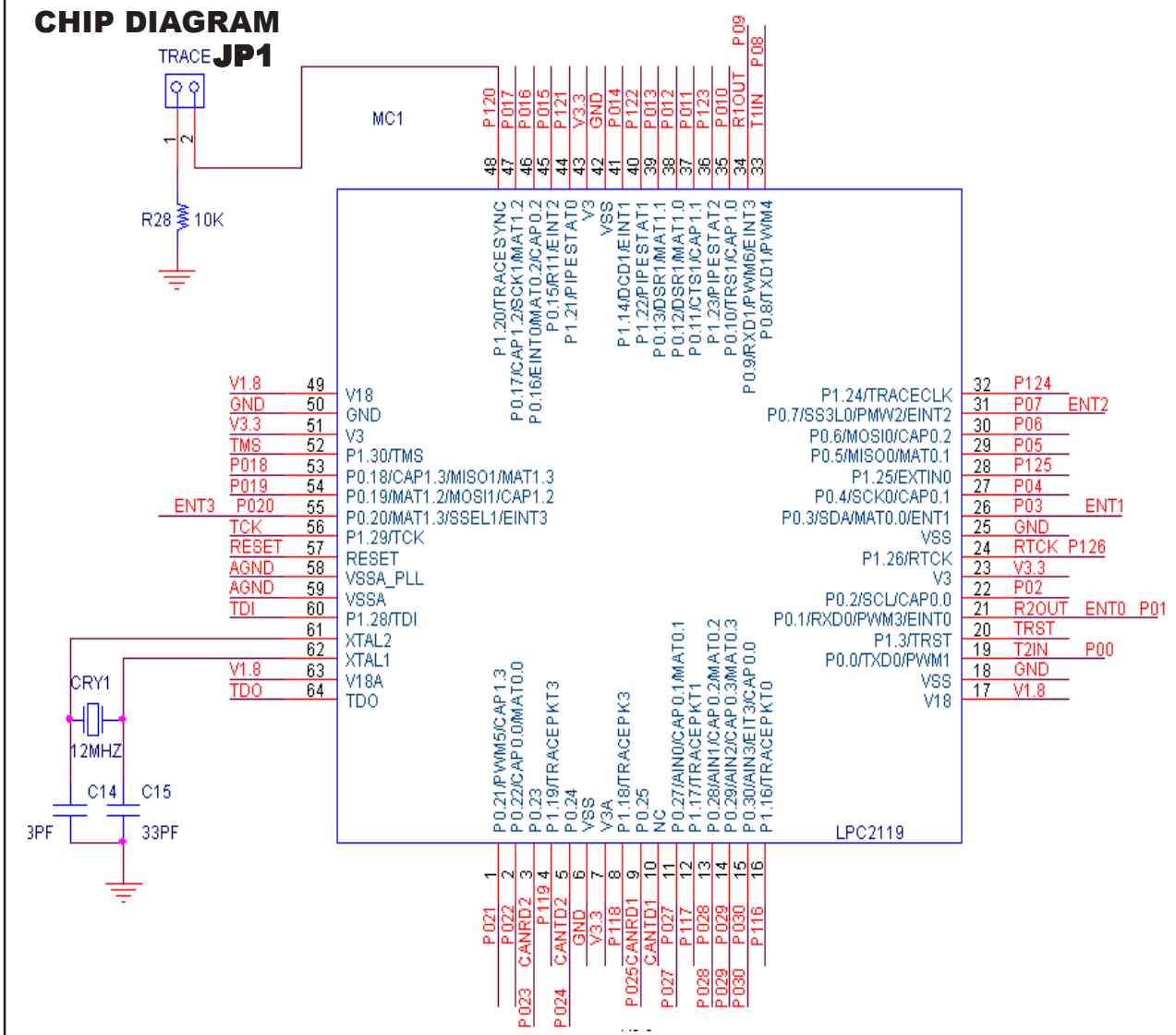
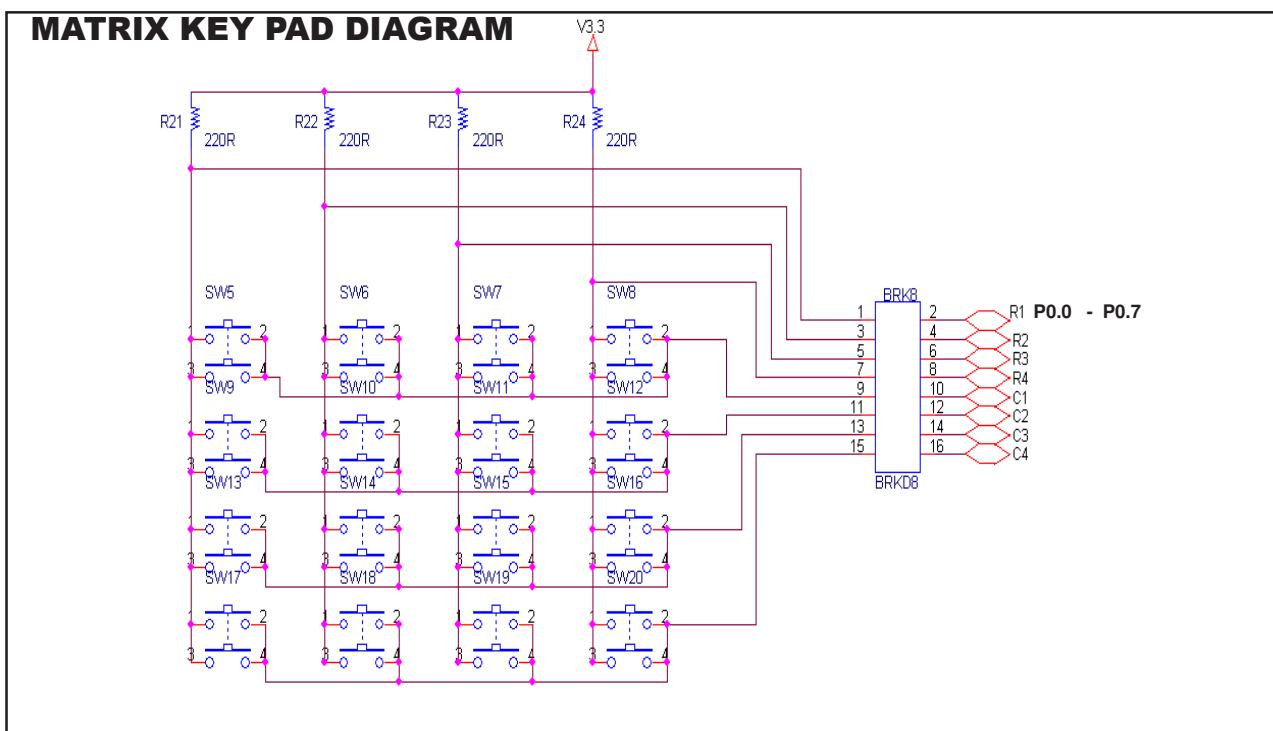


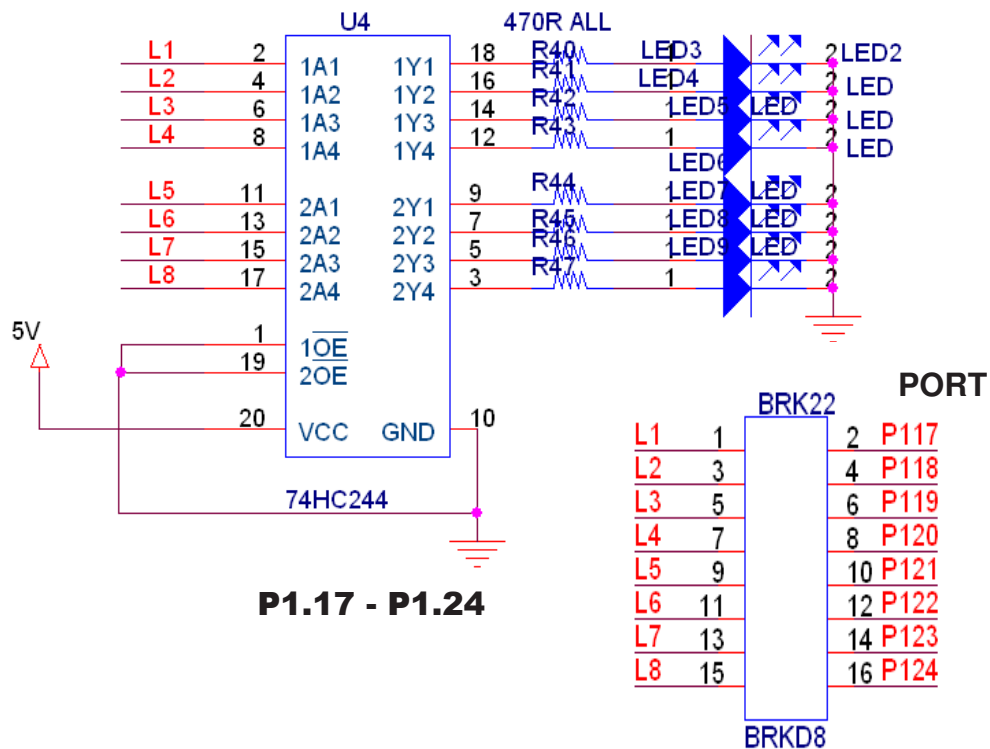
CHIP DIAGRAM



MATRIX KEY PAD DIAGRAM

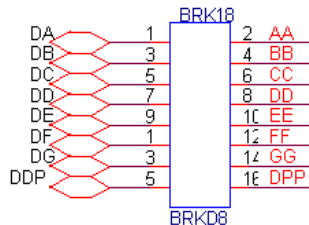


8 LED ARRAY DIAGRAM

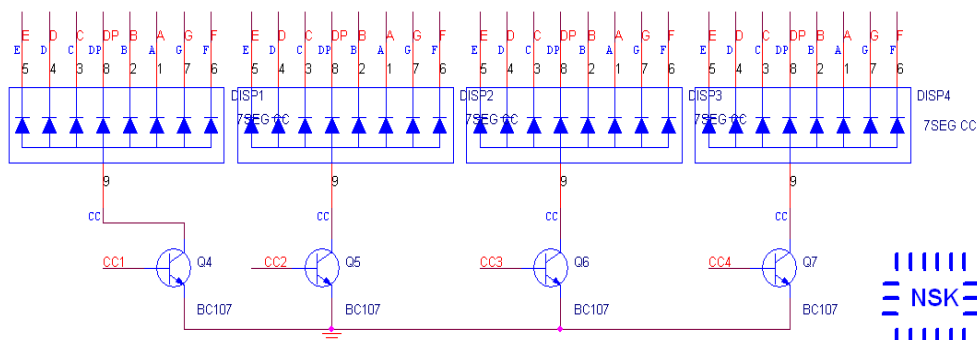
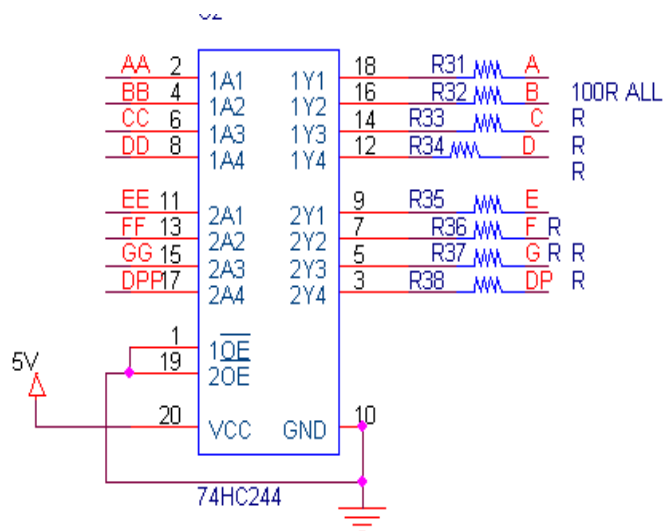
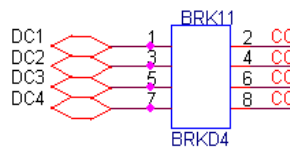


7 SEG DISPLAY DIAGRAM

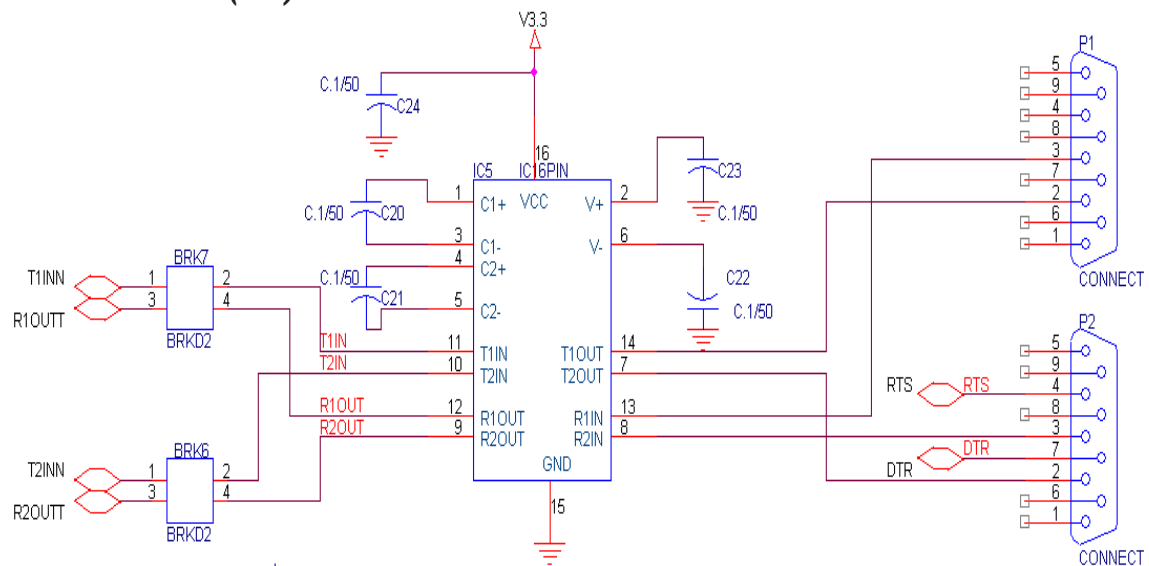
PORT - P0.8 - P0.15



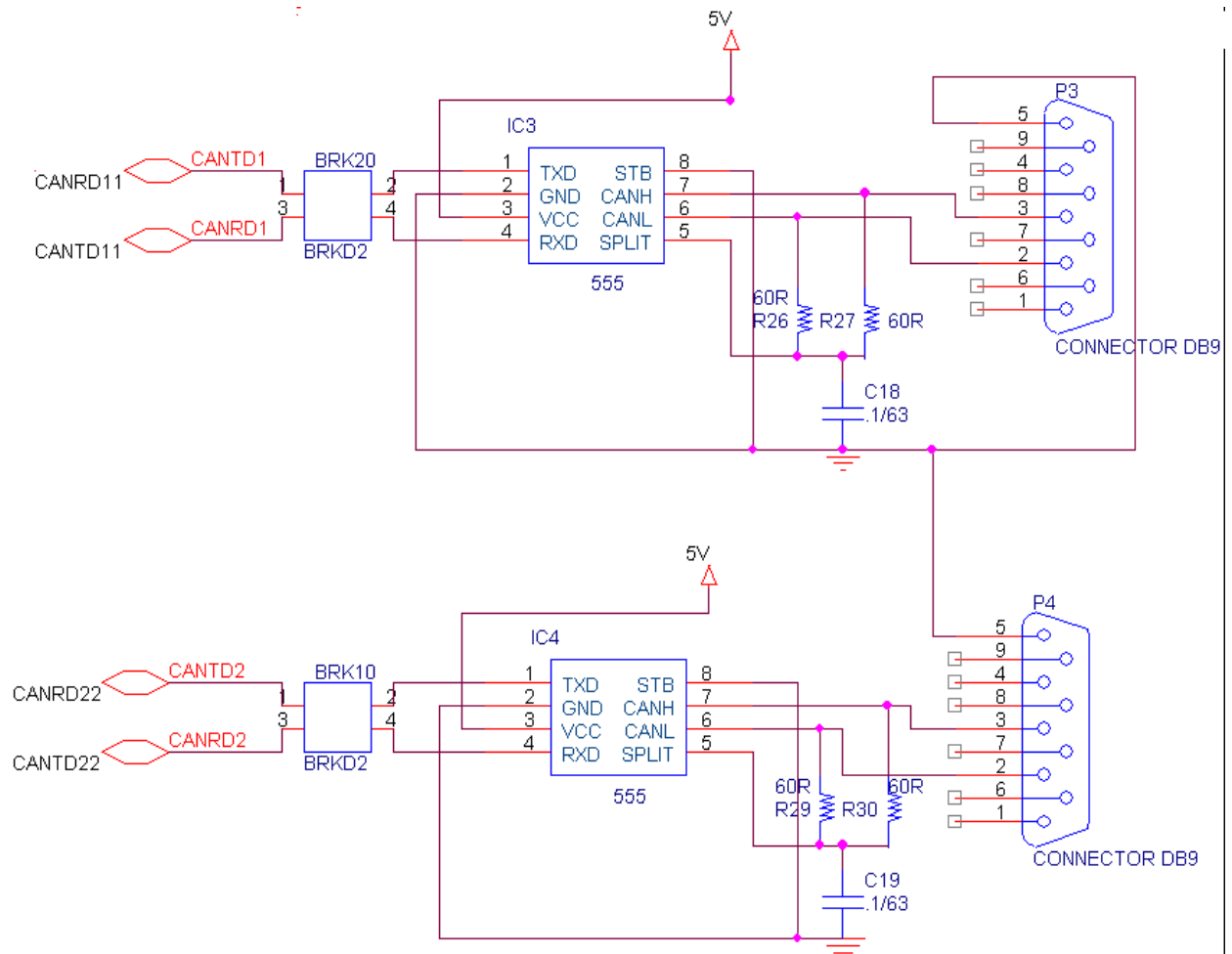
PORT - P0.16 - P0.18



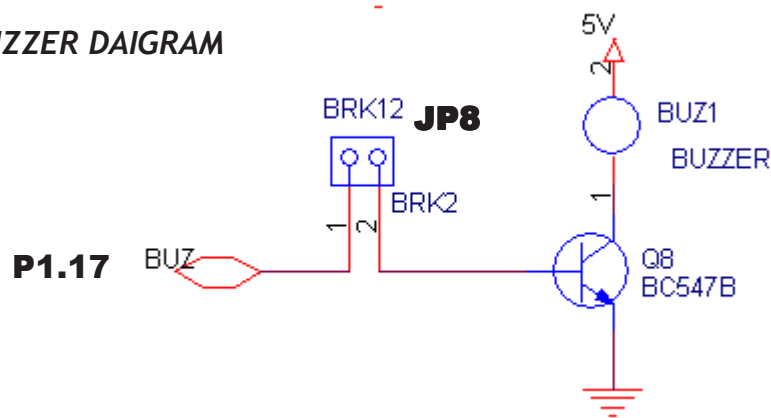
UART1/UART2(ISP)



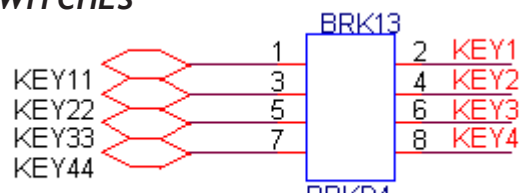
CAN1&CAN2 DETAILS



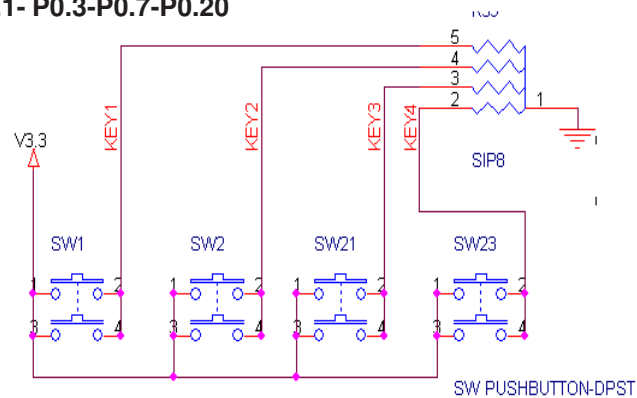
BUZZER DAIGRAM



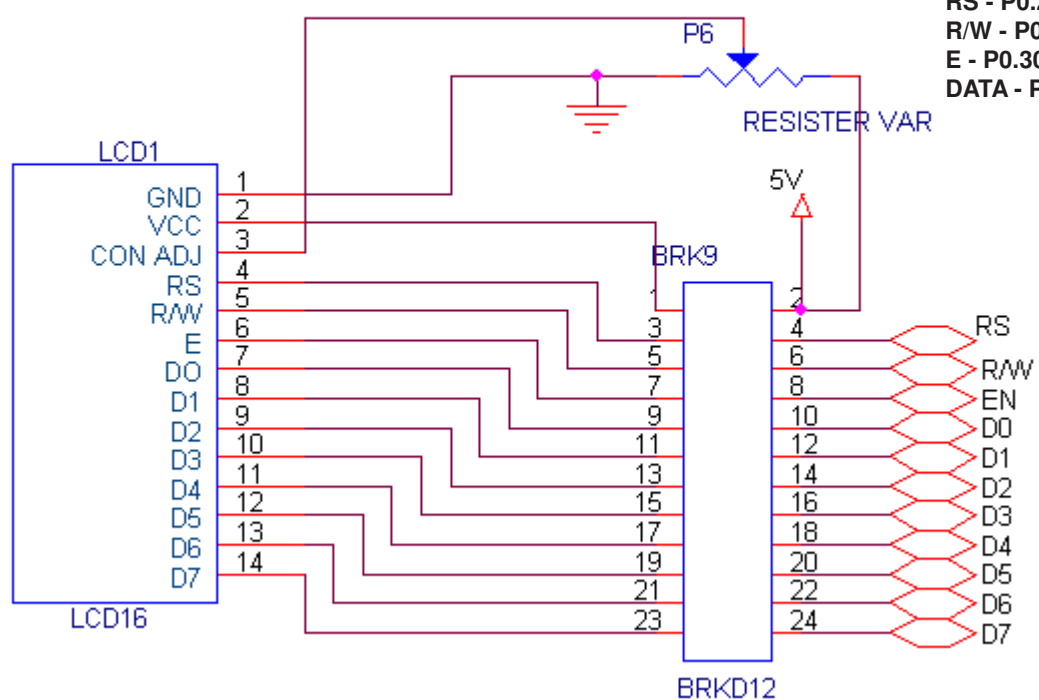
4 INTRUPPT SWITCHES



PORT DETAILS - P0.1- P0.3-P0.7-P0.20



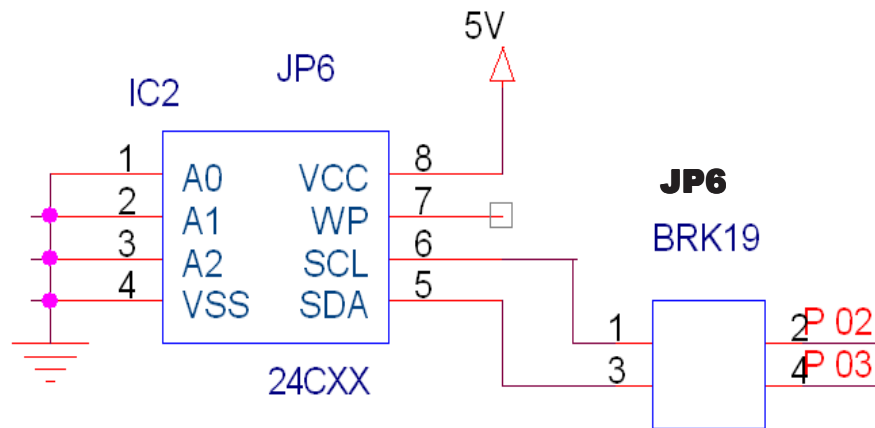
LCD DISPLAY



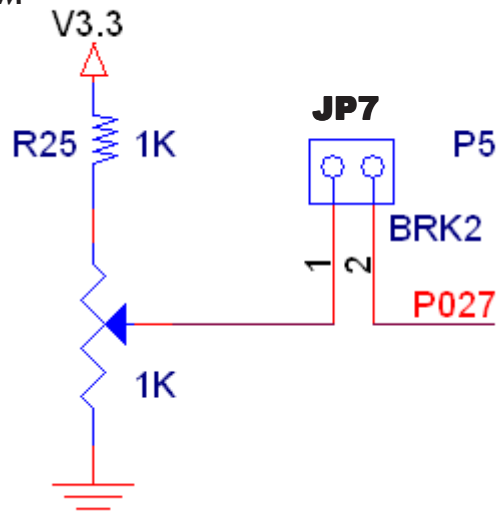
PORT DETAILS

RS - P0.28
R/W - P0.29
E - P0.30
DATA - P1.16 - 1.23

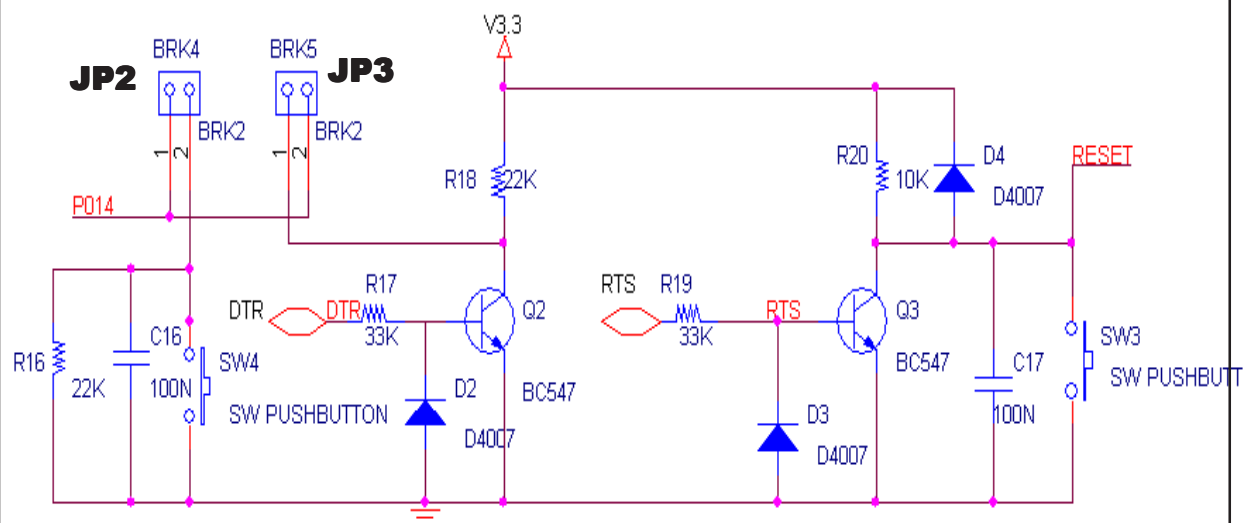
I2C 24C DIAGRAM



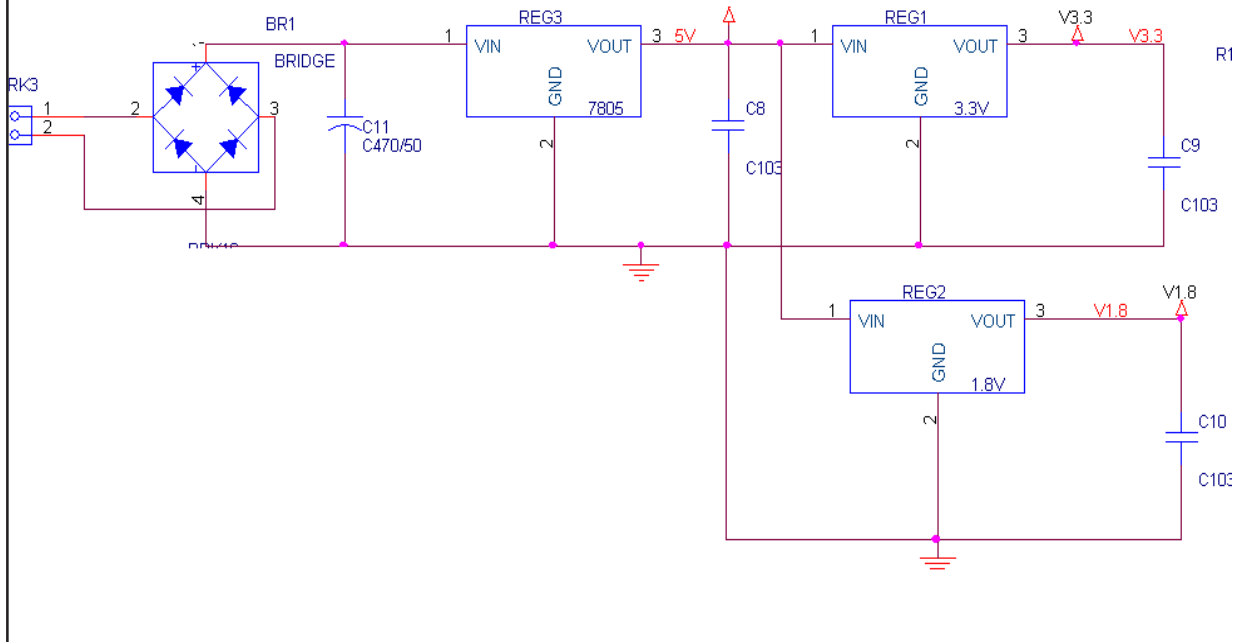
ADC CH0 DIAGRAM



AUTO ISP DIAGRAM

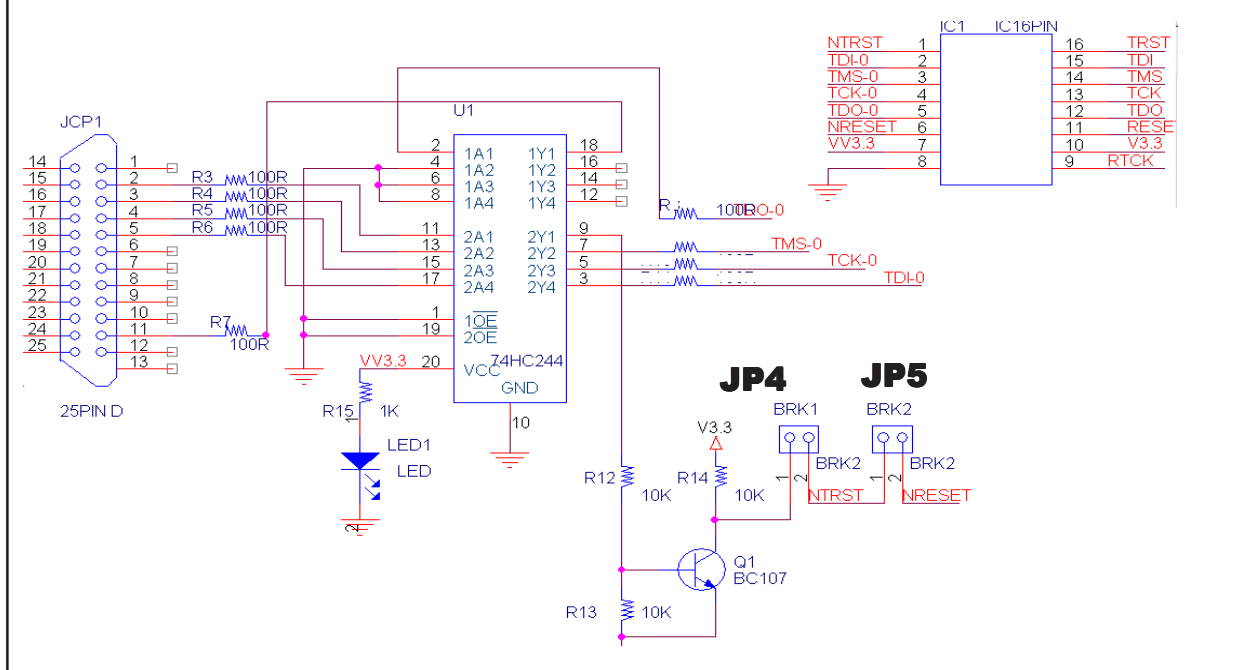


POWER SUPPLY



JTAG SECTION

JTAG SWITCH



BURG CONNECTORS OUTPUT

