

Freq (GHz):
 20 (GHz)

50
☒ Normalize

Define Source/Load Network Terminations...

768.55664 pH

☒ Lock Source Impedance
 ☐ Lock Load Impedance

Gamma: <
 Z: +j

VSWR:
 Y: +j

Current Schematic: 2nd_transistor_Sparameter [PA_1stage_lib/2

SmartComponent: DA_SmithChartMatch1

Network Response

Max: 0
 Type: dB
 Trace1: S22
 Trace2: S11
 Min: -50

Start Freq: 4e9 Stop Freq: 6.0e9

Network Schematic

Z0:
 Loss: ☐
 Q:

Fig. 2 Source input impedance matching

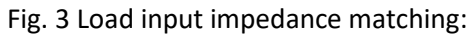


Fig. 3 Load input impedance matching:

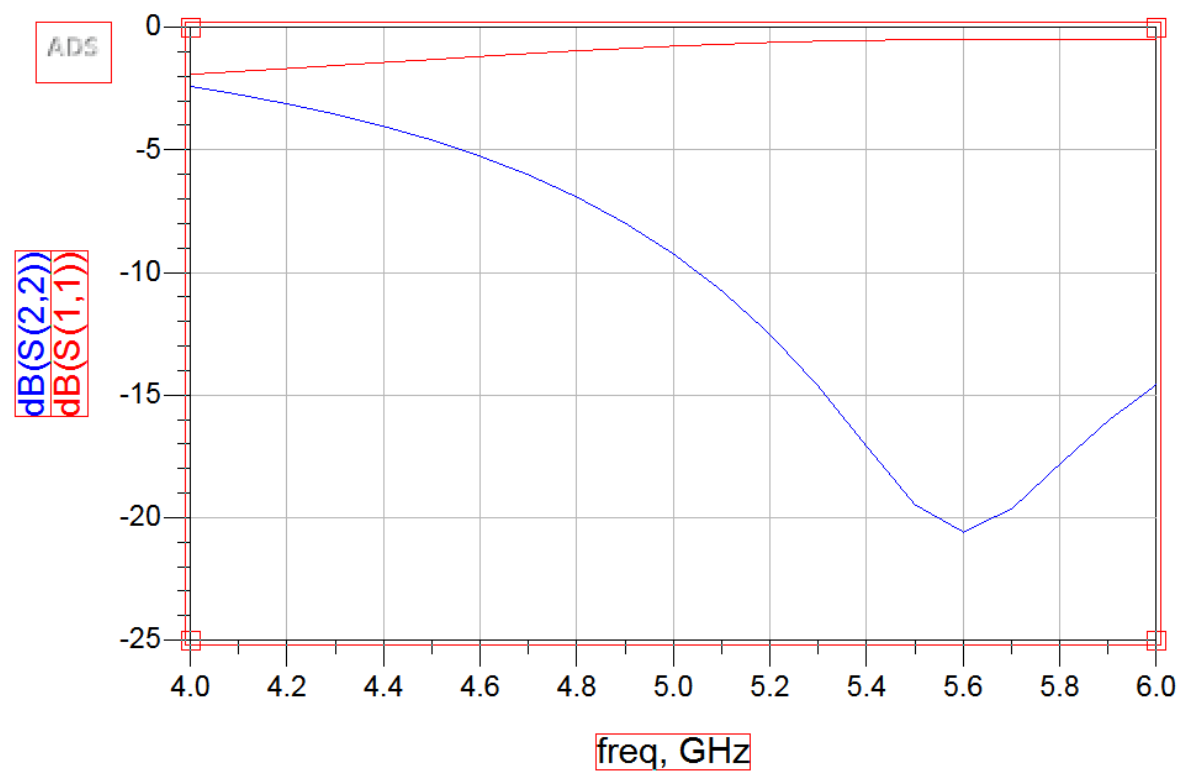


Fig. 4 S11 and S22 simulation from Fig. 1