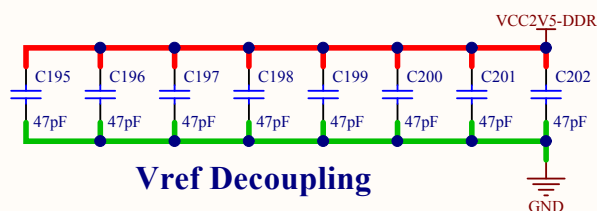
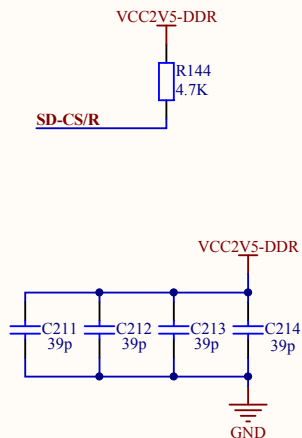
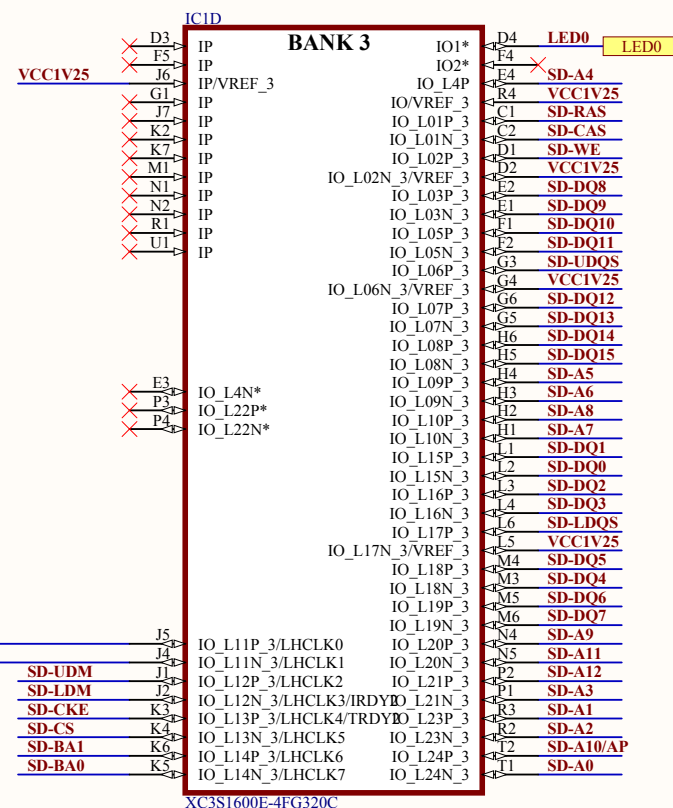


VCC Bank3 = 2.5V



Sheet: DDR SDRAM

Author: Le Tri Trong

Company: Envi-tech Viet Nam

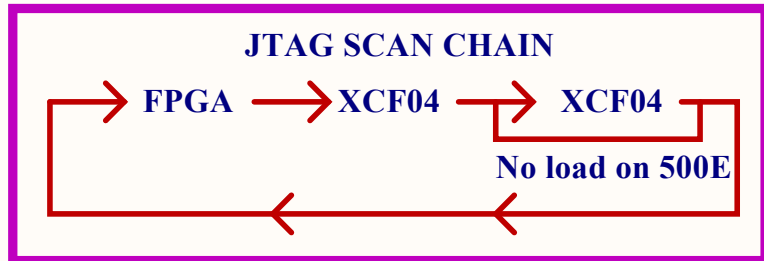
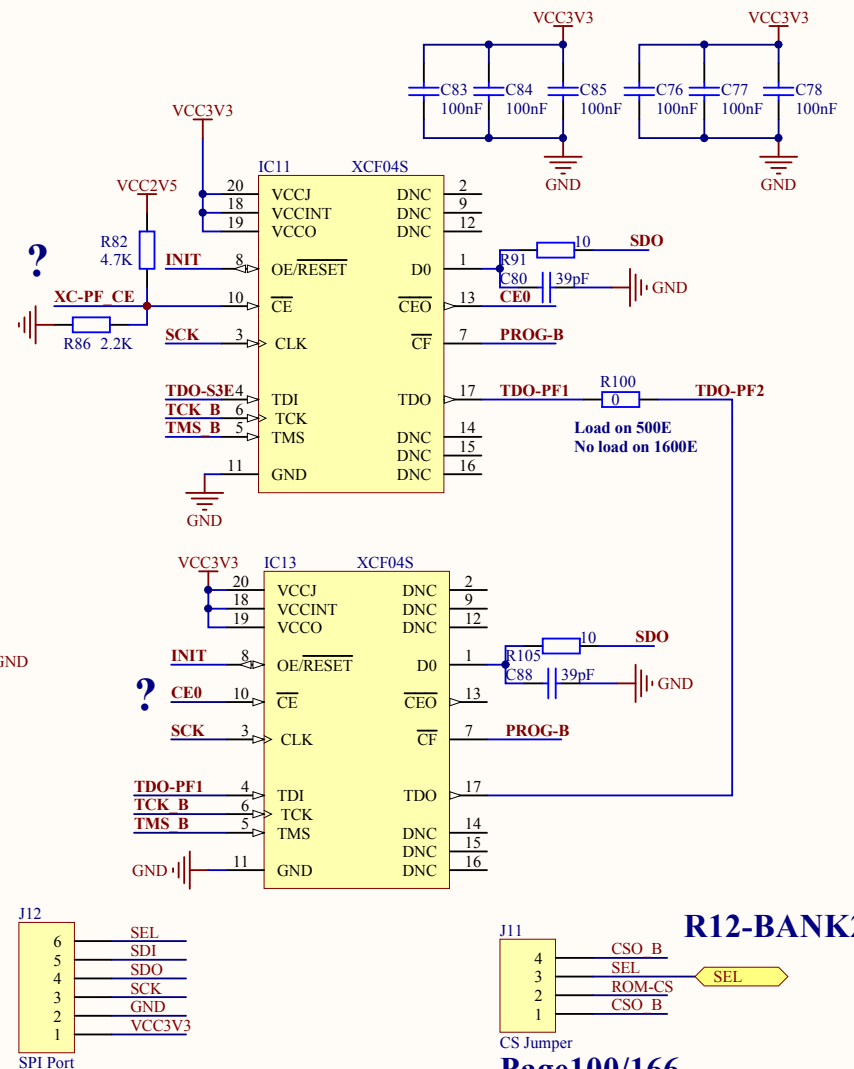
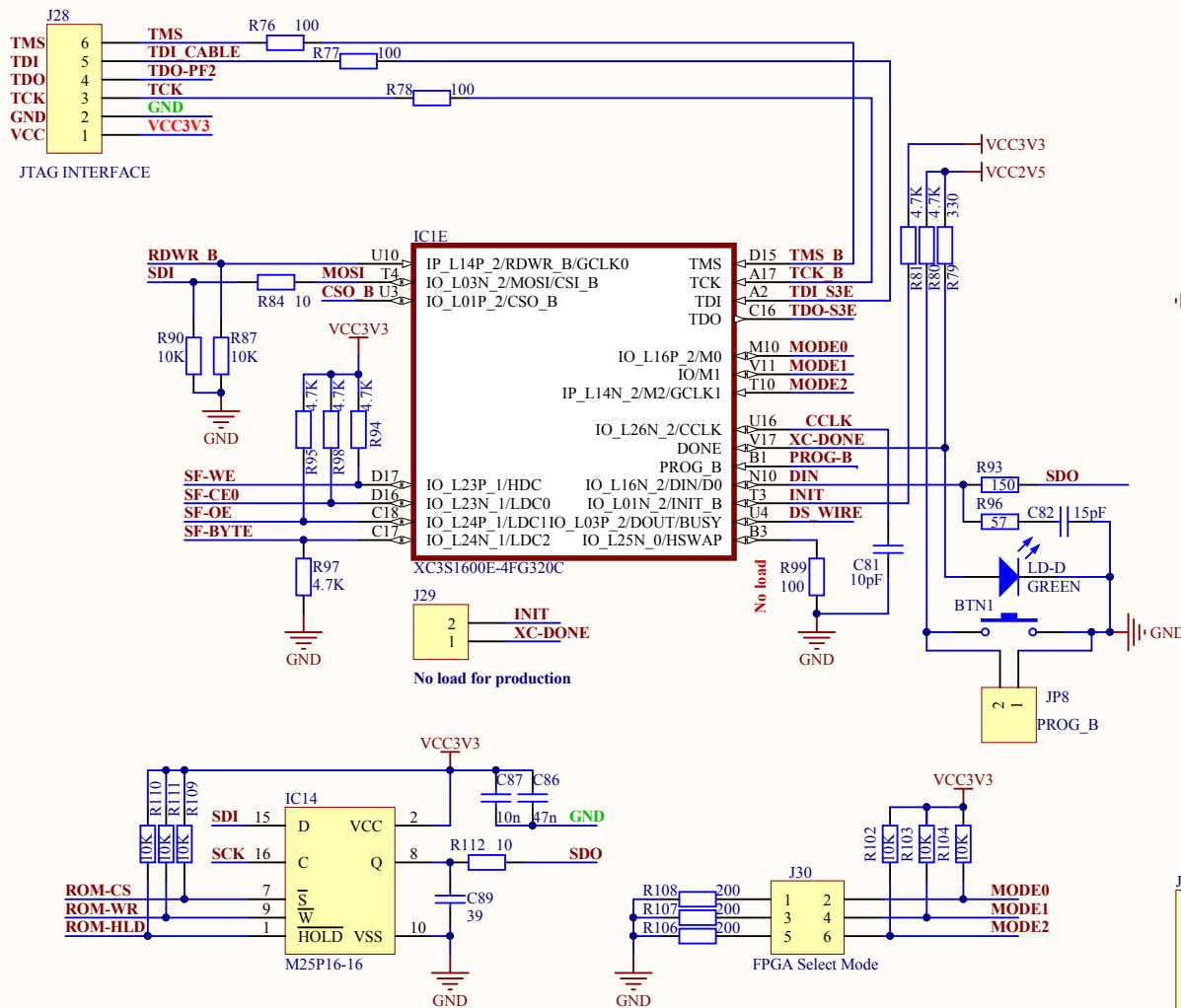
Address: Minh Khai - Tu Liem - HN
0983.943.357

SIGNAL MONITORING

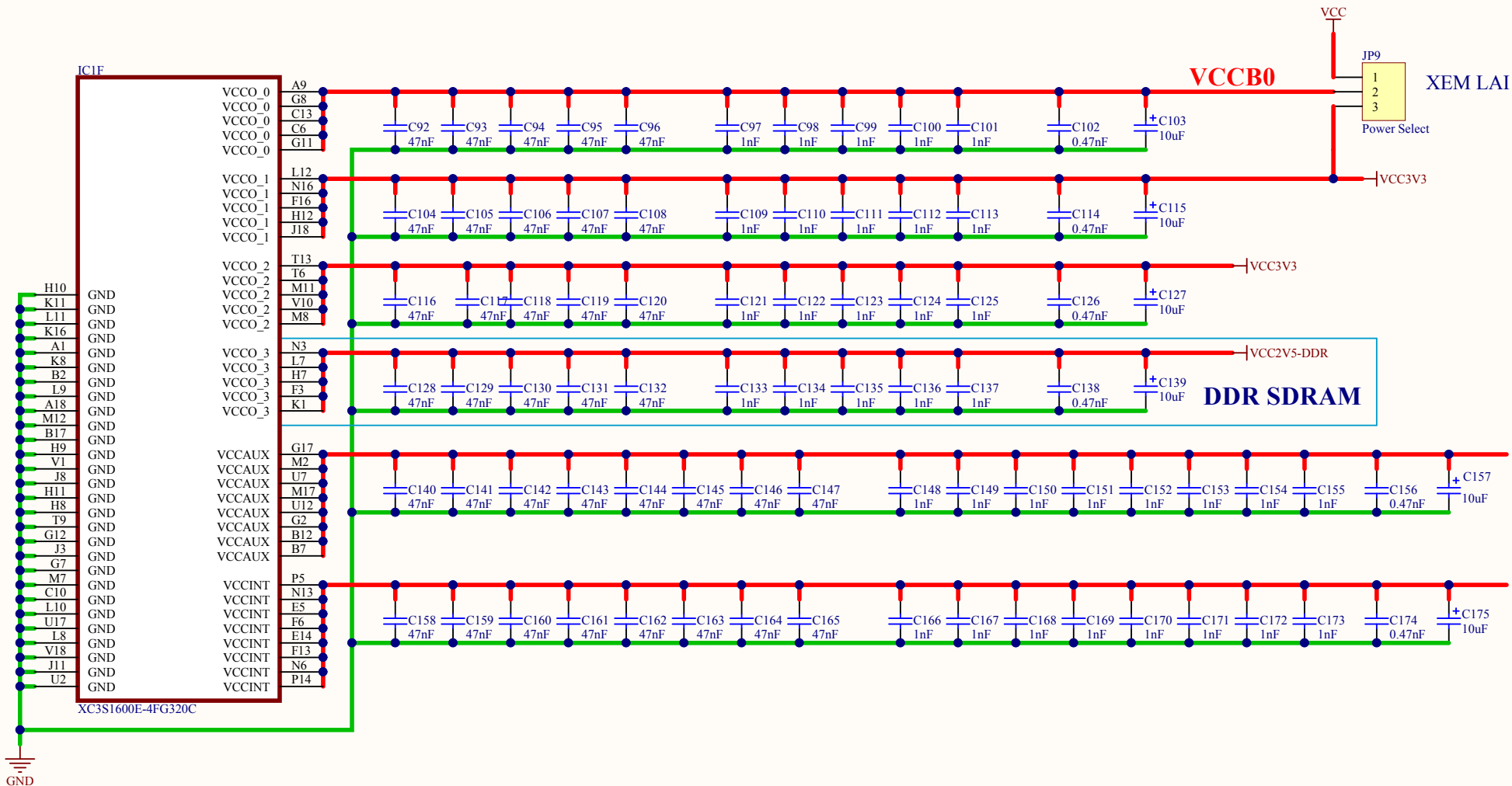
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Sheet: 2 28/05/2012

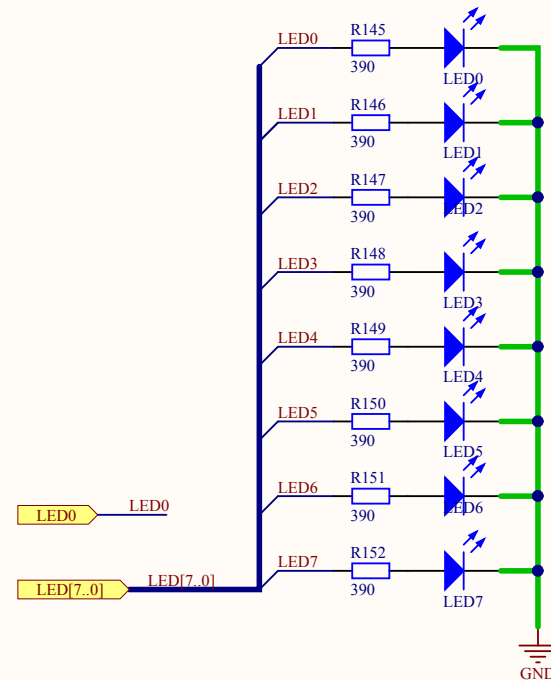
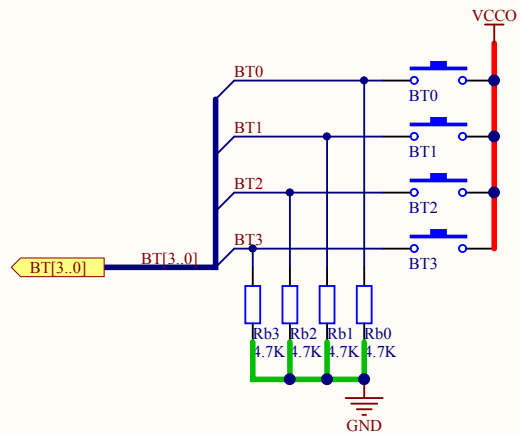
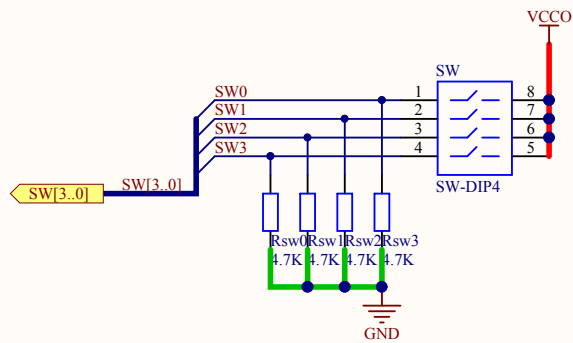
HYKNIGHT



Sheet: XC3SE Configuration and JTAG			SIGNAL MONITORING		
Author:	Le Tri Trong				
Company:	Envi-tech Viet Nam		Size: A4	08:00:00	HYKNIGHT
Address:	Minh Khai - Tu Liem - HN 0983.943.357		Sheet: 3	28/05/2012	



Sheet: XC3SE Power decoupling			SIGNAL MONITORING		
Author:	Le Tri Trong				
Company:	Envi-tech Viet Nam		Size: A4	08:00:00	HYKNIGHT
Address:	Minh Khai - Tu Liem - HN 0983.943.357		Sheet: 4	28/05/2012	



Sheet: Peripheral			SIGNAL MONITORING		
Author:	Le Tri Trong				
Company:	Envi-tech Viet Nam		Size: A4	08:00:00	HYKNIGHT
Address:	Minh Khai - Tu Liem - HN 0983.943.357		Sheet: 5	28/05/2012	

1	2	3	4
A	<div>DDRAM MS_DDRAM.SchDoc</div>	<div>Peripheral MS_Peripheral.SchDoc</div>	A
B		<div>FPGA MS_FPGA.SchDoc</div>	B
C	<div>CONFIG MS_CONFIG.SchDoc</div>	<div>PWDecoupling MS_S3EDECOUPLING.SchDoc</div>	C
D		<div>Sheet: *</div> <div>Author: Le Tri Trong</div> <div>Company: Envi-tech Viet Nam Address: Minh Khai - Tu Liem - HN 0983.943.357</div>	<div>*</div> <div>Size: A4 * Sheet: * *</div> <div>HYKNIGHT</div>
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