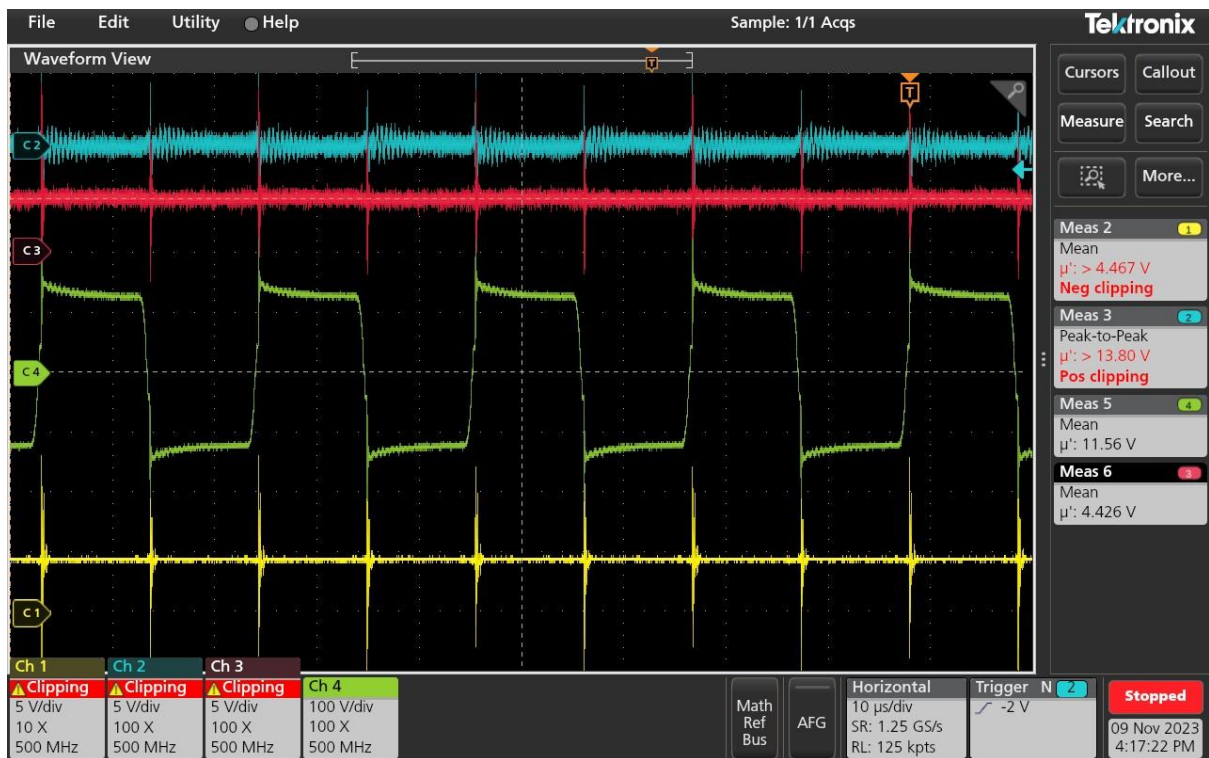


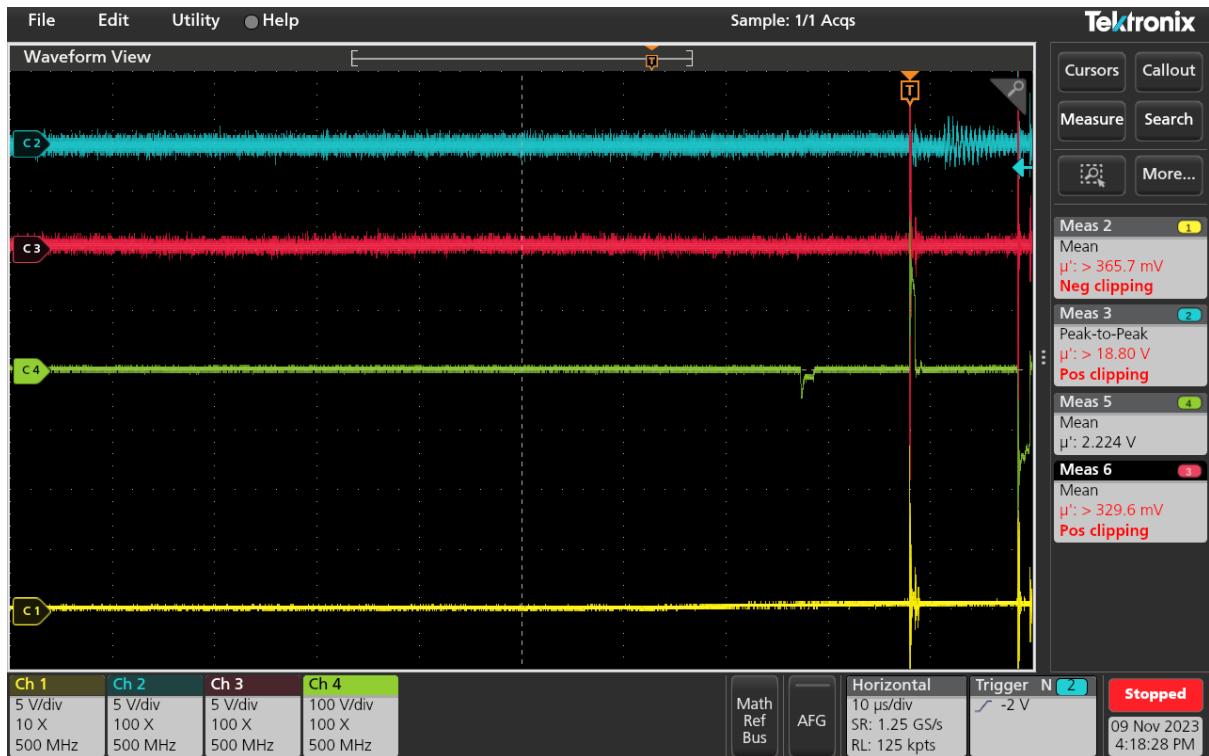


Ch 1=COMP, Ch2=CS, Ch3=SS Pin, Ch4=EA- (all pins correspond to 28950 pins)

(Input Voltage =152V, Output=23V), Load Resistance=40hms



(Ch1=COMP, Ch2=CS, Ch3=SS, Ch4=Transformer Primary)



(Ch1=COMP, Ch2=CS, Ch3=SS, Ch4=Transformer Primary)

When Output voltage reaches 24V, it goes to burst mode.



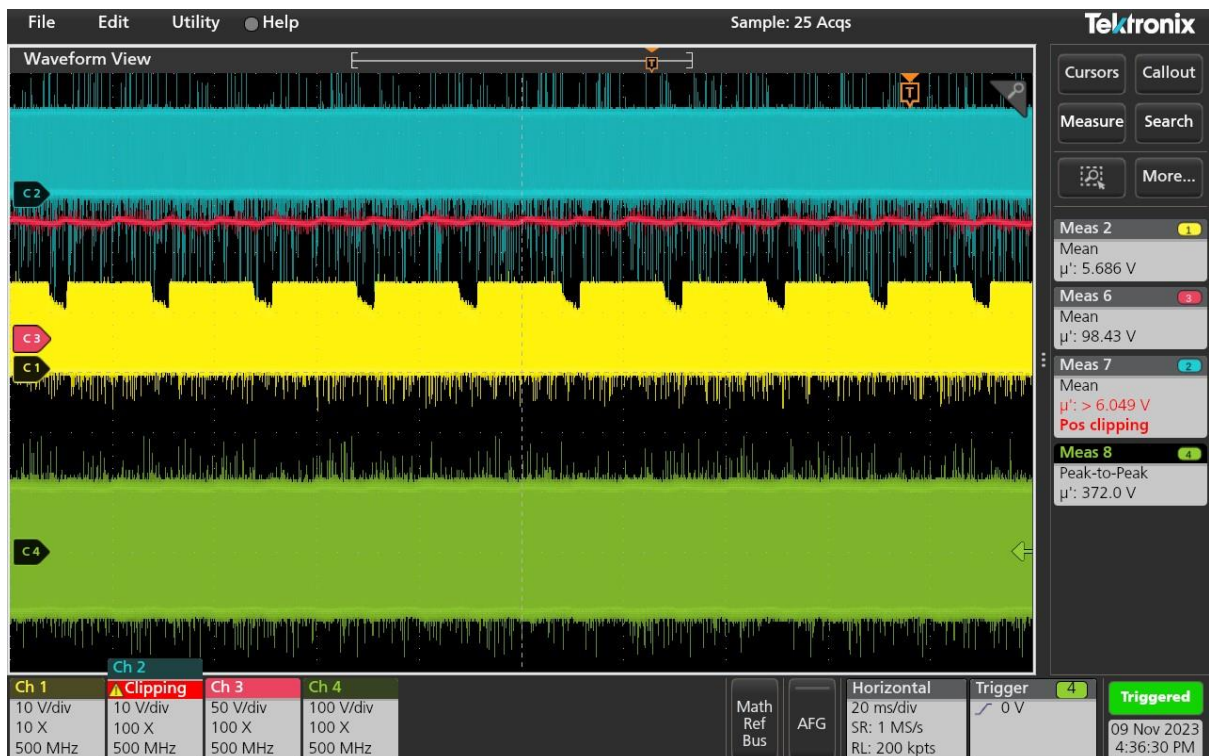
Ch1=Input Current, Ch2=Output Voltage, Ch3=Input Voltage, Ch4=Transformer Primary

Before Entering Reset mode

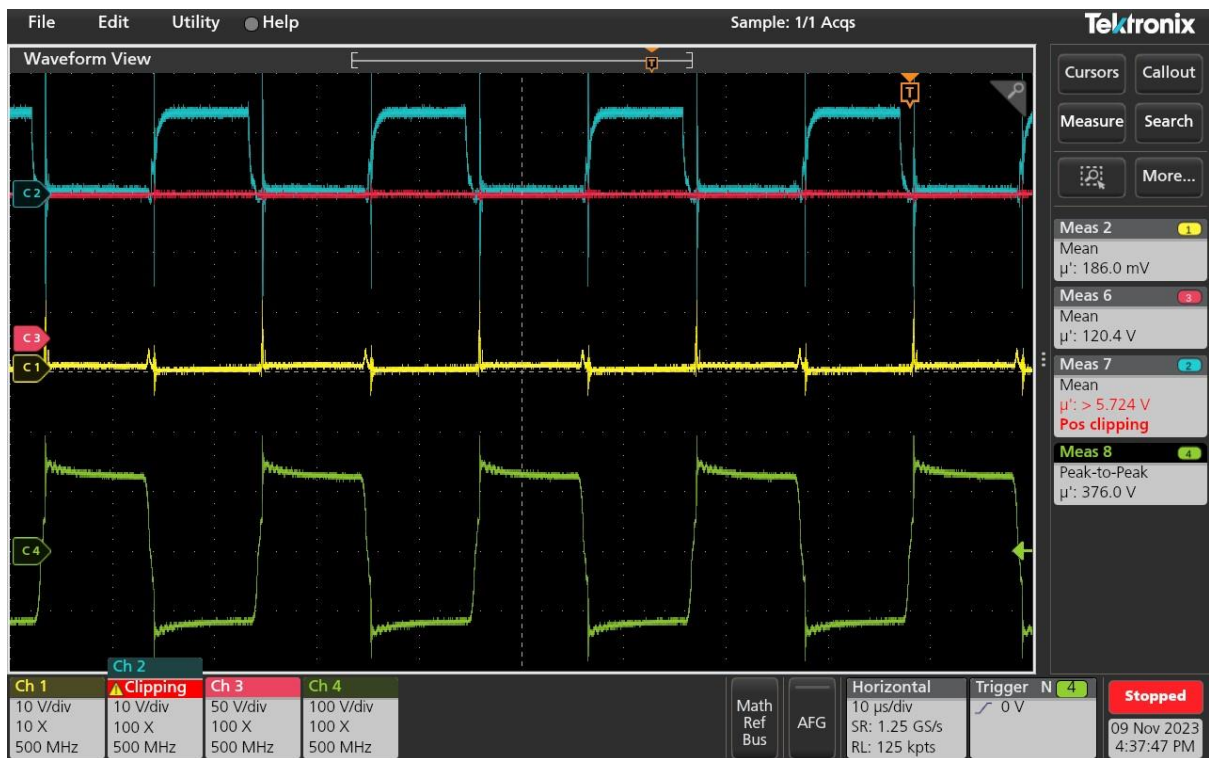


Ch1=GATE_QA, Ch2=GATE_QC, Ch3=Vout, Ch4=Tranformer Primary

The Gate voltage of QA starts dipping as the output voltage reaches near the reference (24V)



Zoomed Out Waveform of above condition.



Ch1=GATE_QA, Ch2=GATE_QC, Ch3=Vout, Ch4=Tranformer Primary

When voltage is just before burst mode



Ch1=GATE_QA, Ch2=GATE_QC, Ch3=Vout, Ch4=Tranformer Primary

In Burst mode